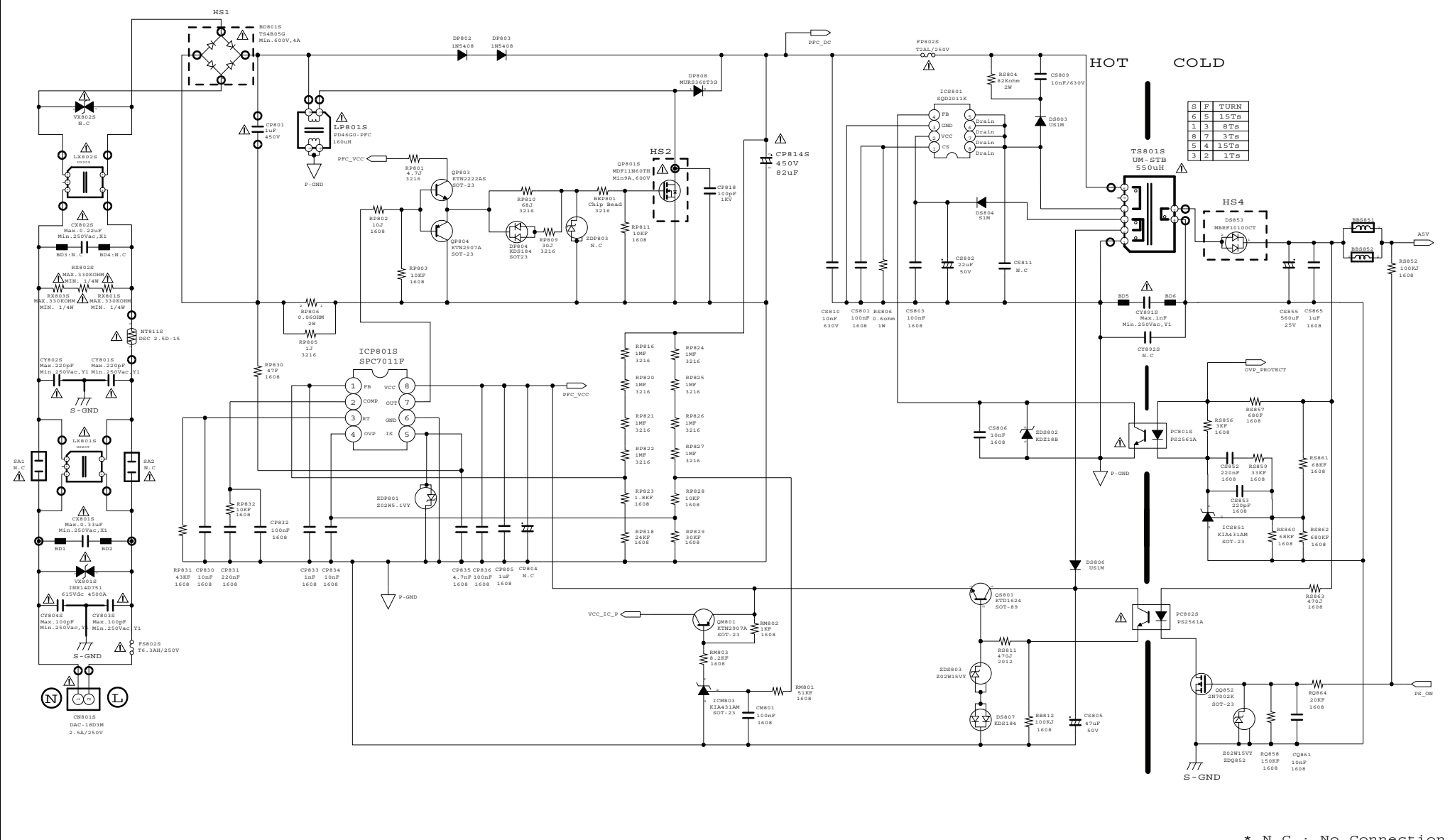
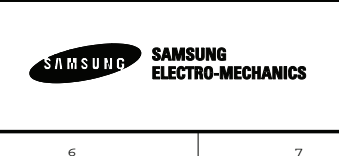
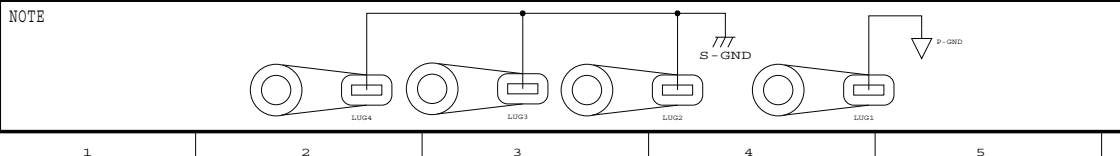


4. CIRCUIT DRAWING

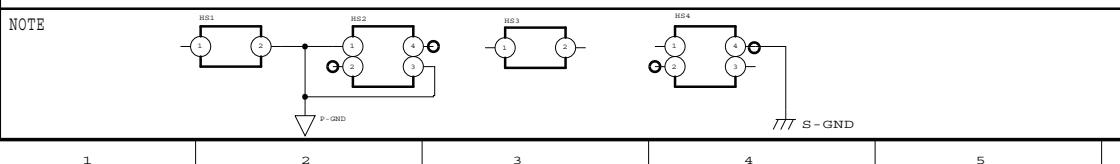
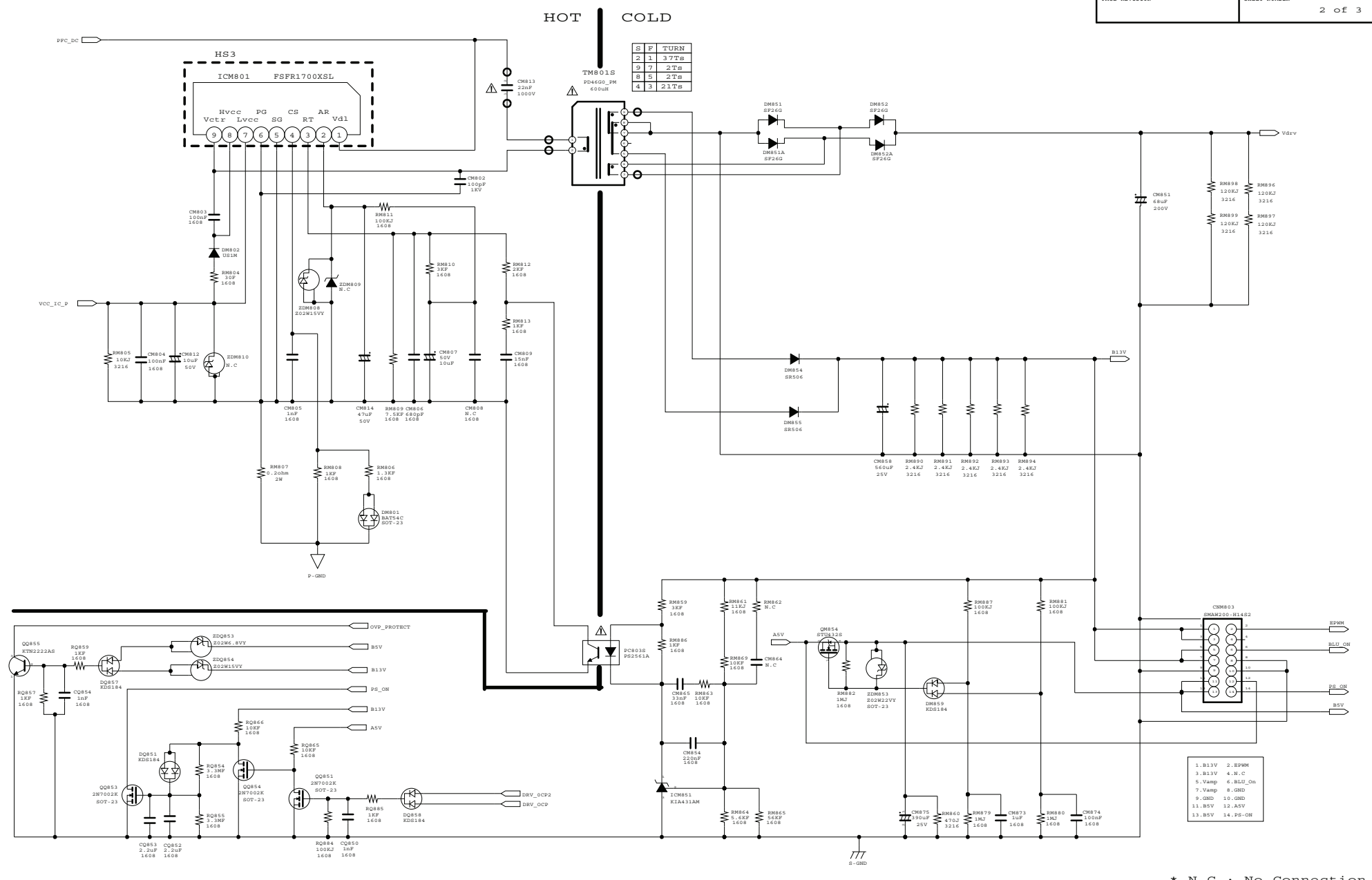


S	F	TURN
1	1	1ST
2	2	2ND
3	3	3RD
4	4	4TH
5	5	5TH
6	6	6TH
7	7	7TH
8	8	8TH
9	9	9TH
10	10	10TH

* N.C : No Connection

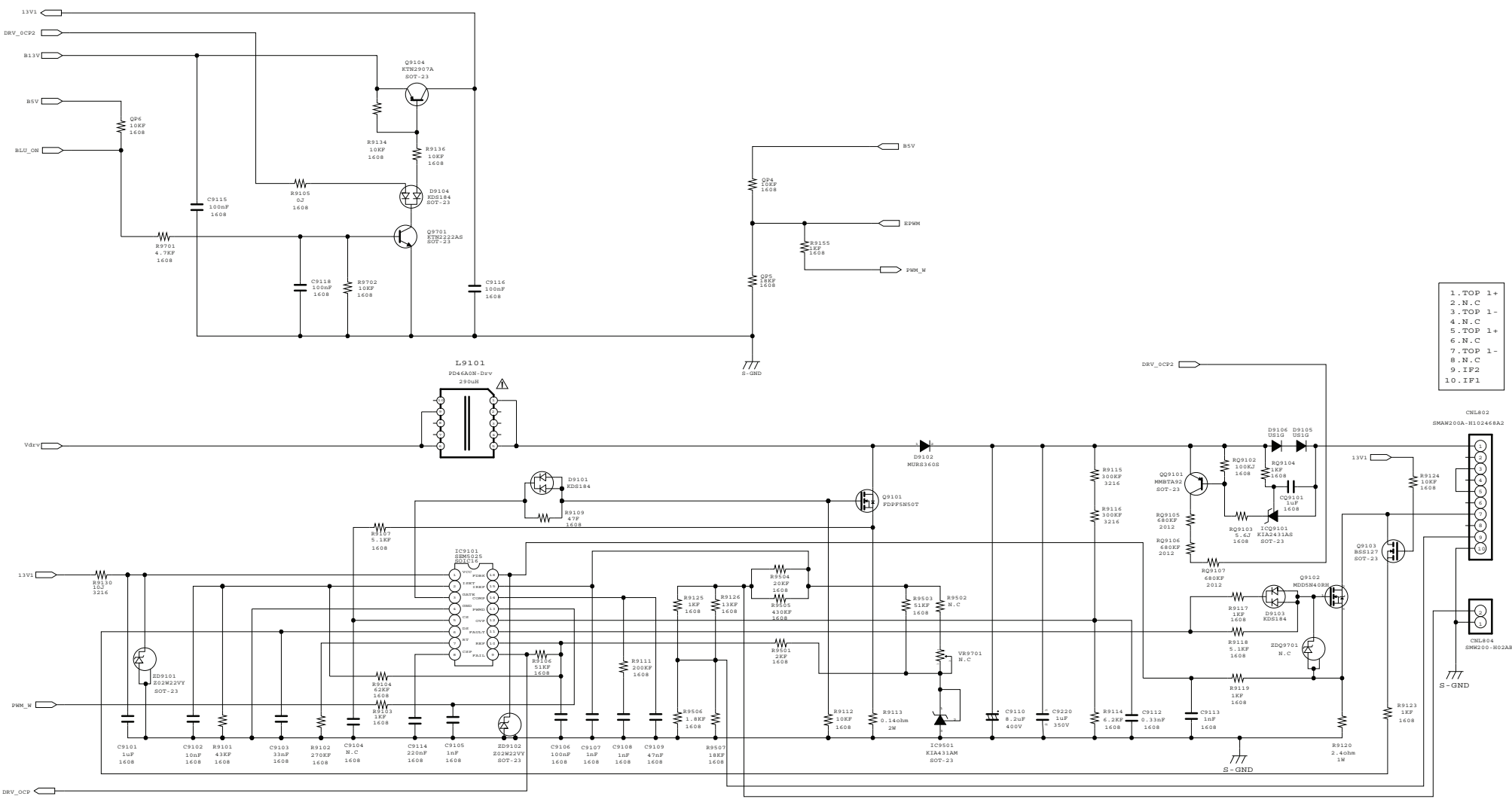


USER	DATE	11.04.27
MODEL NAME PD46G0_BSM	SCALE	DRAWN Y.M.Jung
PDM NUMBER PSLF121A03S	REVISION R.B	CHECKED T.S.Kim
BN44-00473A	CommonScore	APPROVED H.G.Choi



* N.C : No Connection

		USER	DATE
		MODEL NAME	11.04.27
FDM NUMBER PSLF121A03S BN44-00473A	SCALE	DRAWN	Y.M.Jung
	REVISION	CHECKED	T.S.Kim
	LEVEL	APPROVED	H.G.Choi



- 1. TOP 1+
- 2. N.C
- 3. TOP 1-
- 4. N.C
- 5. TOP 1+
- 6. N.C
- 7. TOP 1-
- 8. N.C
- 9. IP2
- 10. IP1

CHL802
SMAN200A-R102468A2

CHL804
SMK200-R02AB

MODE	IP1/IP2
308mA	OPEN
290mA	SHORT

* N.C : No Connection

NOTE



USER	DATE	11.04.27
MODEL NAME PD46G0_BSM	SCALE	DRAWN Y.M.Jung
PDM NUMBER PSLP121A03S	REVISION R.B	CHECKED T.S.Kim
BN44-00473A	LEVEL	APPROVED H.G.Choi

5. BLOCK DIAGRAM PART

