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MS-7519

ATX Version: 0A



CPU: Intel Pentium 4, Pentium D, Core2 Duo, Wolfdale, Pentium and Yorkfield processors in LGA775 Package.

System Chipset:

Intel Eaglelake - G/P (G, P3North Bridge)
Intel ICH10 (South Bridge)

On Board Device:

CLOCK Gen -- ICS 9LPRS110A
LPC Super I/O -- Fintek F71882F
LAN -- Realtek 8111C (PCIE)
HD Audio Codec -- RTL888
1394 Controller -- JMB381
PCIE to PATA/SATA Bridge -- JMB363

Main Memory:

Dual-channel DDR-II * 4

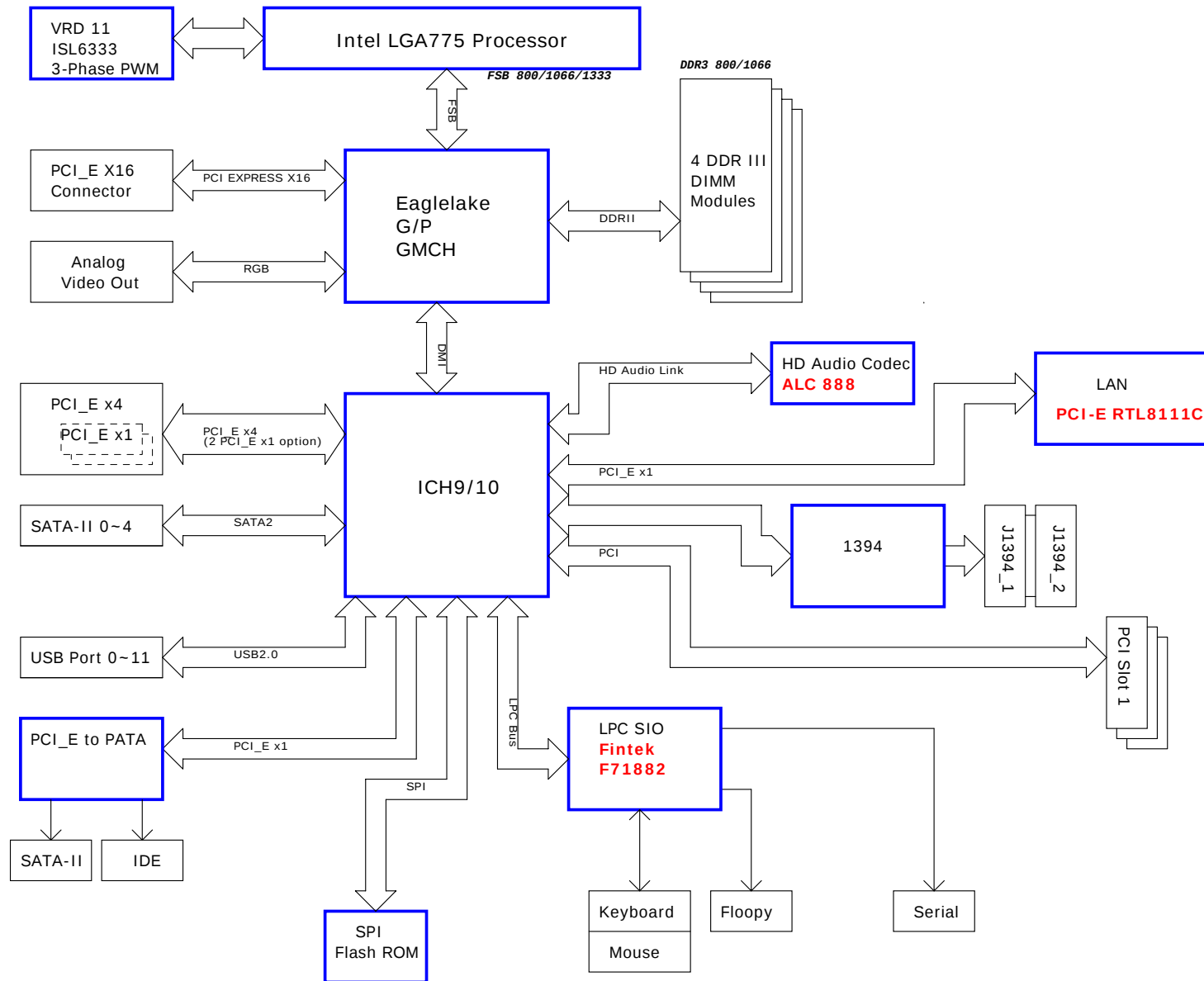
Expansion Slots:

PCI EXPRESS X16 SLOT *1
PCI EXPRESS X1 SLOT * 2
PCI SLOT * 3

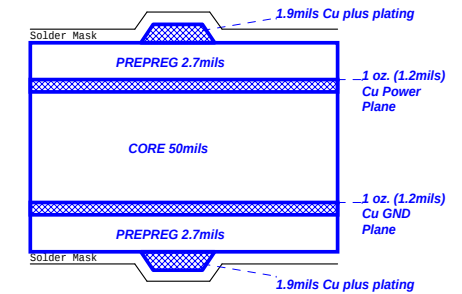
PWM: ISL6333

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	MS-7519			
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Block Diagram

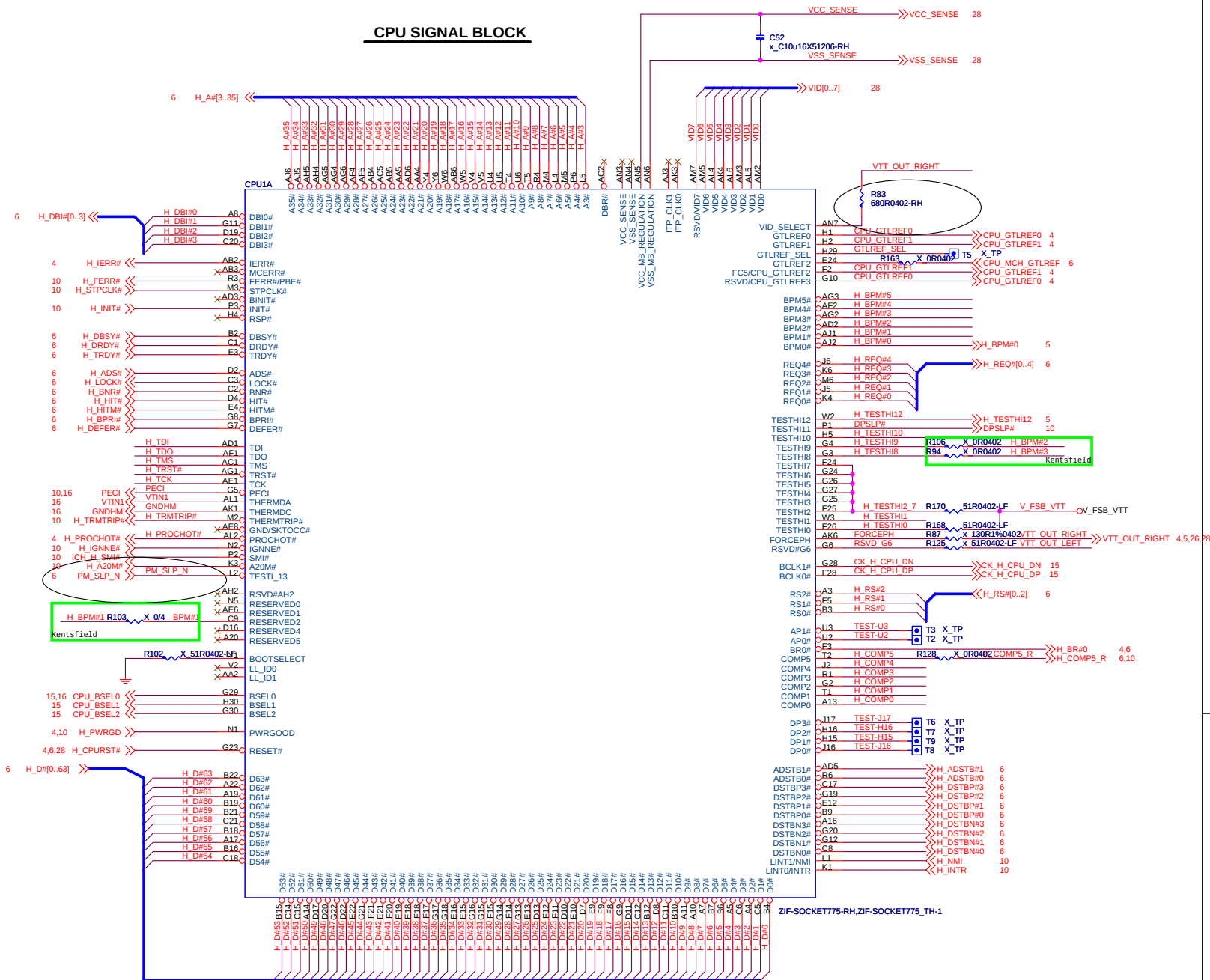


Board Stack-up (1080 Prepreg Considerations)

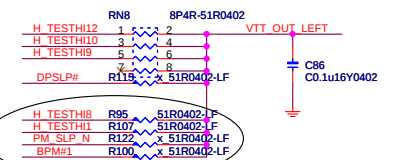
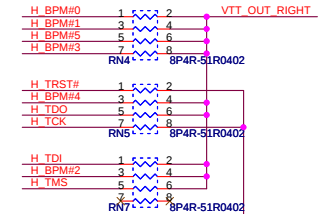
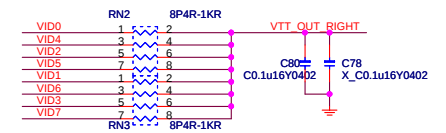


Single End 50ohm Top/Bottom : 4mils
 USB2.0 - 90ohm : 15/4.5/7.5/4.5/15
 SATA - 95ohm : 15/4/8/4/15
 LAN - 100ohm : 15/4/8/4/15
 PCIE - 95ohm : 15/4/8/4/15
 IEEE1394 - 110ohm : 15/4/9/4/15
 IDE : 15/4/8/4/15

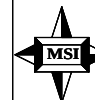
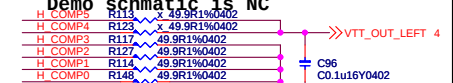
CPU SIGNAL BLOCK



PULL HIGHT PULL DOWN



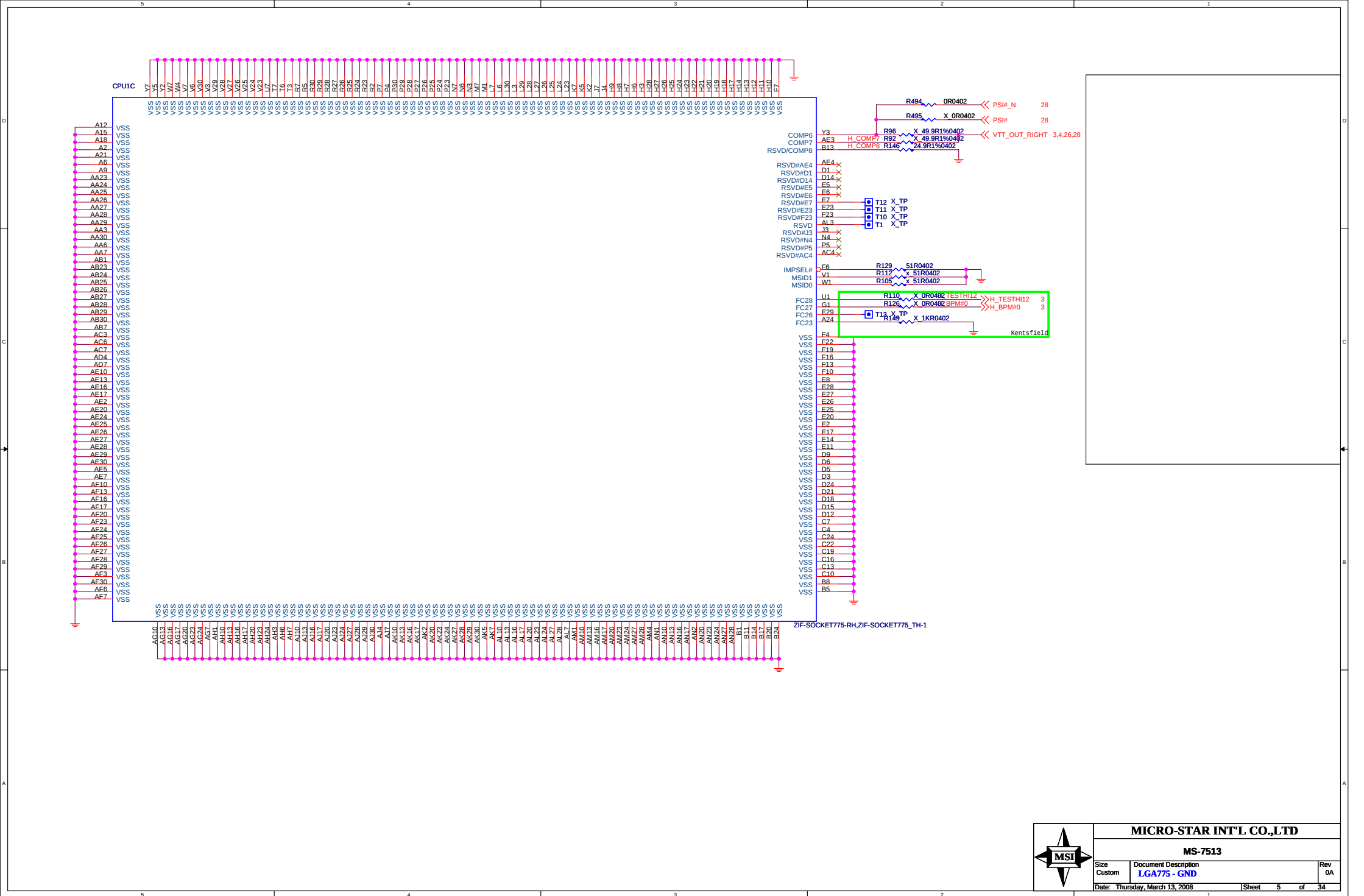
PM_SLP_N/H_TESTHI1
Demo schmatic is NC



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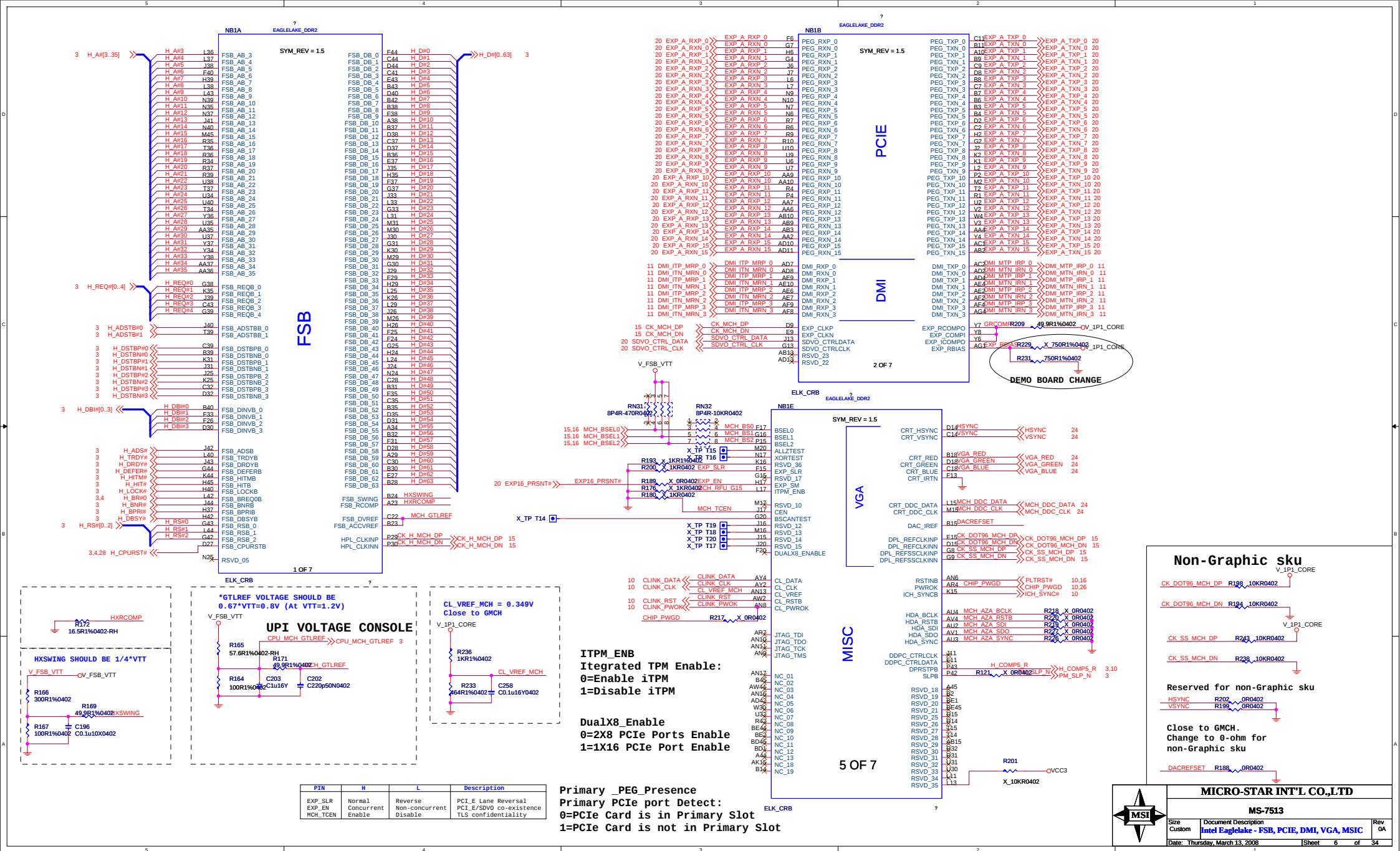
Size Custom	Document Description LGA775 - Signal	Rev 0A
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Size Custom	Document Description LGA775 - GND	Rev 0A
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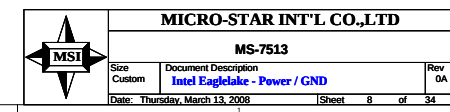


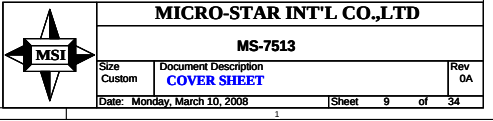
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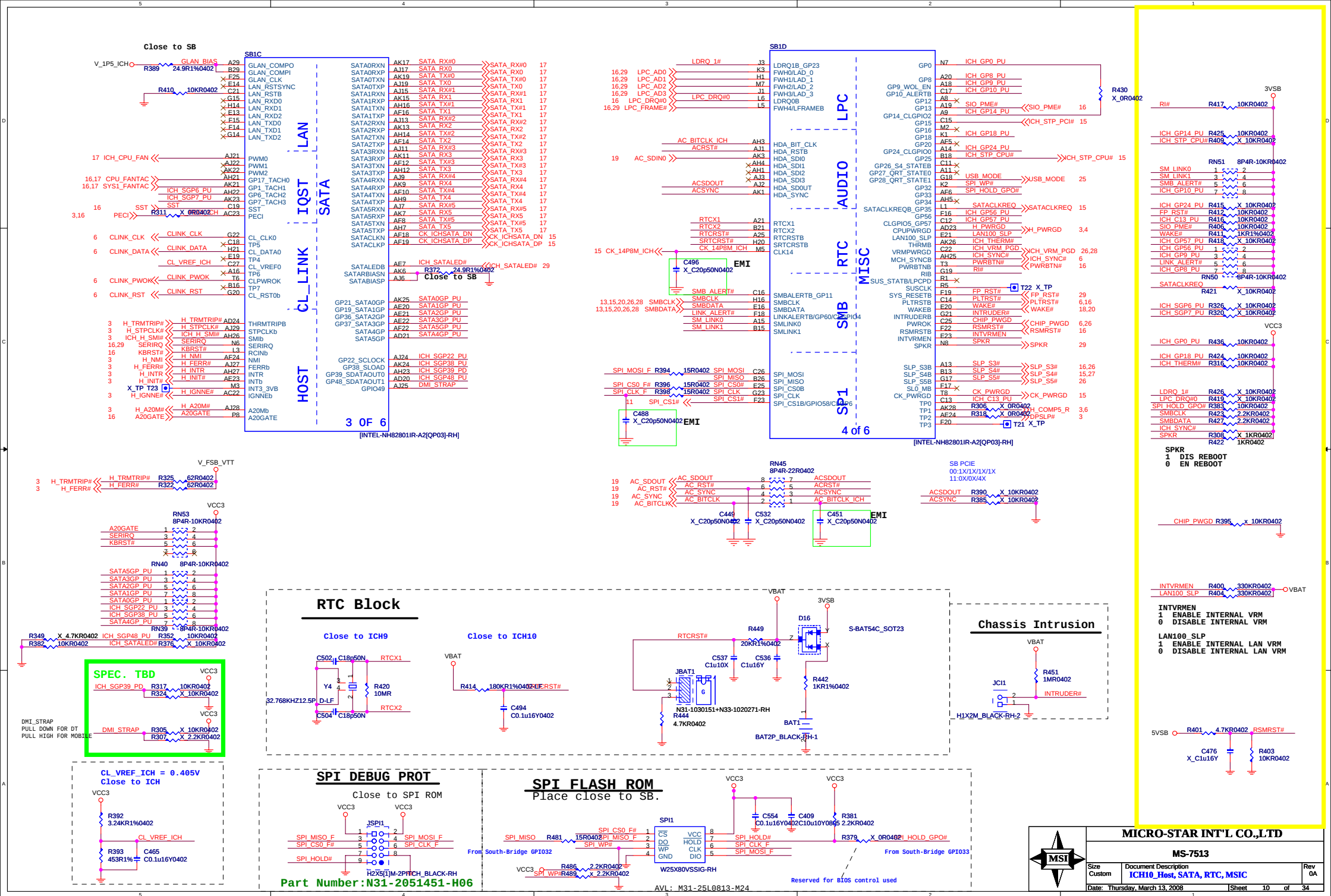
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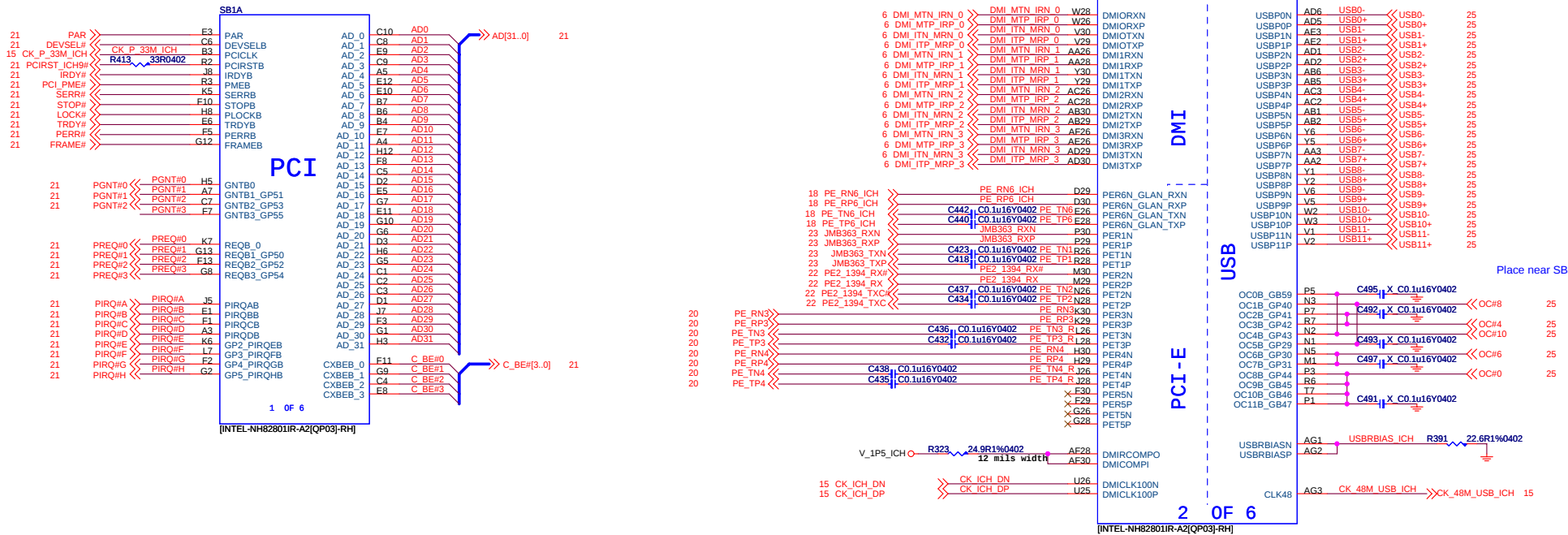
Size Custom	Document Description Intel Eaglelake - FSB, PCIE, DMI, VGA, MSIC	Rev 0A
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For non-Graphic sku
change to 0-ohm (0603)

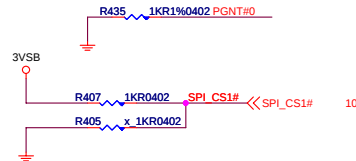






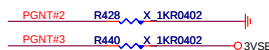


SB STRAPPING RESISTOR



BOOT SELECT STRAPS

BOOT DEVICE	GNT#0	SPI_CS1#
FWH	1	1
SPI	0	X
PCI	1	0



SIGNAL	H	L	DES.
GNT3	DIS	EN	A16 OVERRIDE
GNT2	N/A	SET BIT	PCIE PORT CONFIG 2 BIT 0 (5-6)

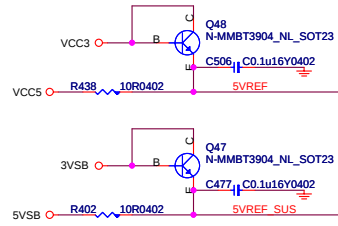


MICRO-STAR INT'L CO.,LTD

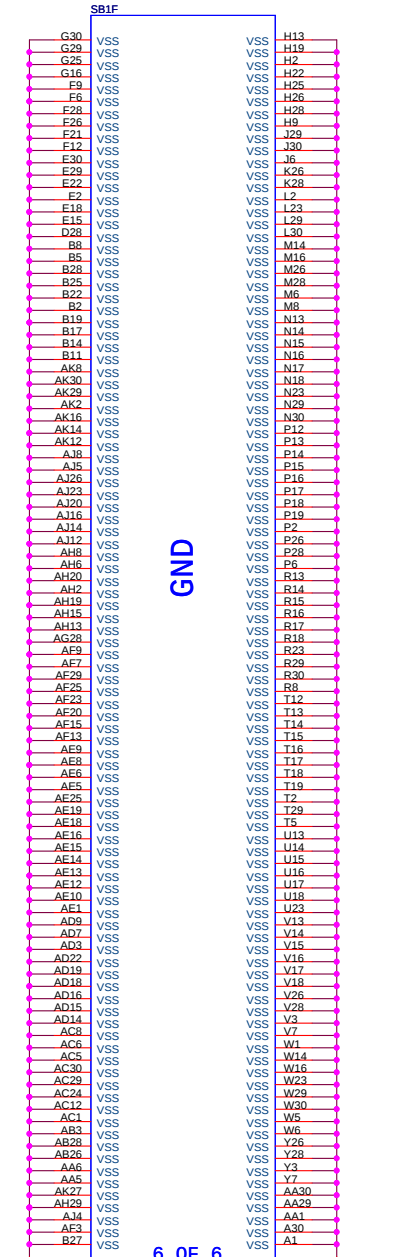
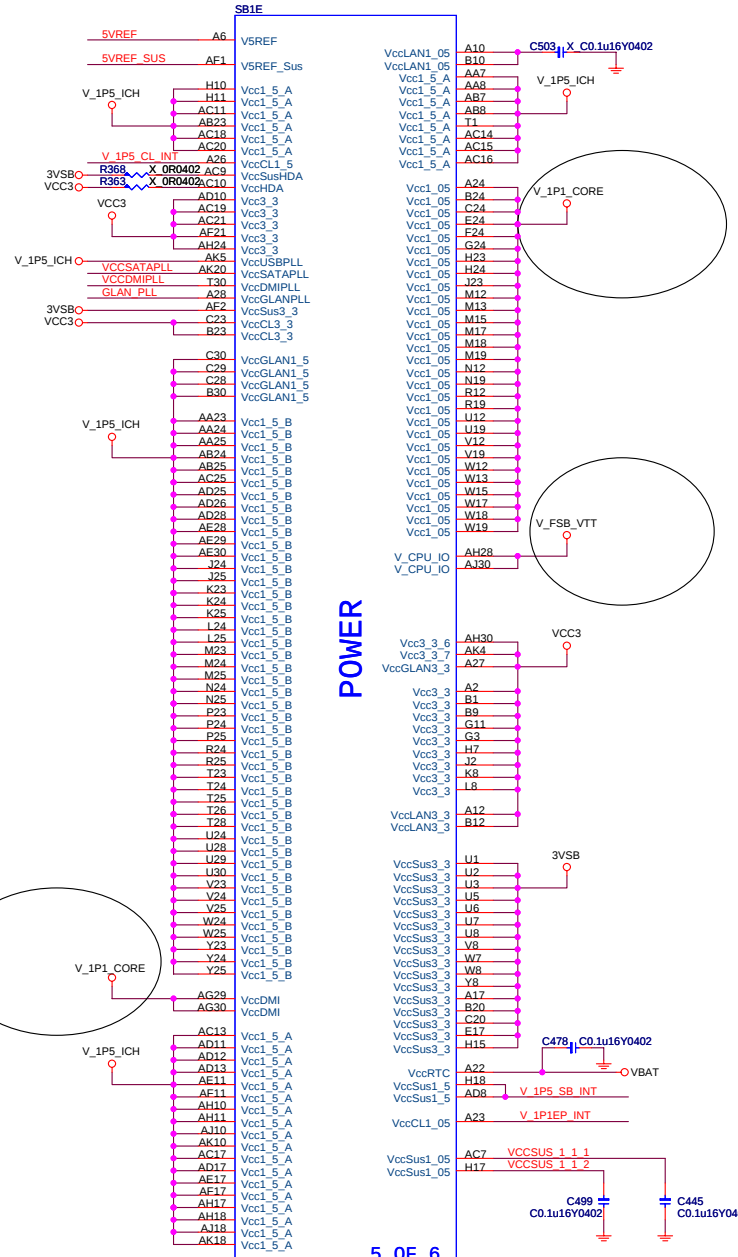
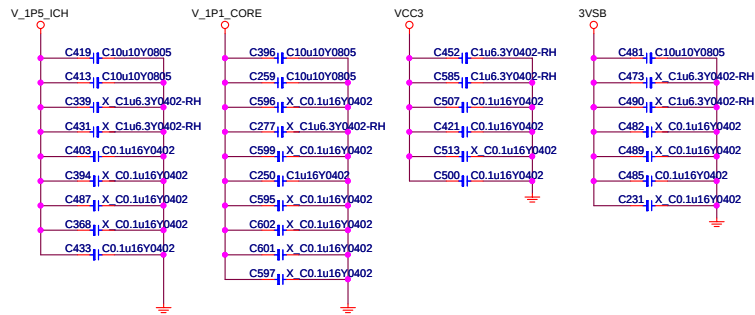
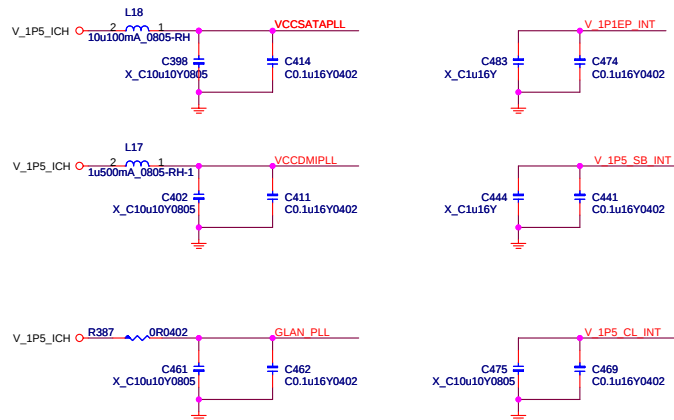
MS-7513

Size	Document Description	Rev
Custom	ICH10_PCI, USB, DMI, PCIe1	0A
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5VREF & 5VREF_SUS Sequencing Circuit



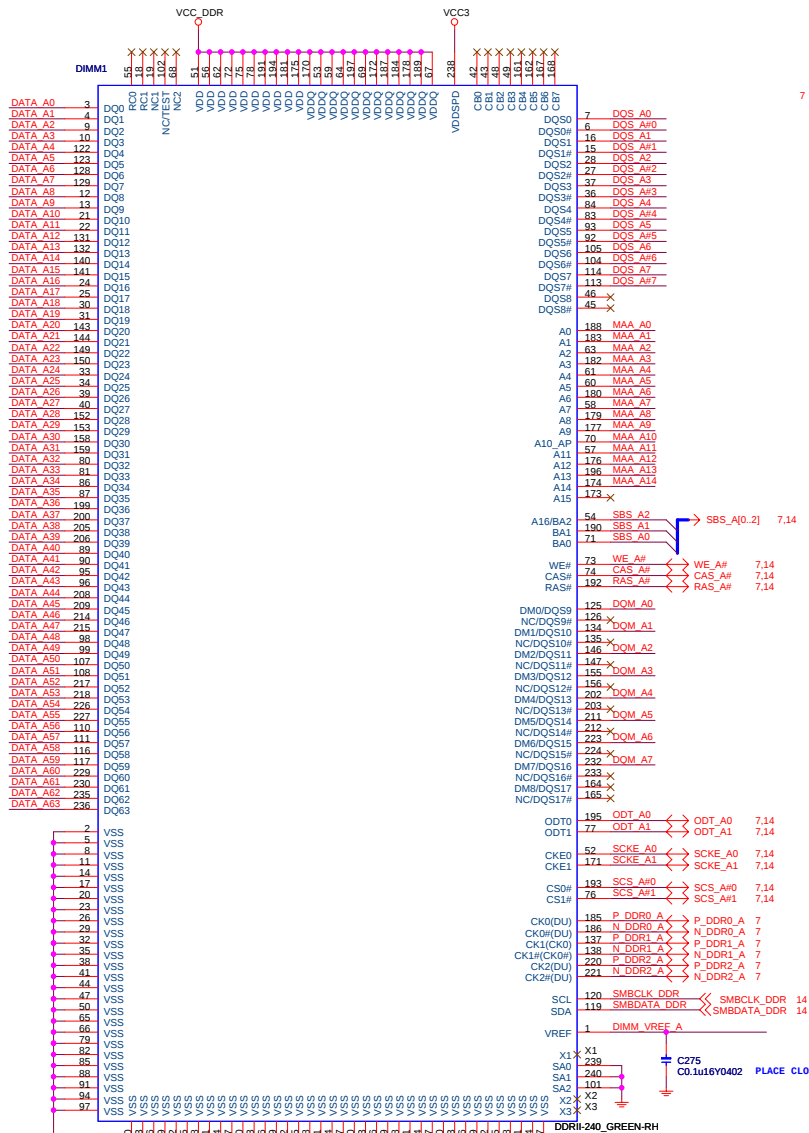
SB POWER



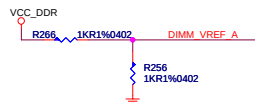
AC7, H17 & H18, AD8
spec TBD



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Size	Document Description	Rev	
Custom	ICH9/10 - Power, GND	0A	
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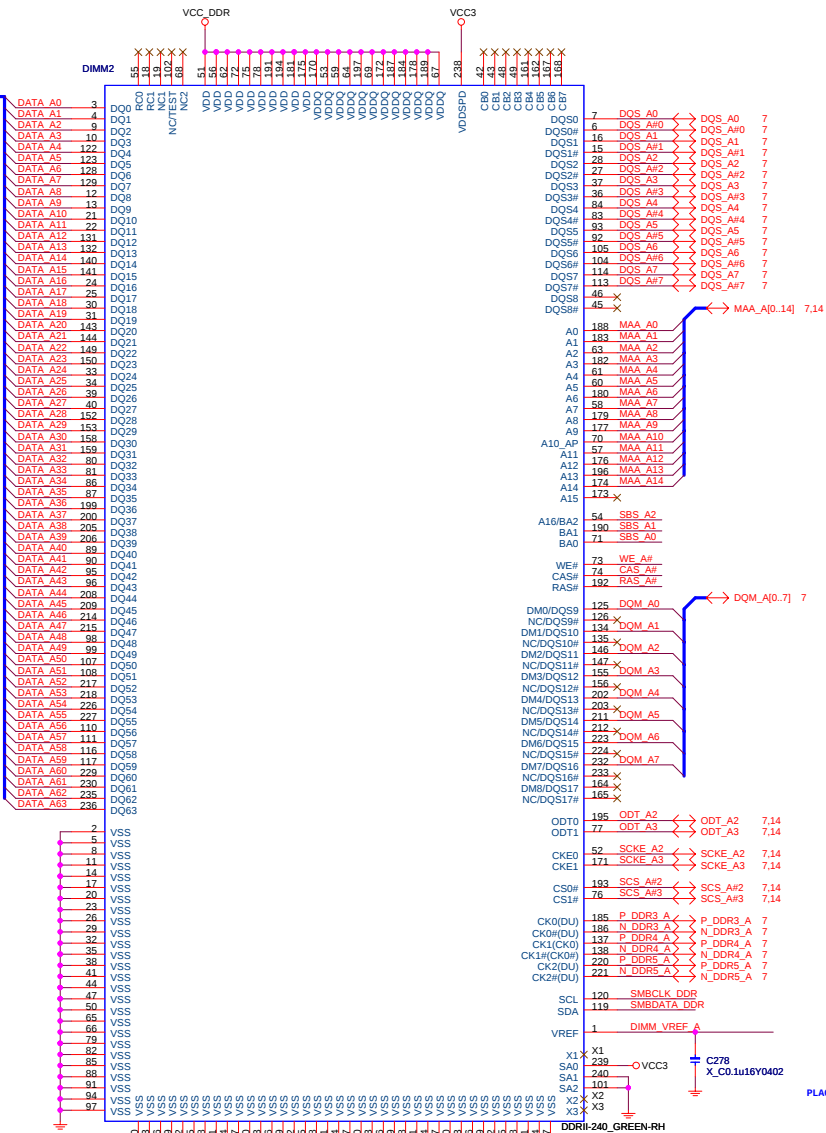


DDR2 DIMM_A1



ADDRESS: 000
0xA0

SMBCLK_DDR R91 33R0402
SMBDATA_DDR R89 33R0402
10,15,20,26,28
10,15,20,26,28



DDR2 DIMM_A2



ADDRESS: 001
0xA2



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Size Custom	Document Description	Rev 0A
DDR2 Chane-A / Chane-B		
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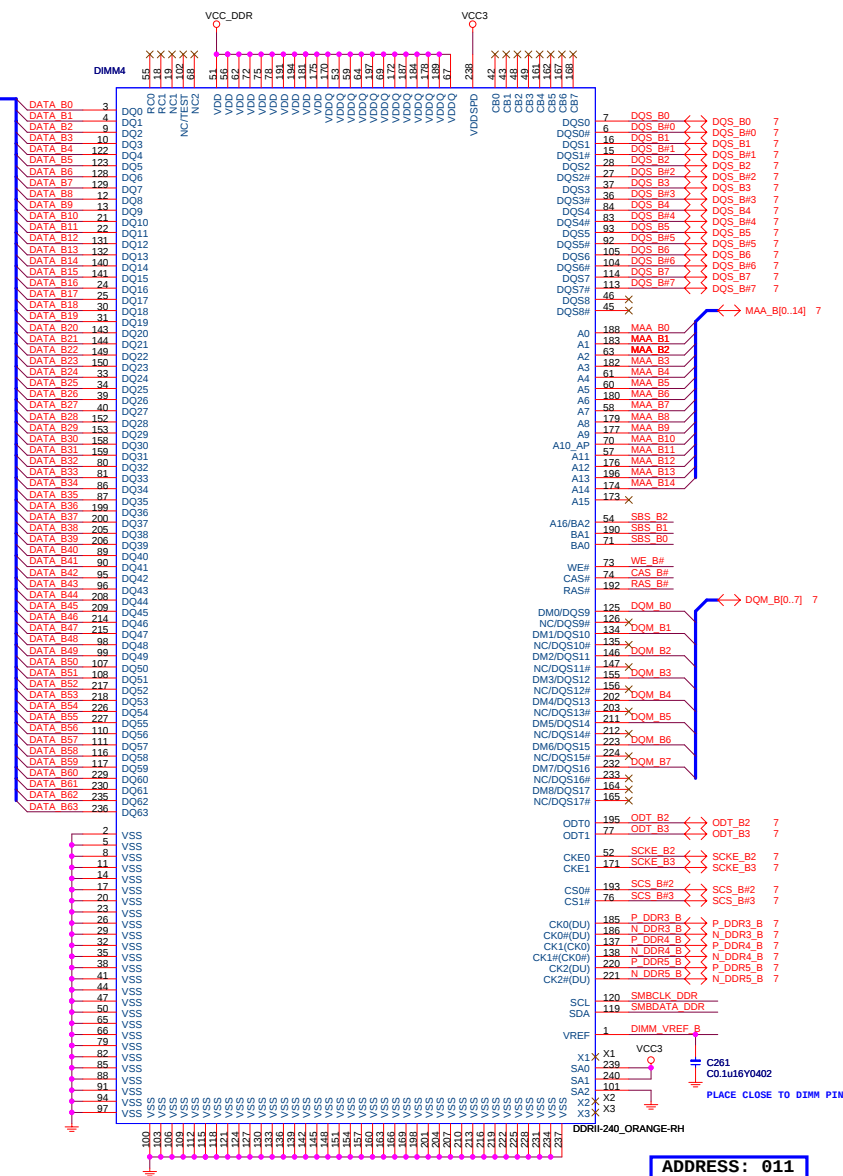
DDR II Termination



DDRII DIMM_B1

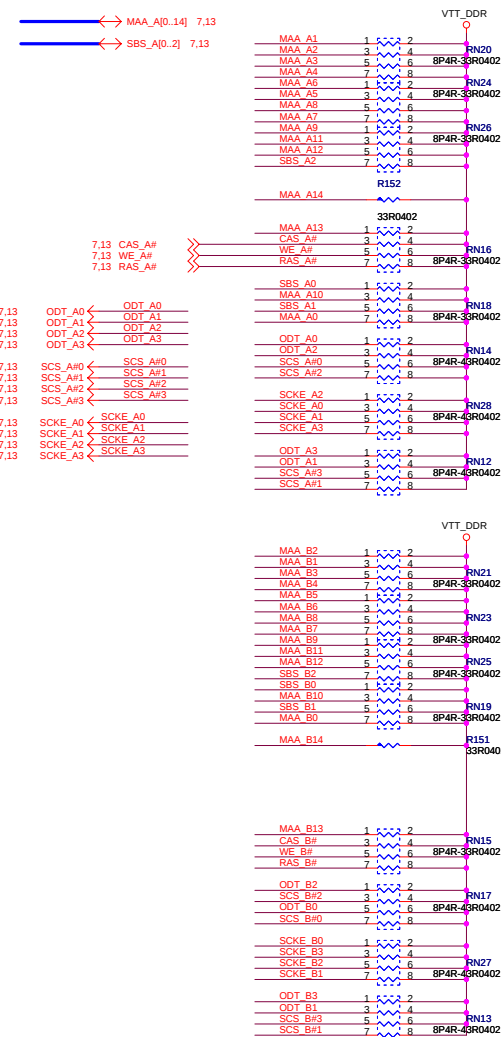


ADDRESS: 010
0xA4



DDRII DIMM_B2

ADDRESS: 011
0xA6

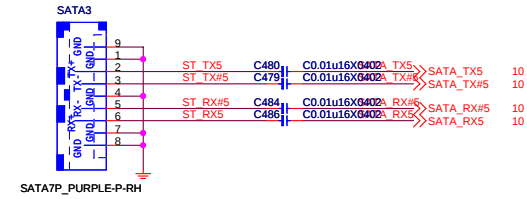
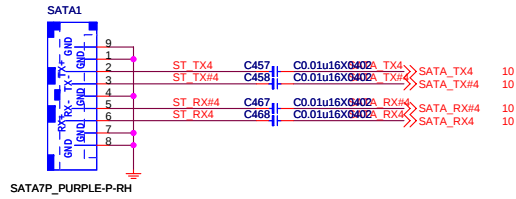
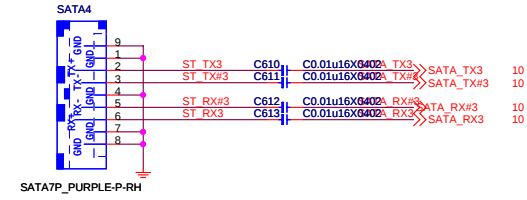
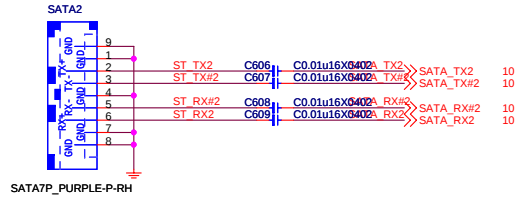
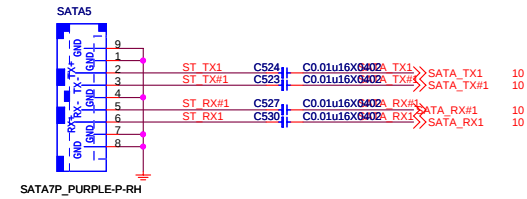
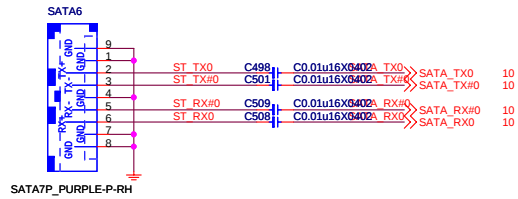


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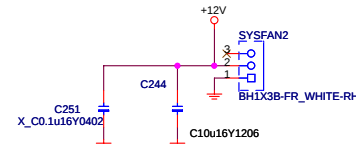
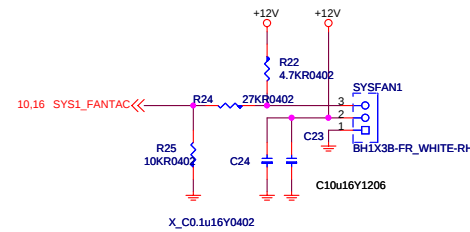
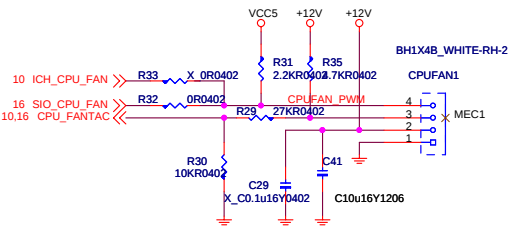
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Size	Document Description
Custom	DDR3 Chanel-A / Chanel-B

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FAN-COUNTROL CIRCUIT



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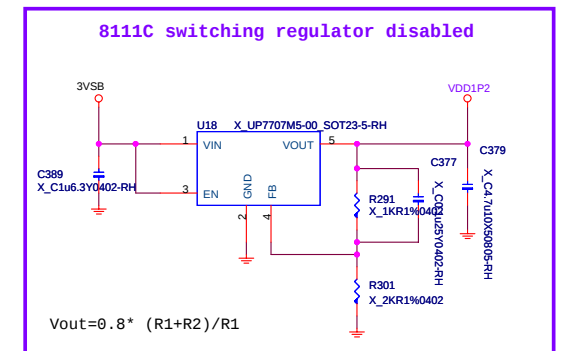
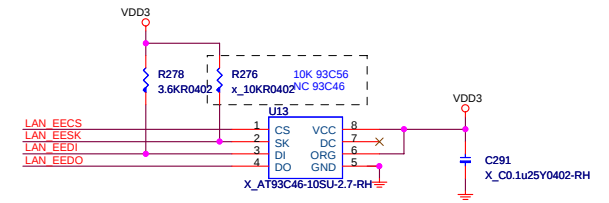
Size	Document Description	Rev
Custom	SATA & e-SATA Ports and Fan Control	0A
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VDD3R R270 0R0805 VDDSR C330

40mil width

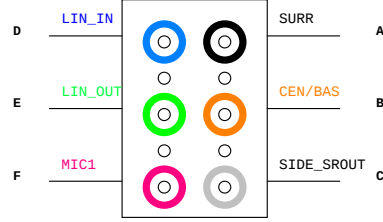
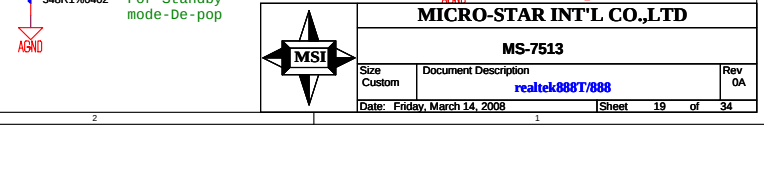
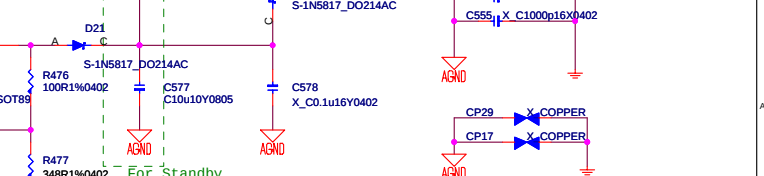
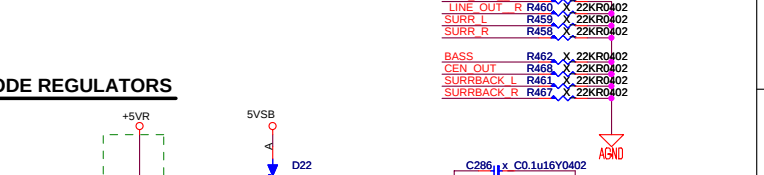
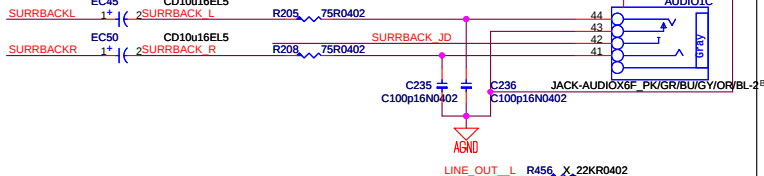
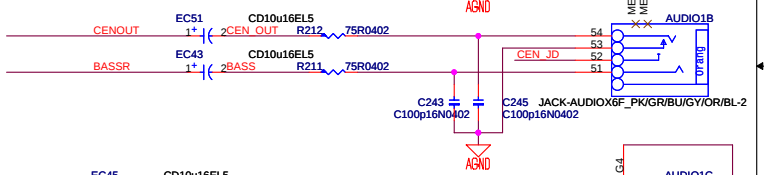
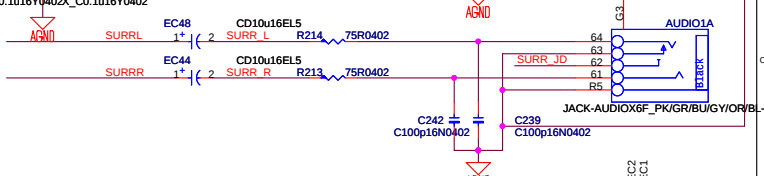
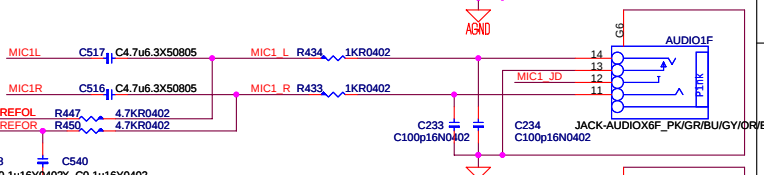
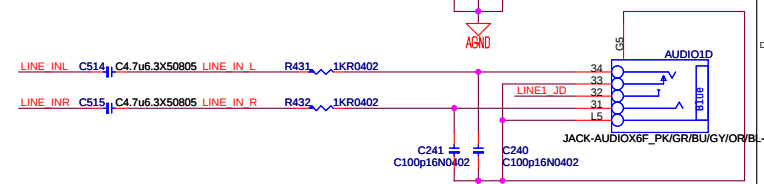
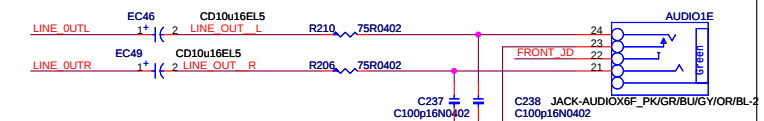
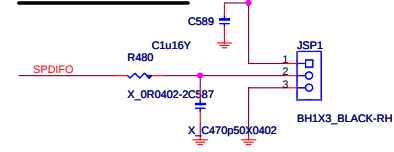
C220 100nF C329 100nF C336 100nF C337 100nF

C657 C346 near Pin63 200mil, C346 must be nearly Pin63

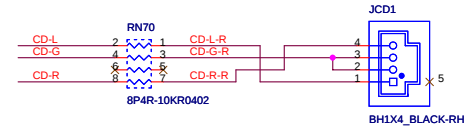


Size Custom	Document Description LAN Realtek RTL8111C(PCIE)	Rev 0A
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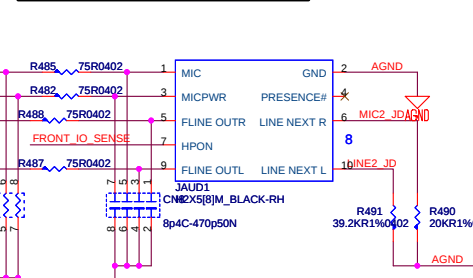
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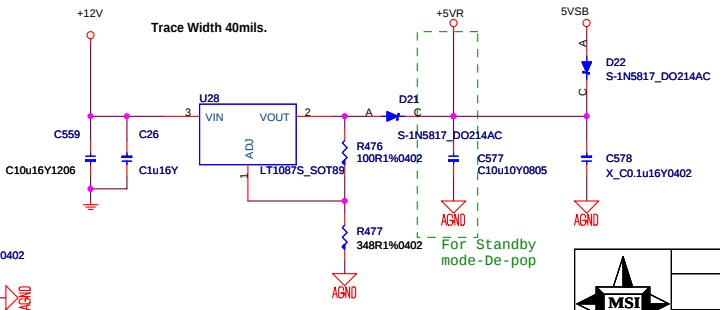
CD_IN



Azalia Front Audio Connector



AUDIO CODE REGULATORS



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Custom	realtek8887/888	0A
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Trace width > 200 mils



10,13,15,26,28 SMBCLK >> SMB
10,13,15,26,28 SMBDATA << SMB



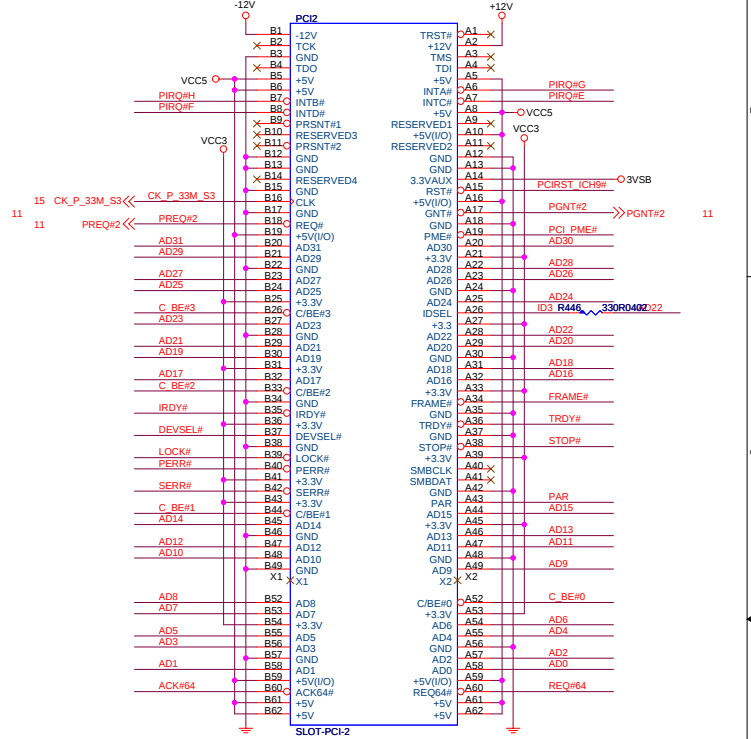
3VSB VCC3 +12V



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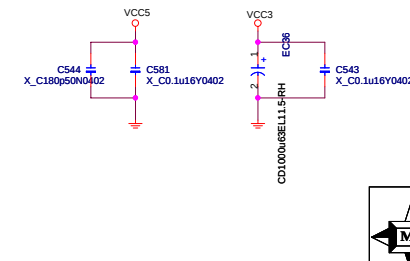
Size Custom	Document Description PCIE x16, x1	Rev 0A
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PCI SLOT 3 (PCI VER: 2.2 COMPLY)



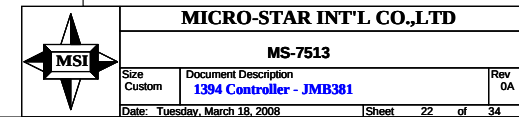
```
IDSEL = AD22
MASTER = PREQ#2
PIRQ#G
```

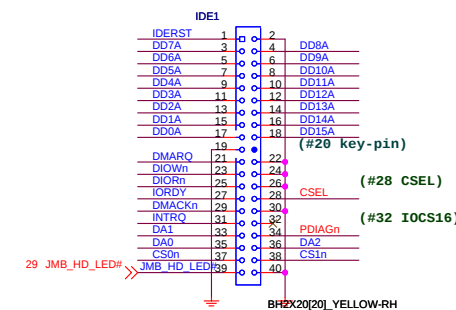
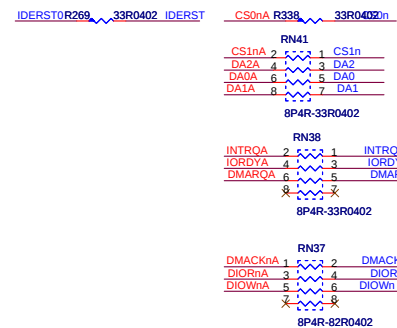
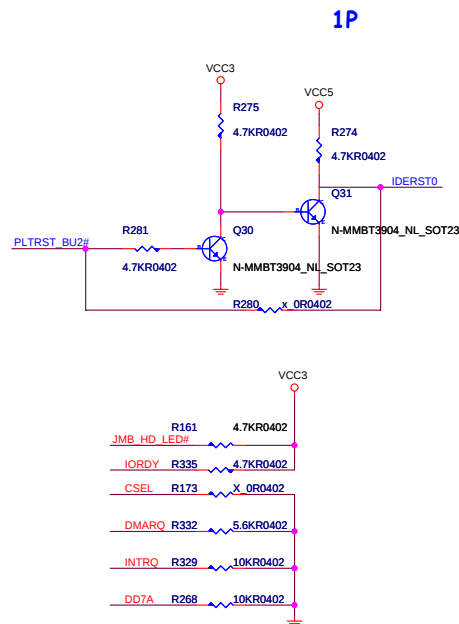
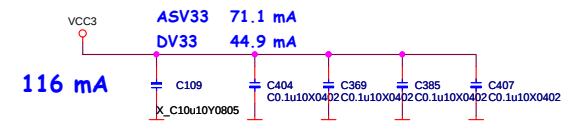
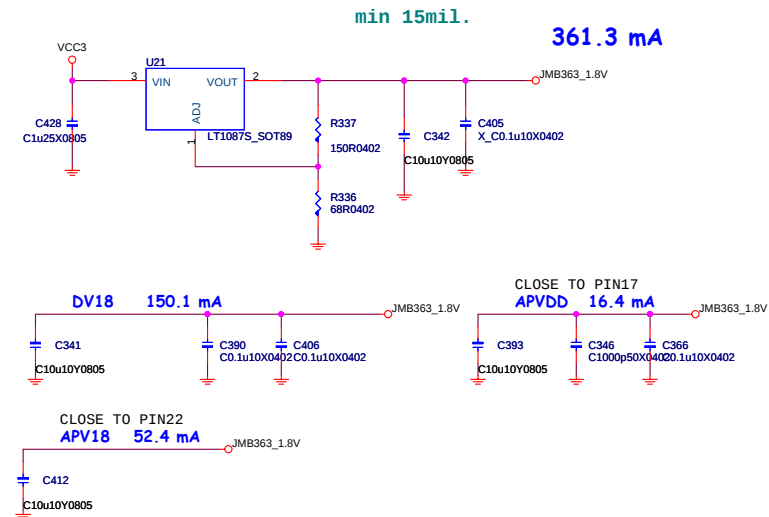
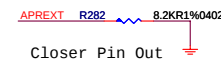
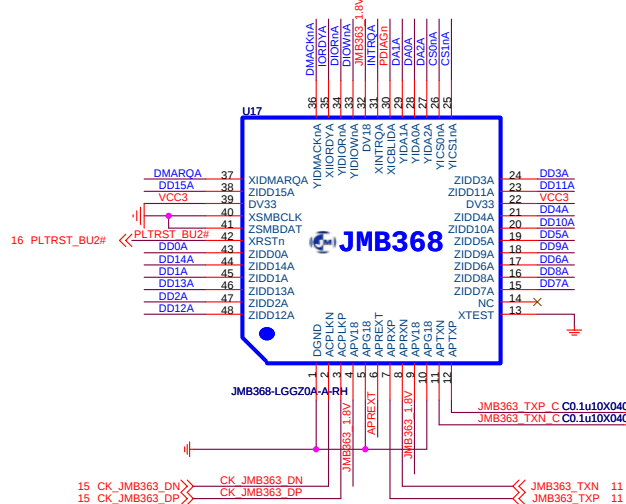
PCI SLOT DECOUPLING CAPACITORS



Size Custom	Document Description PCI Slot 1 & 2	Rev 0A
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Rear 1394 port

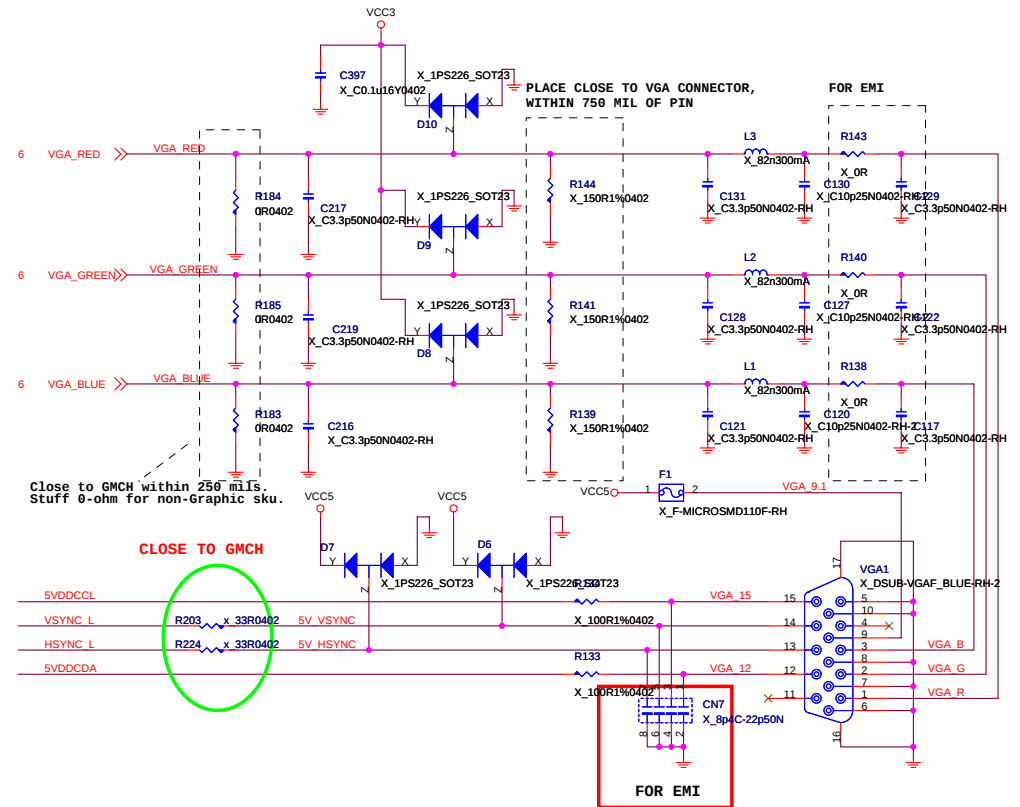
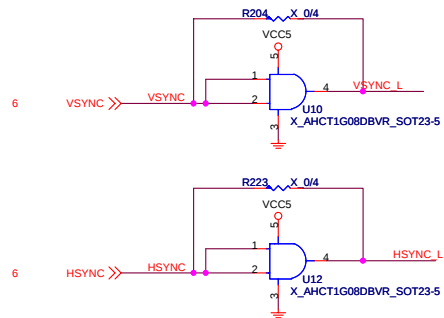
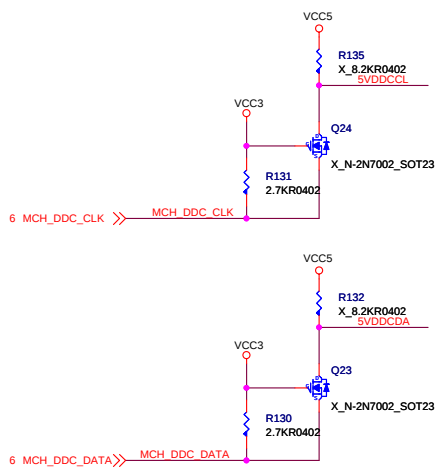




if the length of JMB-368 to IDE connector more than 4inch, that must stuff damping resistor.

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Custom	JMB-363 e-SATA X2/ IDE X1	0A	
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Video Connector



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Size	Custom
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Document Description
Onboard VGA

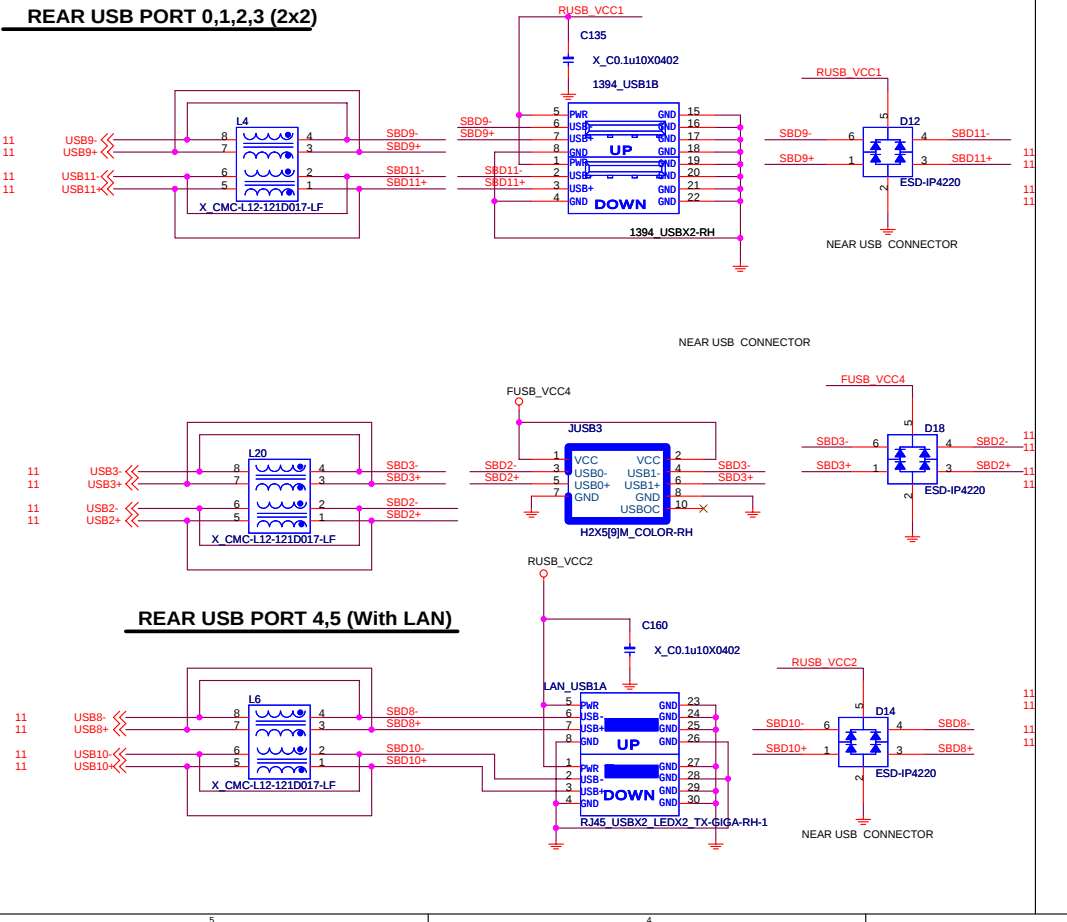
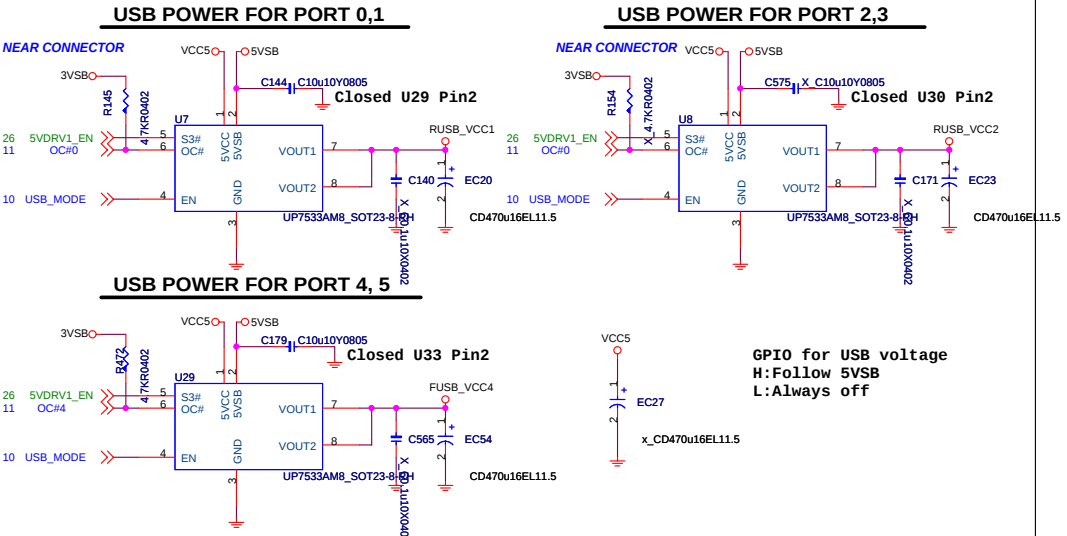
Rev
0A

Date: Thursday, March 13, 2008

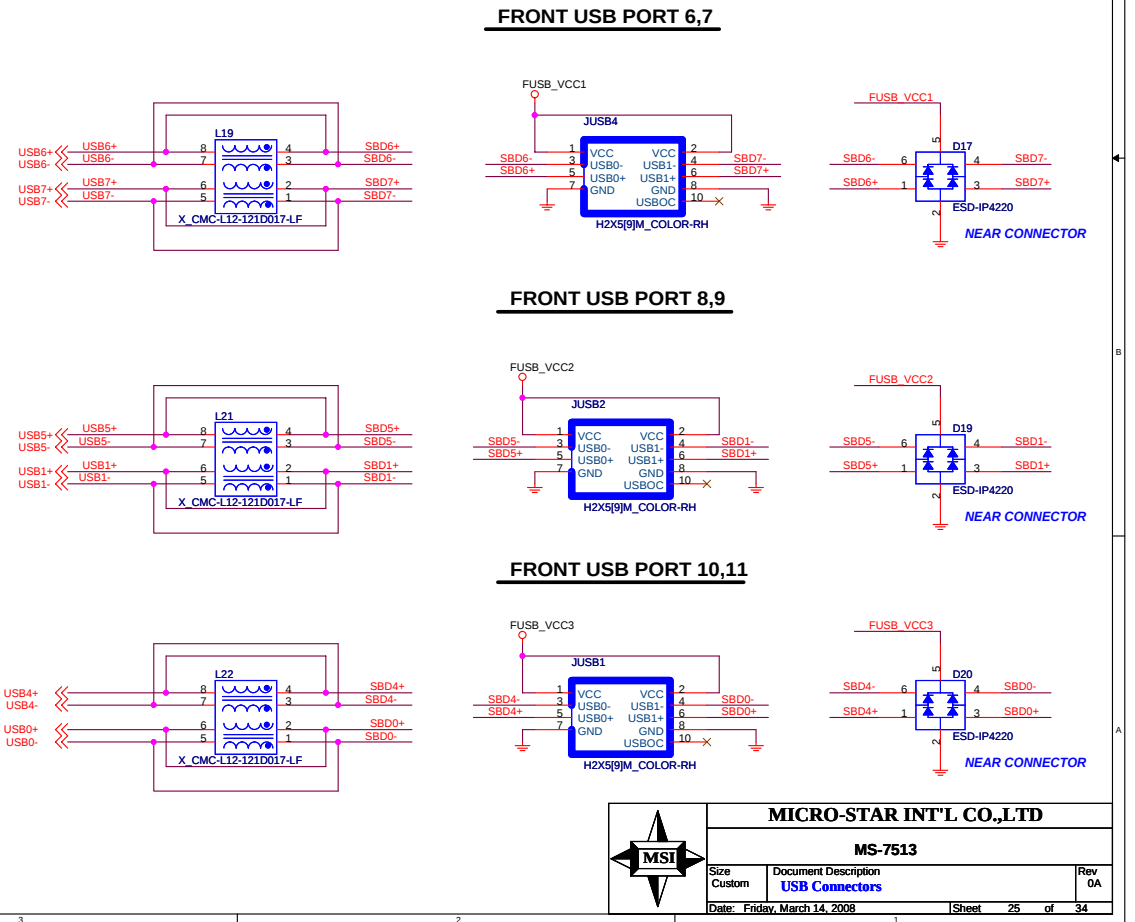
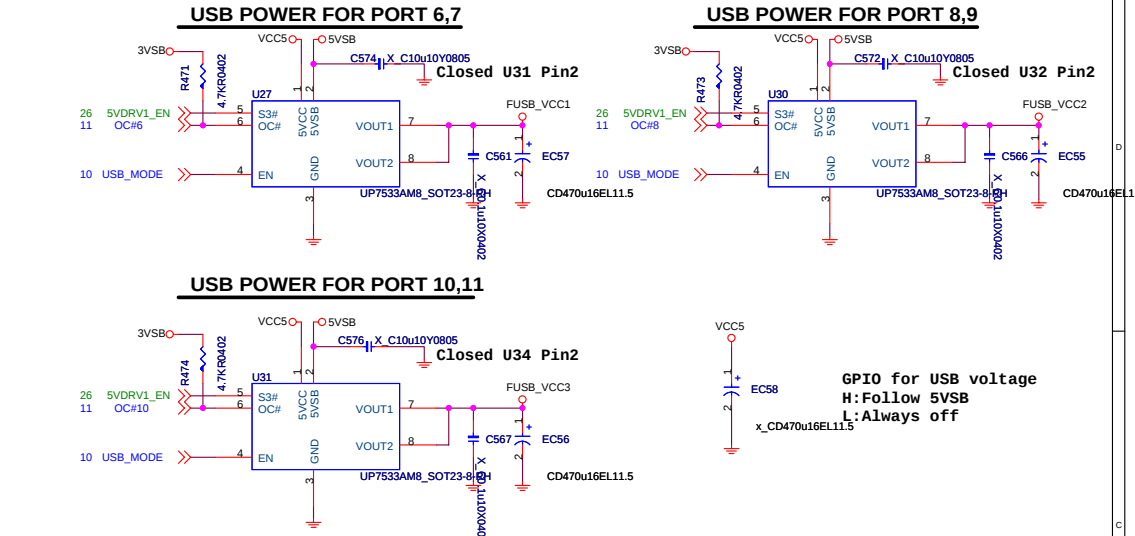
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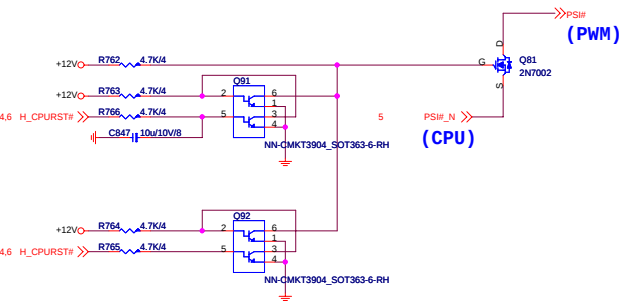
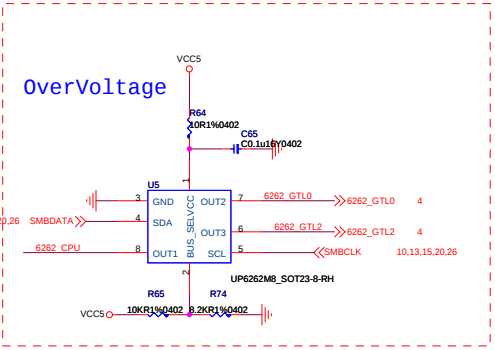
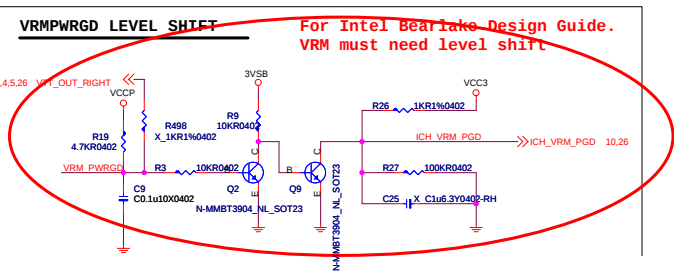
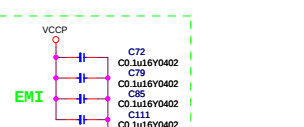
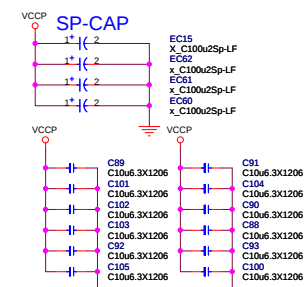
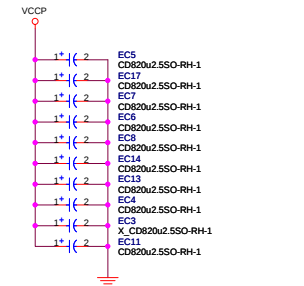
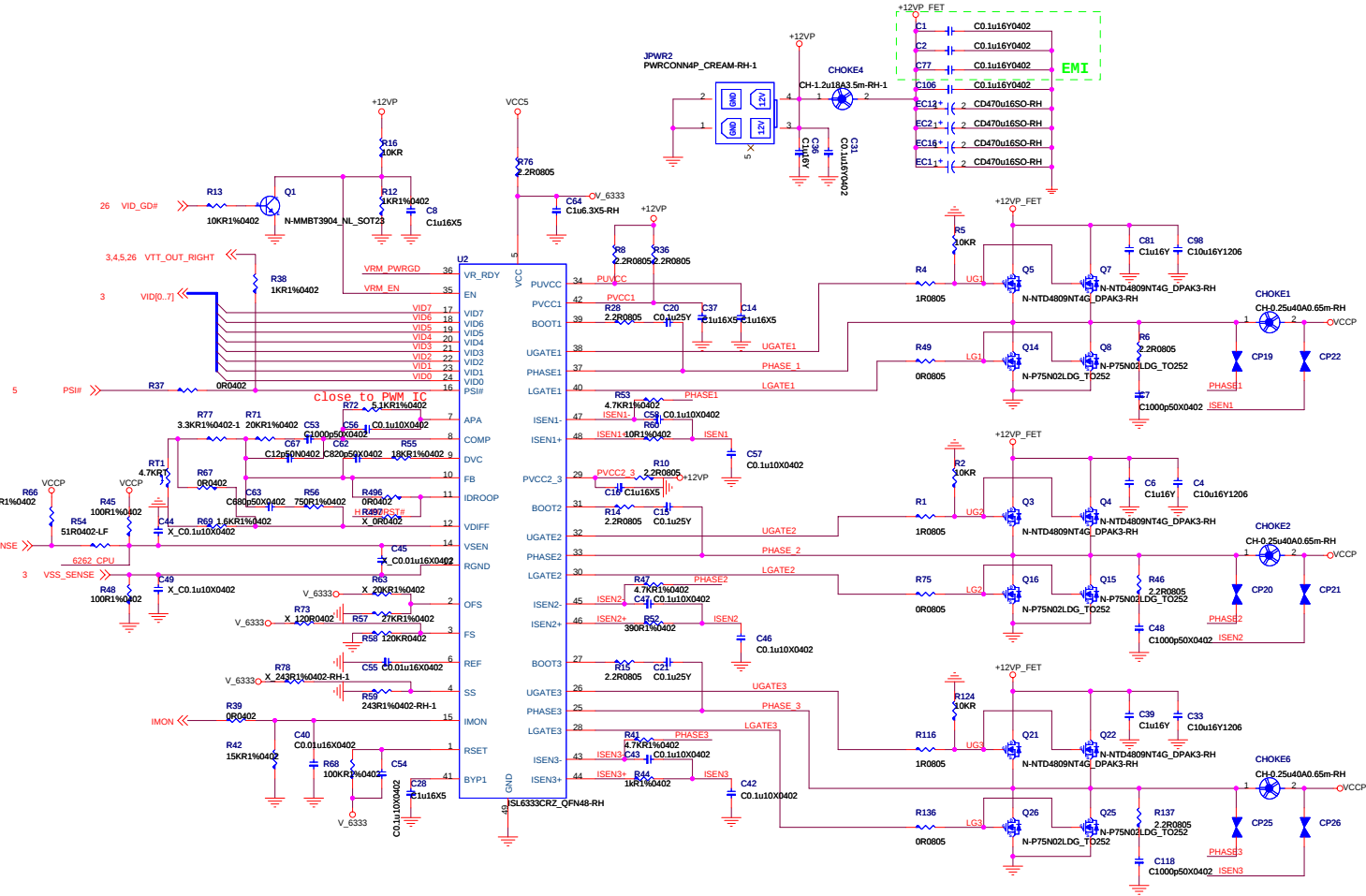
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Rear USB Connector



Front USB Connector





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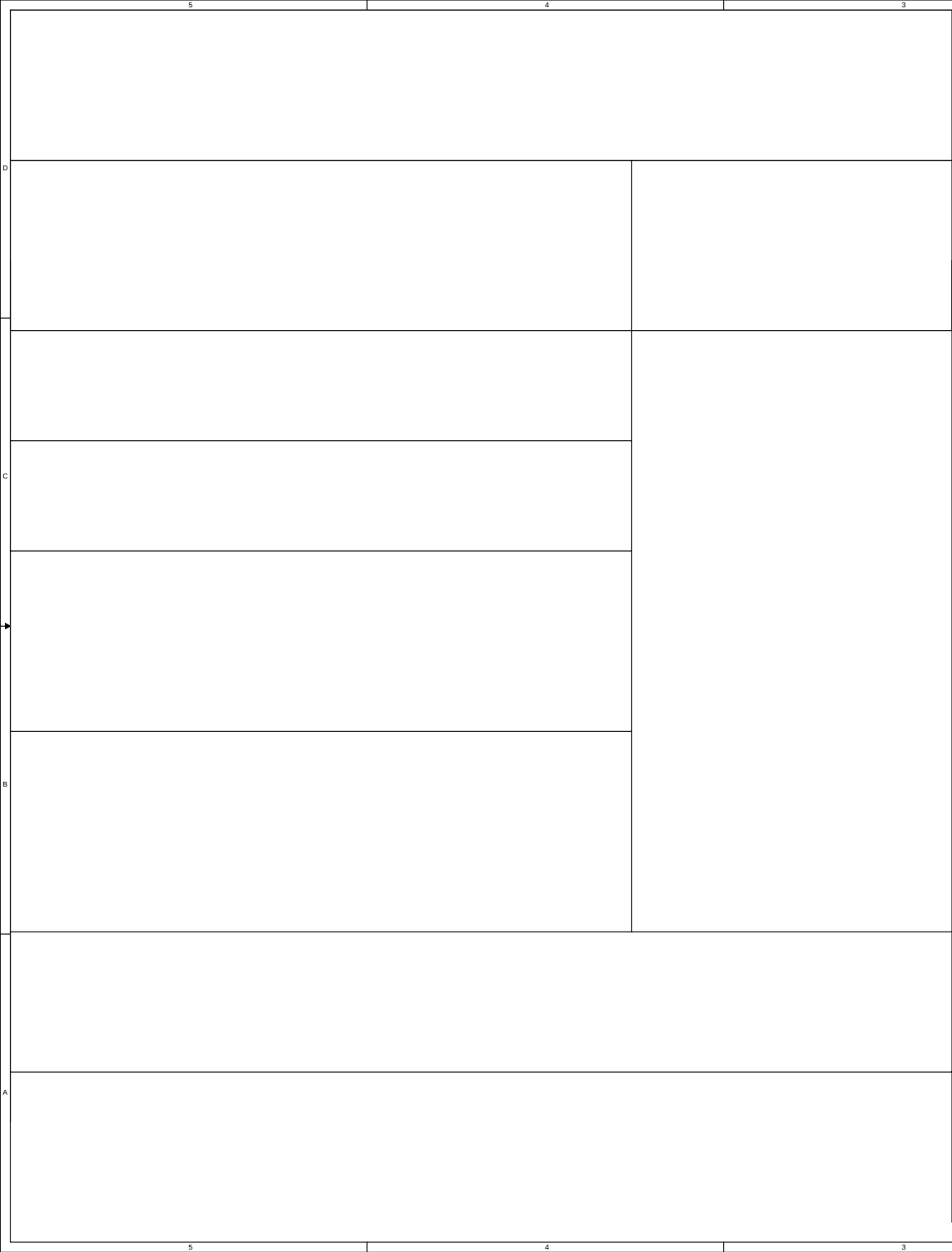
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Figure 1: Schematic diagram of the power supply circuit for the ADXL345. The circuit shows a VCC5 pin connected to a 5VSB supply through a 100nF capacitor. The 5VSB supply is also connected to a 5VSB pin. The ADXL345 chip is shown with pins C13, C604, C545, C510, C583, C107, C582, C531, C58, and C35. The chip is connected to ground through a 100nF capacitor. The chip is also connected to a 5VSB supply through a 100nF capacitor.

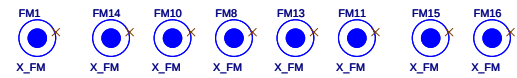
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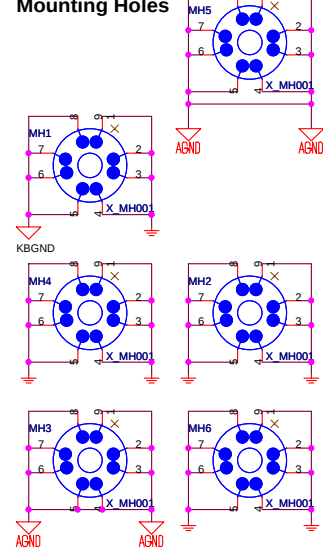
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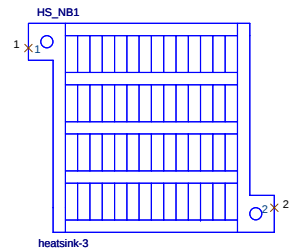
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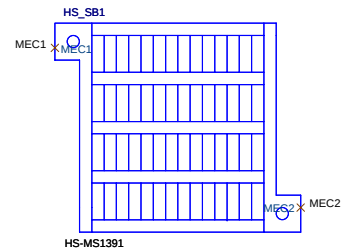
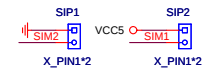
Mounting Holes



EMI



Simulation



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