

## Nadia Nedjah (NEDJAH, N.)

Curriculum Vitae with relevant information only

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### Academic Titles

- 1994 - 1997** Ph.D. in Computation.  
University of Manchester — Institute of Science and Technology, UMIST, United Kingdom  
Research title: *Pattern-Matching Automata for Efficient Evaluation in Equational Programming*.
- 1987 - 1990** M.Sc. in Computation.  
Université D'Annaba/Glasgow University, Algeria/United Kingdom  
Research title: *Evaluation des Performances de Compilateurs du Langage Pascal* (in French).
- 1982 - 1987** Graduation in Systems Engineering and Computation.  
Université D'Annaba, Algeria  
Project title: *Conception et réalisation d'un interpréteur et méteur au point pour un sous-ensemble du langage Pascal* (in French).
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### Professional Activities

- 2005 - now** Full Professor, Universidade do Estado do Rio de Janeiro (UERJ), Brazil  
**1999 - 2005** Associate Professor, Universidade Veiga de Almeida (UVA), Brazil  
**1990 - 1998** Adjunct Professor, Université do Annaba (UA), Algeria
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### Main research projects

**Swarm Robotics:** Swarm robotics is a new approach allowing the coordination of a large number of small robots to perform a complex task. Research in this area makes it possible to apply the principles of swarm intelligence to solve real problems, where human action is not recommended. We investigate three research axes: *(i)* focusing on the robot, targeting a specific type of autonomous robots, applying a swarm scenario that can be performed by the selected robot and testing the efficiency of the proposed swarm; *(ii)* focusing on the scenario, having a set of swarm control algorithms, choosing/engineering a type of robot suitable for the experiment and testing the efficiency of this swarm; *(iii)* focusing on the swarm, devising a set of scenarios for swarm control and the different types of robots needed to execute each scenario, testing the swarm's efficiency.

**Intelligent Systems: Techniques and Applications:** An intelligent system is anyone that perceives, reasons, learns and acts based on methodologies in the area of computational intelligence. This area deals with the development of methods based on mechanisms that translate the concept of intelligence and the application of these methods in solving problems in different areas of knowledge. Currently, some methods used include fuzzy logic, neural networks, genetic algorithms, collective intelligence, evolutionary electronics, evolutionary computation. New methods tend to emerge from the moment when the concept of intelligence evolves, due to advances in the biomedical and human areas. In this way, intelligent systems offer solutions that could be used by man, in order to optimize costs, time and material resources, as well as safety, accessibility, mobility. One example is the use of robots in the American space program, from the space station itself to the exploration of new celestial bodies. Recently, the decision of the American government was to no longer use astronauts, mainly because of the time it takes for a space trip to reach more distant celestial bodies. In this context, this project aims to develop methodologies for Intelligent Systems, with application to current complex problems. The emphasis will be on problems associated with the area of Electronic Engineering. Thus, in addition to consolidating postgraduate studies, the project will contribute to the training of human resources in the area.

**Design and Synthesis of a Quantum Processing Machine:** This project divides the quantum machine into 3 elements: a classic processor, a RAM memory and a quantum coprocessor. The classic processor is responsible for executing the conventional instructions that are part of its instruction set. RAM memory contains the user program to be executed, which in addition to conventional instructions can include quantum instructions. The coprocessor is responsible for executing quantum instructions, including a quantum memory. Quantum memory stores the quantum state of the machine, which can only be manipulated by the coprocessor. In order to physically implement the machine, we intend

to use a prototyping platform equipped with a conventional ARM or PowerPC type processor and re-configurable devices to implement the co-processor and its quantum memory. In addition to the implementation of these components, we will have to take into account the interconnection architecture that will allow communication between them.

**Automatic Circuit Synthesis:** As digital systems become larger and more complex, designers need more powerful synthesis tools to help them design digital circuits. Synthesis systems have proven to be efficient in supporting the design of integrated circuits, in particular ASICs. They allow circuit design starting from a high-level specification, which can be reasonably independent of the target technology. Another advantage is that the project becomes more portable. When the description is at a high level, you can easily incorporate modifications. As a result, the design more readily adapts to modifications. Project capture and optimization is a complex process, requiring integrated, computer-aided tools in order to accomplish these tasks at different levels of representation. Finally, synthesis systems allow you to reduce project execution time. This project consists of developing the tools necessary to implement the steps of the synthesis process. The main objective is to incorporate these tools into an integrated synthesis system, which will allow the designer of a digital system to obtain the final design specification, in a given technology, from an initial high-level specification, which is independent of any technology.

**Embedded Cryptographic Systems:** The objective of this project is the implementation of an embedded cryptographic system, with application in smart cards, which has a low manufacturing cost, compared to a magnetic stripe card, but which offers greater security than this one. Therefore, we are going to investigate architectures that allow an area of implementation of the integrated circuit at a cost competitive with that of the magnetic stripe card. Let's focus our attention on the RSA cryptographic system and especially the modular exponentiation operation, which is the most time-consuming operation. In order to design high-speed hardware for RSA, one needs not only good and efficient modular arithmetic algorithms, but also a multiplier architecture that is optimized for these algorithms. For the purpose of prototyping, a Xilinx FPGA board will be used, already at our disposal, thanks to the university program that Xilinx offers free of charge. This platform supports an 8051 family microcontroller, RAM memory, FPGA and external interface logic. Through it, we will be able to try out different architectural solutions, starting from a software and hardware partitioning of the encryption algorithm, verifying the cost and performance of the implementation. We have already carried out a first experiment using the Barrett method, combined with the binary exponentiation method. At the end, we will be able to determine the algorithm that offers the best relationship between execution time and space required for a hardware implementation.

**Sistema Criptográfico Embutido com Aplicação em Smart Cards:** O objetivo deste projeto é a implementação de um sistema criptográfico embutido, com aplicação em smart cards, que tenha um custo de fabricação baixo, comparado a um cartão de fita magnética, e que ofereça maior segurança do que este. Assim sendo, vamos investigar arquitetura que permitam uma área de implementação do circuito integrado a um custo competitivo com o do cartão de fita magnética. Vamos concentrar nossa atenção no sistema criptográfico RSA e, principalmente, na operação de exponenciação modular, que é a operação que consome mais tempo. A fim de projetar um hardware de alta velocidade para RSA, precisa-se não somente de algoritmos de aritmética modular bons e eficientes, mas também de uma arquitetura do multiplicador que seja otimizada para estes algoritmos. Para efeito de prototipagem, utilizaremos uma FPGA Vertex II-Pro da Xilinx, contendo um processador embutido do tipo PowerPC. Através dela, poderemos experimentar diferentes soluções arquiteturais, a partir de um partitioning em software e hardware do algoritmo de criptografia, verificando o custo e o desempenho da implementação. Ao final, poderemos determinar o algoritmo que oferece a melhor relação entre tempo de execução e espaço requerido para uma implementação em smart card.

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## Articles Published in Periodicals

1. NEDJAH, N.; MOURELLE, Luiza de Macedo ; MORAIS, R. G. . Inspiration-wise swarm intelligence meta-heuristics for continuous optimisation: a survey - part III. International Journal of Bio-Inspired Computation, v. 17, p. 199-214, 2021.
2. NEDJAH, N.; RIBEIRO, L. M. ; MOURELLE, Luiza de Macedo . Communication Optimization for Efficient Dynamic Task Allocation in Swarm Robotics. APPLIED SOFT COMPUTING, v. 105, p. 107297, 2021.

3. CHAUDHARY, P. ; GUPTA, BRIJ B. ; CHANG, X. ; NEDJAH, N. ; CHUI, K. T. . Enhancing Big Data Security through integrating XSS Scanner into Fog nodes for SMEs Gain. TECHNOLOGICAL FORECASTING AND SOCIAL CHANGE, v. 168, p. 120754, 2021.
  4. NEDJAH, N.; CARDOSO, K. H. ; MOURELLE, Luiza de Macedo . An Efficient Distributed Approach for a Self-Healing Smart Grid Using Minimal Spanning Tree. International Journal of Energy Research, v. 45, p. 15049-15084, 2021.
  5. NEDJAH, N.; MOURELLE, Luiza de Macedo ; MORAIS, R. G. . Inspiration-wise swarm intelligence meta-heuristics for continuous optimisation: a survey - part I. International Journal of Bio-Inspired Computation, v. 15, p. 207-223, 2020.
  6. MORAIS, REINALDO G. ; Nedjah, Nadia ; MOURELLE, LUIZA M. . A novel metaheuristic inspired by Hitchcock birds' behavior for efficient optimization of large search spaces of high dimensionality. SOFT COMPUTING, v. 24, p. 5633-5655, 2020.
  7. NEDJAH, N.; LUIZA DE M. MOURELLE ; PEDRO JORGE DE ALBUQUERQUE DE OLIVEIRA . Simultaneous Localization and Mapping using Swarm Intelligence based Methods. EXPERT SYSTEMS WITH APPLICATIONS, v. 159, p. 113547, 2020.
  8. PINHEIRO, C. A. P. ; NEDJAH, N. ; MOURELLE, Luiza de Macedo . Detection and classification of pulmonary nodules using deep learning and swarm intelligence. MULTIMEDIA TOOLS AND APPLICATIONS, v. 79, p. 15437-15465, 2020.
  9. Nedjah, Nadia; MOURELLE, Luiza de Macedo ; MORAIS, REINALDO GOMES . Inspiration-wise swarm intelligence meta-heuristics for continuous optimisation: a survey - part II. International Journal of Bio-Inspired Computation, v. 16, p. 195-212, 2020.
  10. Nedjah, Nadia; WYANT, RAFAEL S. ; MOURELLE, LUIZA M. ; GUPTA, BRIJ B. . Efficient fingerprint matching on smart cards for high security and privacy in smart systems. INFORMATION SCIENCES, v. 479, p. 622-639, 2019.
  11. ARAUJO, R. A. ; NEDJAH, N. ; OLIVEIRA, A. L. I. ; MEIRA, S. R. L. . A Deep Increasing-Decreasing-Linear Neural Network for Financial Time Series Prediction. NEUROCOMPUTING, v. 347, p. 59-81, 2019.
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12. NEDJAH, N.; SILVA JR, L. . Review of methodologies and tasks in swarm robotics towards standardization. Swarm and Evolutionary Computation, v. 50, p. 100565, 2019.
  13. TAVARES, Y. M. ; NEDJAH, N. ; MOURELLE, Luiza de Macedo . Embedded implementation of template matching using correlation and particle swarm optimisation. International Journal of Bio-Inspired Computation, v. 11, p. 102-109, 2018.
  14. RAMOS, J. ; NEDJAH, N. ; MOURELLE, Luiza de Macedo ; GUPTA, B. B. . Visual data mining for crowd anomaly detection using artificial bacteria colony. MULTIMEDIA TOOLS AND APPLICATIONS, v. 77, p. 17755-17777, 2018.
  15. CARDOSO, ALEXANDRE DE VASCONCELOS ; TAVARES, YURI MARCHETTI ; Nedjah, Nadia ; MOURELLE, Luiza de Macedo . Co-Design System for Template Matching Using Dedicated Co-Processor and Cuckoo Search. INTERNATIONAL JOURNAL OF SWARM INTELLIGENCE RESEARCH (PRINT), v. 9, p. 58-74, 2018.
  16. NEDJAH, N.; BEZERRA, H. D. F. L. ; MOURELLE, Luiza de Macedo . Automatic generation of harmonious music using cellular automata based hardware design. INTEGRATION-THE VLSI JOURNAL, v. 62, p. 205-223, 2018.

17. ARAUJO, R. A. ; NEDJAH, N. ; SEIXAS, J. M. ; OLIVEIRA, A. L. I. ; MEIRA, S. R. L. . Evolutionary-morphological learning machines for high-frequency financial time series prediction. *Swarm and Evolutionary Computation*, v. 42, p. 1-15, 2018.
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19. CRUZ, NICOLÁS BULLA ; Nedjah, Nadia ; DE MACEDO MOURELLE, LUIZA . Robust distributed spatial clustering for swarm robotic based systems. *APPLIED SOFT COMPUTING*, v. 57, p. 727-737, 2017.
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21. SA, A. O. ; NEDJAH, N. ; MOURELLE, Luiza de Macedo . Distributed and resilient localization algorithm for Swarm Robotic Systems. *APPLIED SOFT COMPUTING*, v. 57, p. 738-750, 2017.
22. BONILLA, ALEJANDRA ; NEDJAH, N. ; MOURELLE, Luiza de Macedo . Online Phoneme Recognition using Multi-layer Perceptron Networks Combined with Recurrent Non-linear Autoregressive Neural Networks with Exogenous Inputs. *NEUROCOMPUTING*, v. 265, p. 78-90, 2017.
23. NEDJAH, N.; WYANT, R. S. ; MOURELLE, Luiza de Macedo . Efficient biometric palm-print matching on smart-cards for high security and privacy. *MULTIMEDIA TOOLS AND APPLICATIONS*, v. 76, p. 22671-22701, 2017.
24. TAVARES, Y. M. ; NEDJAH, N. ; DE MACEDO MOURELLE, LUIZA . Tracking Patterns with Particle Swarm Optimization and Genetic Algorithms. *International Journal of Swarm Intelligence Research*, v. 8, p. 34-49, 2017.
25. NEDJAH, N.; WYANT, R. S. ; MOURELLE, Luiza de Macedo ; GUPTA, B. B. . Efficient yet robust biometric iris matching on smart cards for data high security and privacy. *Future Generation Computer Systems*, v. 76, p. 18-32, 2017.
26. JUNIOR, LUNEQUE SILVA ; Nedjah, Nadia . Wave Algorithm Applied to Collective Navigation of Robotic Swarms. *APPLIED SOFT COMPUTING*, v. 57, p. 698-707, 2017.
27. NEDJAH, N.; CALAZAN, R. M. ; MOURELLE, Luiza de Macedo . Particle, Dimension and Cooperation-Oriented PSO Parallelization Strategies for Efficient High-Dimension Problem Optimizations on Graphics Processing Units. *Computer Journal (Print)*, v. 59, p. 810-835, 2016.
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29. MENDONCA, R. M. ; NEDJAH, N. ; MOURELLE, Luiza de Macedo . Efficient Distributed Algorithm of Dynamic Task Assignment for Swarm Robotics. *Neurocomputing (Amsterdam)*, v. 172, p. 345-355, 2016.
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34. Nedjah, Nadia; DE MACEDO MOURELLE, LUIZA ; WANG, CHAO . A Parallel Yet Pipelined Architecture for Efficient Implementation of the Advanced Encryption Standard Algorithm on Reconfigurable Hardware. *International Journal of Parallel Programming*, v. 44, p. 1102-1117, 2016.
35. FARIAS, MARCOS SANTANA ; Nedjah, Nadia ; MOURELLE, Luiza de Macedo ; DE CARVALHO, PAULO VICTOR RODRIGUES . Desenvolvimento de um equipamento portátil para identificação de radionuclídeos. *Brazilian Journal of Radiation Sciences*, v. 4, p. 14, 2016.
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