

CPU-Z TXT Report

Binaries

CPU-Z version 1.91.0.x32

Processors

Number of sockets 1
Number of threads 2

APICs

Socket 0

-- Core 0 (ID 0)
 -- Thread 00
-- Core 1 (ID 1)
 -- Thread 11

Timers

ACPI timer 3.580 MHz
Perf timer 14.318 MHz
Sys timer 1.000 KHz

Processors Information

Socket 1 ID = 0
Number of cores 2 (max 2)
Number of threads 2 (max 2)
Manufacturer GenuineIntel
Name Intel Mobile Core 2 Duo T5870
Codename Merom
Specification Intel(R) Core(TM)2 Duo CPU T5870 @
2.00GHz
Package (platform ID) Socket P (478) (0x7)
CPUID 6.F.D
Extended CPUID 6.F
Core Stepping M0
Technology 65 nm
Core Speed 1596.2 MHz
Multiplier x Bus Speed 8.0 x 199.5 MHz
Base frequency (cores) 199.5 MHz
Base frequency (ext.) 199.5 MHz
Rated Bus speed 798.1 MHz
Stock frequency 2000 MHz
Instructions sets MMX, SSE, SSE2, SSE3, SSSE3, EM64T
Microcode Revision 0xA3
L1 Data cache 2 x 32 KBytes, 8-way set associative, 64-byte
line size

```

L1 Instruction cache 2 x 32 KBytes, 8-way set associative, 64-byte
line size
L2 cache           2048 KBytes, 8-way set associative, 64-byte line
size
Max CPUID level    0000000Ah
Max CPUID ext. level 80000008h
Cache descriptor Level 1, D, 32 KB, 1 thread(s)
Cache descriptor Level 1, I, 32 KB, 1 thread(s)
Cache descriptor Level 2, U, 2 MB, 2 thread(s)
FID/VID Control    yes
FID range          6.0x - 10.0x
Max VID            1.175 V

```

```

Temperature 0      54 degC (129 degF) (Core #0)
Temperature 1      55 degC (130 degF) (Core #1)
Clock Speed 0     n.a. (Core #0)
Clock Speed 1     n.a. (Core #1)

```

Thread dumps

CPU Thread 0

```

APIC ID           0
Topology          Processor ID 0, Core ID 0, Thread ID 0

```

CPUID

```

0x00000000      0x0000000A 0x756E6547 0x6C65746E 0x49656E69
0x00000001      0x000006FD 0x00020800 0x0000E39D 0xBFEBFBFF
0x00000002      0x05B0B101 0x005657F0 0x00000000 0x2CB4307D
0x00000003      0x00000000 0x00000000 0x00000000 0x00000000
0x00000004      0x04000121 0x01C0003F 0x0000003F 0x00000001
0x00000004      0x04000122 0x01C0003F 0x0000003F 0x00000001
0x00000004      0x04004143 0x01C0003F 0x00000FFF 0x00000001
0x00000005      0x00000040 0x00000040 0x00000003 0x00022220
0x00000006      0x00000003 0x00000002 0x00000001 0x00000000
0x00000007      0x00000000 0x00000000 0x00000000 0x00000000
0x00000008      0x00000400 0x00000000 0x00000000 0x00000000
0x00000009      0x00000000 0x00000000 0x00000000 0x00000000
0x0000000A      0x07280202 0x00000000 0x00000000 0x00000503
0x80000000      0x80000008 0x00000000 0x00000000 0x00000000
0x80000001      0x00000000 0x00000000 0x00000001 0x20100000
0x80000002      0x65746E49 0x2952286C 0x726F4320 0x4D542865
0x80000003      0x44203229 0x43206F75 0x20205550 0x54202020
0x80000004      0x30373835 0x20402020 0x30302E32 0x007A4847
0x80000005      0x00000000 0x00000000 0x00000000 0x00000000
0x80000006      0x00000000 0x00000000 0x08006040 0x00000000
0x80000007      0x00000000 0x00000000 0x00000000 0x00000000
0x80000008      0x00003024 0x00000000 0x00000000 0x00000000

```

```

MSR 0x0000001B      0x00000000 0xFEE00900
MSR 0x00000017      0x001C0000 0x9A708A25
MSR 0x0000003F      0x00000000 0x00000000
MSR 0x000000CE      0x80170B2D 0x3B3B080F
MSR 0x000001A0      0x00000013 0x64972489
MSR 0x000000EE      0x00000000 0x9AB90400

```

MSR 0x0000011E	0x00000000	0x74702107
MSR 0x000000CD	0x00000000	0x000001A2
MSR 0x0000019C	0x00000000	0x88300100
MSR 0x00000198	0x06170A25	0x0600081E
MSR 0x00000199	0x00000000	0x0000081E

CPU Thread 1

APIC ID	1
Topology	Processor ID 0, Core ID 1, Thread ID 0

CPUID

0x00000000	0x0000000A	0x756E6547	0x6C65746E	0x49656E69
0x00000001	0x000006FD	0x01020800	0x0000E39D	0xBFEBFBFF
0x00000002	0x05B0B101	0x005657F0	0x00000000	0x2CB4307D
0x00000003	0x00000000	0x00000000	0x00000000	0x00000000
0x00000004	0x04000121	0x01C0003F	0x0000003F	0x00000001
0x00000004	0x04000122	0x01C0003F	0x0000003F	0x00000001
0x00000004	0x04004143	0x01C0003F	0x00000FFF	0x00000001
0x00000005	0x00000040	0x00000040	0x00000003	0x00022220
0x00000006	0x00000003	0x00000002	0x00000001	0x00000000
0x00000007	0x00000000	0x00000000	0x00000000	0x00000000
0x00000008	0x00000400	0x00000000	0x00000000	0x00000000
0x00000009	0x00000000	0x00000000	0x00000000	0x00000000
0x0000000A	0x07280202	0x00000000	0x00000000	0x00000503
0x80000000	0x80000008	0x00000000	0x00000000	0x00000000
0x80000001	0x00000000	0x00000000	0x00000001	0x20100000
0x80000002	0x65746E49	0x2952286C	0x726F4320	0x4D542865
0x80000003	0x44203229	0x43206F75	0x20205550	0x54202020
0x80000004	0x30373835	0x20402020	0x30302E32	0x007A4847
0x80000005	0x00000000	0x00000000	0x00000000	0x00000000
0x80000006	0x00000000	0x00000000	0x08006040	0x00000000
0x80000007	0x00000000	0x00000000	0x00000000	0x00000000
0x80000008	0x00003024	0x00000000	0x00000000	0x00000000

MSR 0x0000001B	0x00000000	0xFEE00800
MSR 0x00000017	0x001C0000	0x9A708A25
MSR 0x0000003F	0x00000000	0x00000000
MSR 0x000000CE	0x80170B2D	0x3B3B080F
MSR 0x000001A0	0x00000013	0x64972489
MSR 0x000000EE	0x00000000	0x9AB90400
MSR 0x0000011E	0x00000000	0x74702107
MSR 0x000000CD	0x00000000	0x000001A2
MSR 0x0000019C	0x00000000	0x88300100
MSR 0x00000198	0x06170A25	0x0600081E
MSR 0x00000199	0x00000000	0x0000081E

BIOS

UEFI	No
BIOS Vendor	American Megatrends Inc.
BIOS MSG	64-0100-000001-00101111-060509-Cantiga
BIOS Date	06/05/09
Mainboard Vendor	000001

Chipset

Northbridge Intel GL40 rev. 09
Southbridge Intel 82801IM (ICH9-M) rev. 03
Memory Type DDR2
Memory Size 4 GBytes
Channels Dual, (Symmetric)
Memory Frequency 399.0 MHz (1:2)
CAS# latency (CL) 6.0
RAS# to CAS# delay (tRCD) 6
RAS# Precharge (tRP) 6
Cycle Time (tRAS) 18
Host Bridge 0x2A40

Memory SPD

DIMM # 1
SMBus address 0x50
Memory type DDR2
Module format SO-DIMM
Module Manufacturer(ID) (000000000000000000000000)
Size 2048 MBytes
Max bandwidth PC2-6400 (400 MHz)
Part number GU332G0ALEPR8H2L6C
Number of banks 8
Data width 64 bits
Correction None
Nominal Voltage 1.80 Volts
EPP no
XMP no
AMP no
JEDEC timings table CL-tRCD-tRP-tRAS-tRC @ frequency
JEDEC #1 4.0-4-4-12-16 @ 266 MHz
JEDEC #2 5.0-5-5-15-20 @ 333 MHz
JEDEC #3 6.0-6-6-18-24 @ 400 MHz

DIMM # 2
SMBus address 0x52
Memory type DDR2
Module format SO-DIMM
Module Manufacturer(ID) (000000000000000000000000)
Size 2048 MBytes
Max bandwidth PC2-6400 (400 MHz)
Part number GU332G0ALEPR8H2L6C
Number of banks 8
Data width 64 bits
Correction None
Nominal Voltage 1.80 Volts
EPP no
XMP no
AMP no
JEDEC timings table CL-tRCD-tRP-tRAS-tRC @ frequency
JEDEC #1 4.0-4-4-12-16 @ 266 MHz
JEDEC #2 5.0-5-5-15-20 @ 333 MHz
JEDEC #3 6.0-6-6-18-24 @ 400 MHz

```

DIMM #                1
SPD registers
  00  01  02  03  04  05  06  07  08  09  0A  0B  0C  0D  0E  0F
00    80 08 08 0E 0A 61 40 00 05 25 40 00 82 08 00 00
10    0C 08 70 01 04 00 03 30 45 3D 50 3C 1E 3C 2D 01
20    17 25 05 12 3C 1E 1E 00 06 3C 7F 80 14 1E 00 00
30    00 00 00 00 00 00 00 00 00 00 00 00 00 00 12 DF
40    00 00 00 00 00 00 00 00 00 47 55 33 33 32 47 30
50    41 4C 45 50 52 38 48 32 4C 36 43 42 00 00 00 00
60    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

DIMM #                2
SPD registers
  00  01  02  03  04  05  06  07  08  09  0A  0B  0C  0D  0E  0F
00    80 08 08 0E 0A 61 40 00 05 25 40 00 82 08 00 00
10    0C 08 70 01 04 00 03 30 45 3D 50 3C 1E 3C 2D 01
20    17 25 05 12 3C 1E 1E 00 06 3C 7F 80 14 1E 00 00
30    00 00 00 00 00 00 00 00 00 00 00 00 00 00 12 DF
40    00 00 00 00 00 00 00 00 00 47 55 33 33 32 47 30
50    41 4C 45 50 52 38 48 32 4C 36 43 42 00 00 00 00
60    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

Monitoring

Mainboard Model X71Q (0x00000111 - 0x00002C68)

LPCIO

Hardware Monitors

Hardware monitor D3D

PCI Devices

Register space PCI Express, base address = 0x0E000000

Description Host Bridge
Location bus 0 (0x00), device 0 (0x00), function 0 (0x00)

Common header

Vendor ID 0x8086
Model ID 0x2A40
Revision ID 0x09
PI 0x00
SubClass 0x00
BaseClass 0x06
Cache Line 0x00
Latency 0x00
Header 0x00

PCI header

Subvendor ID 0x1043
Subsystem ID 0x11C7
Int. Line 0x00
Int. Pin 0x00

PCI capability

Caps class Vendor Dependant
Caps offset 0xE0

PCI registers

	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
00	86	80	40	2A	06	00	90	20	09	00	00	06	00	00	00	00
10	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
20	00	00	00	00	00	00	00	00	00	00	00	00	43	10	C7	11
30	00	00	00	00	E0	00	00	00	00	00	00	00	00	00	00	00
40	01	90	D1	FE	00	00	00	00	01	00	D1	FE	00	00	00	00
50	00	00	50	03	19	00	00	00	00	00	00	00	00	00	00	00
60	01	00	00	E0	00	00	00	00	01	80	D1	FE	00	00	00	00
70	00	00	00	00	00	00	00	00	01	08	00	00	00	00	00	00
80	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
90	10	11	11	00	00	33	33	00	40	00	4F	00	00	1A	38	00
A0	20	00	00	14	00	00	00	00	00	00	00	00	00	00	00	00
B0	00	C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00
C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
D0	00	00	00	00	00	00	00	00	00	00	00	00	70	02	00	00
E0	09	00	0A	61	B1	6D	51	1F	B9	E1	00	00	00	00	00	00
F0	00	00	00	00	00	00	00	00	A0	0F	09	00	00	00	00	00

Description VGA Controller
Location bus 0 (0x00), device 2 (0x02), function 0 (0x00)

Common header

Vendor ID 0x8086
Model ID 0x2A42
Revision ID 0x09
PI 0x00
SubClass 0x00
BaseClass 0x03
Cache Line 0x00
Latency 0x00
Header 0x80

PCI header

Address 0 (memory) 0xFD800000
Address 2 (memory) 0xD0000000
Address 4 (port) 0x0000CC00
Subvendor ID 0x1043

```

Subsystem ID          0x1862
Int. Line             0x00
Int. Pin              0x01
PCI capability
  Caps class          Message Signalled Interrupts
  Caps offset         0x90
PCI capability
  Caps class          Power Management
  Caps offset         0xD0
  Caps version        1.2

```

```

PCI registers
  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  86 80 42 2A 07 04 90 00 09 00 00 03 00 00 80 00
10  04 00 80 FD 00 00 00 00 0C 00 00 D0 00 00 00 00
20  01 CC 00 00 00 00 00 00 00 00 00 00 43 10 62 18
30  00 00 00 00 90 00 00 00 00 00 00 00 00 01 00 00
40  00 00 00 00 48 00 00 00 09 00 0A 61 B1 6D 51 1F
50  B9 E1 50 03 19 00 00 00 00 00 00 00 00 00 00 BE
60  00 00 00 00 00 00 02 00 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90  05 D0 01 00 0C 30 E0 FE 70 49 00 00 00 00 00 00
A0  00 00 00 00 09 00 06 20 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 00 00 00 00 00 00 00 00 00 00 00 40 01 00 00
D0  01 00 23 00 00 00 00 00 00 00 00 00 34 00 00 00
E0  00 00 00 00 00 00 00 00 00 80 00 1A 41 20 00 00
F0  0B 19 34 07 CF 00 00 00 A0 0F 09 00 E4 F0 D8 BD

```

```

Description          Display Controller
Location             bus 0 (0x00), device 2 (0x02), function 1 (0x01)

```

```

Common header
  Vendor ID          0x8086
  Model ID           0x2A43
  Revision ID        0x09
  PI                 0x00
  SubClass           0x80
  BaseClass          0x03
  Cache Line         0x00
  Latency            0x00
  Header             0x80

```

```

PCI header
  Address 0 (memory) 0xFDD00000
  Subvendor ID       0x1043
  Subsystem ID       0x1862
  Int. Line          0x00
  Int. Pin           0x00

```

```

PCI capability
  Caps class          Power Management
  Caps offset         0xD0
  Caps version        1.2

```

```

PCI registers
  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  86 80 43 2A 06 00 90 00 09 00 80 03 00 00 80 00
10  04 00 D0 FD 00 00 00 00 00 00 00 00 00 00 00 00
20  00 00 00 00 00 00 00 00 00 00 00 43 10 62 18
30  00 00 00 00 D0 00 00 00 00 00 00 00 00 00 00 00
40  00 00 00 00 48 00 00 00 09 00 0A 61 B1 6D 51 1F

```

```

50 B9 E1 50 03 19 00 00 00 00 00 00 00 00 00 BE
60 00 00 00 00 00 00 02 00 00 00 00 00 00 00
70 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0 00 00 00 00 00 00 00 00 00 00 00 40 01 00
D0 01 00 23 00 00 00 00 00 00 00 00 34 00 00
E0 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0 0B 19 34 07 CF 00 00 00 A0 0F 09 00 00 00 00

```

```

Description          USB Controller (UHCI)
Location             bus 0 (0x00), device 26 (0x1A), function 0 (0x00)

```

```

Common header
Vendor ID           0x8086
Model ID            0x2937
Revision ID         0x03
PI                  0x00
SubClass            0x03
BaseClass           0x0C
Cache Line          0x00
Latency             0x00
Header              0x80

```

```

PCI header
Address 4 (port)   0x0000C880
Subvendor ID       0x1043
Subsystem ID       0x11C7
Int. Line          0x10
Int. Pin           0x01

```

```

PCI capability
Caps class         0x13
Caps offset        0x50

```

```

PCI registers
00 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
10 86 80 37 29 05 00 90 02 03 00 03 0C 00 00 80 00
20 00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30 81 C8 00 00 00 00 00 00 00 00 00 00 10 01 00 00
40 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50 13 00 06 03 00 00 00 00 00 00 00 00 00 00 00 00
60 10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0 00 2F 00 00 00 00 00 00 00 00 01 00 00 00 00 00
D0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0 00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00

```

```

Description          USB Controller (UHCI)
Location             bus 0 (0x00), device 26 (0x1A), function 1 (0x01)

```

```

Common header
Vendor ID           0x8086
Model ID            0x2938
Revision ID         0x03

```



```

    PI                0x00
    SubClass          0x03
    BaseClass         0x0C
    Cache Line        0x00
    Latency            0x00
    Header             0x00
PCI header
  Address 4 (port) 0x0000C800
  Subvendor ID     0x1043
  Subsystem ID     0x11C7
  Int. Line        0x15
  Int. Pin         0x02
PCI capability
  Caps class       0x13
  Caps offset      0x50
PCI registers
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   86 80 38 29 05 00 90 02 03 00 03 0C 00 00 00 00
10   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   01 C8 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30   00 00 00 00 50 00 00 00 00 00 00 00 15 02 00 00
40   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50   13 00 06 03 00 00 00 00 00 00 00 00 00 00 00 00
60   10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 2F 00 00 00 00 00 00 00 00 01 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00

```

```

Description          USB Controller (UHCI)
Location              bus 0 (0x00), device 26 (0x1A), function 2 (0x02)

```

```

Common header
  Vendor ID          0x8086
  Model ID           0x2939
  Revision ID        0x03
  PI                 0x00
  SubClass           0x03
  BaseClass          0x0C
  Cache Line         0x00
  Latency            0x00
  Header             0x00

```

```

PCI header
  Address 4 (port) 0x0000C480
  Subvendor ID     0x1043
  Subsystem ID     0x11C7
  Int. Line        0x13
  Int. Pin         0x04

```

```

PCI capability
  Caps class       0x13
  Caps offset      0x50

```

```

PCI registers
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   86 80 39 29 05 00 90 02 03 00 03 0C 00 00 00 00

```

```

10  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20  81 C4 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 50 00 00 00 00 00 00 00 13 04 00 00
40  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50  13 00 06 03 00 00 00 00 00 00 00 00 00 00 00 00
60  10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 2F 00 00 00 00 00 00 00 00 00 01 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00 00

```

```

Description          USB 2.0 Controller (EHCI)
Location             bus 0 (0x00), device 26 (0x1A), function 7 (0x07)

```

Common header

```

Vendor ID           0x8086
Model ID            0x293C
Revision ID         0x03
PI                  0x20
SubClass            0x03
BaseClass           0x0C
Cache Line          0x00
Latency             0x00
Header              0x00

```

PCI header

```

Address 0 (memory)  0xFDEFBC00
Subvendor ID        0x1043
Subsystem ID        0x11C7
Int. Line           0x12
Int. Pin            0x03

```

PCI capability

```

Caps class          Power Management
Caps offset         0x50
Caps version        1.1

```

PCI capability

```

Caps class          Debug Port
Caps offset         0x58

```

PCI capability

```

Caps class          0x13
Caps offset         0x98

```

PCI registers

```

00  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
10  86 80 3C 29 06 00 90 02 03 20 03 0C 00 00 00 00
20  00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 50 00 00 00 00 00 00 00 12 03 00 00
40  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50  01 58 C2 C9 00 00 00 00 0A 98 A0 20 00 00 00 00
60  20 20 FF 01 00 00 00 00 01 00 00 00 00 20 00 C0
70  00 00 CF 0F 00 00 00 00 00 00 00 00 00 00 00 00
80  00 00 00 00 01 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 13 00 06 03 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

C0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0 00 00 00 00 00 AA FF 00 00 00 00 00 00 00 00
E0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0 00 00 00 00 88 85 40 00 86 0F 04 00 0A 17 02 20

```

```

Description      Multimedia device
Location         bus 0 (0x00), device 27 (0x1B), function 0 (0x00)

```

Common header

```

Vendor ID       0x8086
Model ID        0x293E
Revision ID     0x03
PI              0x00
SubClass        0x03
BaseClass       0x04
Cache Line     0x08
Latency         0x00
Header          0x00

```

PCI header

```

Address 0 (memory) 0xFDEF4000
Subvendor ID       0x1043
Subsystem ID       0x1833
Int. Line          0x16
Int. Pin           0x01

```

PCI capability

```

Caps class      Power Management
Caps offset     0x50
Caps version    1.1

```

PCI capability

```

Caps class      Message Signalled Interrupts
Caps offset     0x60

```

PCI capability

```

Caps class      PCI Express
Caps offset     0x70
Device type     Root Complex Integrated Endpoint Device
Port            0
Version        1.0
Link width     0x (max 0x)

```

Extended capabilities

```

Caps class      Virtual Channel
Caps offset     0x100
Caps class      Root Complex Link Declaration
Caps offset     0x130
Link Entries # 1
Port Number     15

```

PCI registers

```

00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00 86 80 3E 29 06 00 10 00 03 00 03 04 08 00 00 00
10 04 40 EF FD 00 00 00 00 00 00 00 00 00 00 00 00
20 00 00 00 00 00 00 00 00 00 00 00 00 43 10 33 18
30 00 00 00 00 50 00 00 00 00 00 00 00 16 01 00 00
40 01 00 00 07 07 00 00 00 00 00 00 00 00 00 00 00
50 01 60 42 C8 00 00 00 00 00 00 00 00 00 00 00 00
60 05 70 80 00 00 00 00 00 00 00 00 00 00 00 00 00
70 10 00 91 00 00 00 00 10 00 00 10 00 00 00 00 00
80 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

C0  00 04 00 01 00 00 00 00 31 00 A3 02 00 00 00 00
D0  61 00 A3 02 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00
100 02 00 01 13 01 00 00 00 00 00 00 00 00 00 00 00
110 00 00 00 00 01 00 00 80 00 00 00 00 00 00 00 00
120 80 00 00 01 00 00 00 00 00 00 00 00 00 00 00 00
130 05 00 01 00 00 01 00 0F 00 00 00 00 00 00 00 00

```

```

Description          PCI to PCI Bridge
Location             bus 0 (0x00), device 28 (0x1C), function 0 (0x00)
Common header

```

```

Vendor ID           0x8086
Model ID            0x2940
Revision ID         0x03
PI                  0x00
SubClass             0x04
BaseClass           0x06
Cache Line          0x08
Latency              0x00
Header               0x81

```

```

PCI header
Primary bus          0x00
Secondary bus        0x01
Int. Line            0x00
Int. Pin             0x01

```

```

PCI capability
Caps class           PCI Express
Caps offset          0x40
Device type          Root Port of PCI-E Root Complex
Port                 1
Version              1.0
Physical slot         Integrated device
Link width           0x (max 1x)

```

```

PCI capability
Caps class           Message Signalled Interrupts
Caps offset          0x80

```

```

PCI capability
Caps class           Subsystem Vendor
Caps offset          0x90
SubVendor ID         0x1043
SubSystem ID         0x11C7

```

```

PCI capability
Caps class           Power Management
Caps offset          0xA0
Caps version         1.1

```

```

Extended capabilities
Caps class           Virtual Channel
Caps offset          0x100
Caps class           Root Complex Link Declaration
Caps offset          0x180
Link Entries #       1
Port Number          1

```

```

PCI registers
00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00 86 80 40 29 04 05 10 00 03 00 04 06 08 00 81 00
10 00 00 00 00 00 00 00 00 00 01 01 00 F0 00 00 20
20 F0 FF 00 00 F1 FF 01 00 00 00 00 00 00 00 00 00

```

```

30  00 00 00 00 40 00 00 00 00 00 00 00 00 01 02 00
40  10 80 41 00 00 80 00 00 00 00 10 00 11 2C 11 01
50  40 00 01 10 60 05 00 00 00 00 40 00 08 00 00 00
60  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  05 90 01 00 0C 30 E0 FE 80 49 00 00 00 00 00 00
90  0D A0 00 00 43 10 C7 11 00 00 00 00 00 00 00 00
A0  01 00 02 C8 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 80 00 11 08 00 00 00 00
E0  00 0F C7 00 06 07 08 00 30 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00
100 02 00 01 18 00 00 00 00 01 00 00 00 00 00 00 00
110 01 00 00 00 01 00 00 80 00 00 00 00 00 00 00 00
120 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
130 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

Description          PCI to PCI Bridge
Location             bus 0 (0x00), device 28 (0x1C), function 1 (0x01)
Common header
  Vendor ID          0x8086
  Model ID           0x2942
  Revision ID        0x03
  PI                 0x00
  SubClass           0x04
  BaseClass          0x06
  Cache Line         0x08
  Latency            0x00
  Header             0x81
PCI header
  Primary bus        0x00
  Secondary bus      0x02
  Int. Line          0x00
  Int. Pin           0x02
PCI capability
  Caps class         PCI Express
  Caps offset        0x40
  Device type        Root Port of PCI-E Root Complex
  Port               2
  Version            1.0
  Physical slot      Integrated device
  Link width         1x (max 1x)
PCI capability
  Caps class         Message Signalled Interrupts
  Caps offset        0x80
PCI capability
  Caps class         Subsystem Vendor
  Caps offset        0x90
  SubVendor ID       0x1043
  SubSystem ID       0x11C7
PCI capability
  Caps class         Power Management
  Caps offset        0xA0
  Caps version       1.1
Extended capabilities
  Caps class         Virtual Channel
  Caps offset        0x100

```

```

Caps class      Root Complex Link Declaration
Caps offset    0x180
Link Entries # 1
Port Number    2

```

PCI registers

```

      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00    86 80 42 29 06 05 10 00 03 00 04 06 08 00 81 00
10    00 00 00 00 00 00 00 00 00 02 02 00 F0 00 00 20
20    F0 FD F0 FD F1 FF 01 00 00 00 00 00 00 00 00 00
30    00 00 00 00 40 00 00 00 00 00 00 00 00 02 02 00
40    10 80 41 00 00 80 00 00 00 00 10 00 11 2C 11 02
50    42 00 11 30 60 05 00 00 00 00 48 01 08 00 00 00
60    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80    05 90 01 00 0C 30 E0 FE B0 49 00 00 00 00 00 00
90    0D A0 00 00 43 10 C7 11 00 00 00 00 00 00 00 00
A0    01 00 02 C8 00 00 00 00 00 00 00 00 00 00 00 00
B0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0    00 00 00 00 00 00 00 00 80 00 11 08 00 00 00 00
E0    00 0F C7 00 06 07 08 00 30 00 00 00 00 00 00 00
F0    00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00
100   02 00 01 18 00 00 00 00 01 00 00 00 00 00 00 00
110   01 00 00 00 01 00 00 80 00 00 00 00 00 00 00 00
120   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
130   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

Description    PCI to PCI Bridge
Location       bus 0 (0x00), device 28 (0x1C), function 2 (0x02)

```

Common header

```

Vendor ID      0x8086
Model ID      0x2944
Revision ID    0x03
PI            0x00
SubClass      0x04
BaseClass     0x06
Cache Line    0x08
Latency       0x00
Header        0x81

```

PCI header

```

Primary bus    0x00
Secondary bus  0x03
Int. Line     0x00
Int. Pin      0x03

```

PCI capability

```

Caps class     PCI Express
Caps offset    0x40
Device type    Root Port of PCI-E Root Complex
Port          3
Version       1.0
Physical slot  #0
Presence detect no
Link width     0x (max 1x)

```

PCI capability

```

Caps class     Message Signalled Interrupts
Caps offset    0x80

```

PCI capability

```

Caps class     Subsystem Vendor

```

```

Caps offset      0x90
SubVendor ID    0x1043
SubSystem ID    0x11C7
PCI capability
Caps class      Power Management
Caps offset     0xA0
Caps version    1.1
Extended capabilities
Caps class      Virtual Channel
Caps offset     0x100
Caps class      Root Complex Link Declaration
Caps offset     0x180
Link Entries #  1
Port Number     3

```

```

PCI registers
00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00 86 80 44 29 07 05 10 00 03 00 04 06 08 00 81 00
10 00 00 00 00 00 00 00 00 00 03 04 00 D0 D0 00 20
20 00 FE 90 FE 01 FA E1 FC 00 00 00 00 00 00 00 00
30 00 00 00 00 40 00 00 00 00 00 00 00 00 03 02 00
40 10 80 41 01 00 80 00 00 00 00 10 00 11 2C 11 03
50 40 00 01 10 60 05 00 00 3A 10 00 00 08 00 00 00
60 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80 05 90 01 00 0C 30 E0 FE A0 49 00 00 00 00 00 00
90 0D A0 00 00 43 10 C7 11 00 00 00 00 00 00 00 00
A0 01 00 02 C8 00 00 00 00 00 00 00 00 00 00 00 00
B0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0 00 00 00 00 00 00 00 00 80 00 11 08 00 00 00 00
E0 00 0F C7 00 06 07 08 00 30 00 00 00 00 00 00 00
F0 00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00
100 02 00 01 18 00 00 00 00 01 00 00 00 00 00 00 00
110 01 00 00 00 01 00 00 80 00 00 00 00 00 00 00 00
120 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
130 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

Description      PCI to PCI Bridge
Location         bus 0 (0x00), device 28 (0x1C), function 5 (0x05)
Common header
Vendor ID        0x8086
Model ID         0x294A
Revision ID      0x03
PI               0x00
SubClass         0x04
BaseClass        0x06
Cache Line       0x08
Latency          0x00
Header           0x81
PCI header
Primary bus      0x00
Secondary bus    0x05
Int. Line        0x00
Int. Pin         0x02
PCI capability
Caps class       PCI Express
Caps offset      0x40
Device type      Root Port of PCI-E Root Complex

```

```

Port          6
Version       1.0
Physical slot  Integrated device
Link width    1x (max 1x)
PCI capability
Caps class    Message Signalled Interrupts
Caps offset   0x80
PCI capability
Caps class    Subsystem Vendor
Caps offset   0x90
SubVendor ID  0x1043
SubSystem ID  0x11C7
PCI capability
Caps class    Power Management
Caps offset   0xA0
Caps version  1.1
Extended capabilities
Caps class    Virtual Channel
Caps offset   0x100
Caps class    Root Complex Link Declaration
Caps offset   0x180
Link Entries # 1
Port Number   6

```

PCI registers

```

00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00 86 80 4A 29 07 05 10 00 03 00 04 06 08 00 81 00
10 00 00 00 00 00 00 00 00 00 05 05 00 E0 E0 00 00
20 A0 FE A0 FE F1 FC F1 FC 00 00 00 00 00 00 00 00
30 00 00 00 00 40 00 00 00 00 00 00 00 00 02 02 00
40 10 80 41 00 00 80 00 00 00 00 11 00 11 2C 11 06
50 41 00 11 30 60 05 00 00 00 00 48 01 08 00 00 00
60 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80 05 90 01 00 0C 30 E0 FE 90 49 00 00 00 00 00 00
90 0D A0 00 00 43 10 C7 11 00 00 00 00 00 00 00 00
A0 01 00 02 C8 00 00 00 00 00 00 00 00 00 00 00 00
B0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0 00 00 00 00 00 00 00 00 80 00 11 08 00 00 00 00
E0 00 0F C7 00 06 07 08 00 30 00 00 00 00 00 00 00
F0 00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00
100 02 00 01 18 00 00 00 00 01 00 00 00 00 00 00 00
110 01 00 00 00 01 00 00 80 00 00 00 00 00 00 00 00
120 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
130 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

Description    USB Controller (UHCI)
Location       bus 0 (0x00), device 29 (0x1D), function 0 (0x00)
Common header
Vendor ID      0x8086
Model ID       0x2934
Revision ID    0x03
PI             0x00
SubClass       0x03
BaseClass      0x0C
Cache Line     0x00
Latency        0x00
Header         0x80

```



```

PCI header
  Address 4 (port) 0x0000C400
  Subvendor ID      0x1043
  Subsystem ID     0x11C7
  Int. Line        0x17
  Int. Pin         0x01

```

```

PCI capability
  Caps class       0x13
  Caps offset      0x50

```

```

PCI registers
  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  86 80 34 29 05 00 90 02 03 00 03 0C 00 00 80 00
10  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20  01 C4 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 50 00 00 00 00 00 00 00 17 01 00 00
40  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50  13 00 06 03 00 00 00 00 00 00 00 00 00 00 00 00
60  10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 2F 00 00 00 00 00 00 00 00 01 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00

```

```

Description      USB Controller (UHCI)
Location         bus 0 (0x00), device 29 (0x1D), function 1 (0x01)

```

```

Common header
  Vendor ID       0x8086
  Model ID        0x2935
  Revision ID     0x03
  PI              0x00
  SubClass        0x03
  BaseClass       0x0C
  Cache Line      0x00
  Latency         0x00
  Header          0x00

```

```

PCI header
  Address 4 (port) 0x0000C080
  Subvendor ID      0x1043
  Subsystem ID     0x11C7
  Int. Line        0x13
  Int. Pin         0x02

```

```

PCI capability
  Caps class       0x13
  Caps offset      0x50

```

```

PCI registers
  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  86 80 35 29 05 00 90 02 03 00 03 0C 00 00 00 00
10  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20  81 C0 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 50 00 00 00 00 00 00 00 13 02 00 00
40  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50  13 00 06 03 00 00 00 00 00 00 00 00 00 00 00 00
60  10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 2F 00 00 00 00 00 00 00 00 01 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00

```

```

Description          USB Controller (UHCI)
Location             bus 0 (0x00), device 29 (0x1D), function 2 (0x02)

```

```

Common header
Vendor ID           0x8086
Model ID           0x2936
Revision ID        0x03
PI                 0x00
SubClass           0x03
BaseClass          0x0C
Cache Line         0x00
Latency            0x00
Header             0x00

```

```

PCI header
Address 4 (port)   0x0000C000
Subvendor ID       0x1043
Subsystem ID       0x11C7
Int. Line          0x12
Int. Pin           0x03

```

```

PCI capability
Caps class         0x13
Caps offset        0x50

```

```

PCI registers
00  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  86 80 36 29 05 00 90 02 03 00 03 0C 00 00 00 00
10  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20  01 C0 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 50 00 00 00 00 00 00 00 12 03 00 00
40  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50  13 00 06 03 00 00 00 00 00 00 00 00 00 00 00 00
60  10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 2F 00 00 00 00 00 00 00 00 01 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00

```

```

Description          USB 2.0 Controller (EHCI)
Location             bus 0 (0x00), device 29 (0x1D), function 7 (0x07)

```

```

Common header
Vendor ID           0x8086
Model ID           0x293A
Revision ID        0x03
PI                 0x20
SubClass           0x03

```

```

    BaseClass          0x0C
    Cache Line         0x00
    Latency             0x00
    Header              0x00
PCI header
    Address 0 (memory) 0xFDEFB800
    Subvendor ID       0x1043
    Subsystem ID       0x11C7
    Int. Line          0x17
    Int. Pin           0x01
PCI capability
    Caps class         Power Management
    Caps offset        0x50
    Caps version       1.1
PCI capability
    Caps class         Debug Port
    Caps offset        0x58
PCI capability
    Caps class         0x13
    Caps offset        0x98

```

```

PCI registers
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  86 80 3A 29 06 01 90 02 03 20 03 0C 00 00 00 00
10  00 B8 EF FD 00 00 00 00 00 00 00 00 00 00 00 00
20  00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 50 00 00 00 00 00 00 00 17 01 00 00
40  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50  01 58 C2 C9 00 80 00 00 0A 98 A0 20 00 00 00 00
60  20 20 FF 01 00 00 00 00 01 00 00 00 00 20 00 C0
70  00 00 DF 0F 00 00 00 00 00 00 00 00 00 00 00 00
80  00 00 00 00 01 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 13 00 06 03 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 AA FF 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 88 85 40 00 86 0F 04 00 0A 17 02 20

```

```

Description          PCI to PCI Bridge
Location             bus 0 (0x00), device 30 (0x1E), function 0 (0x00)
Common header
    Vendor ID         0x8086
    Model ID          0x2448
    Revision ID       0x93
    PI                 0x01
    SubClass           0x04
    BaseClass         0x06
    Cache Line        0x00
    Latency            0x00
    Header             0x01
PCI header
    Primary bus       0x00
    Secondary bus     0x06
    Int. Line         0xFF
    Int. Pin          0x00
PCI capability
    Caps class        Subsystem Vendor

```

```

Caps offset      0x50
SubVendor ID    0x1043
SubSystem ID    0x11C7

```

PCI registers

```

      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00    86 80 48 24 07 01 10 00 93 01 04 06 00 00 01 00
10    00 00 00 00 00 00 00 00 00 06 06 20 F0 00 80 22
20    B0 FE B0 FE F1 FF 01 00 00 00 00 00 00 00 00 00
30    00 00 00 00 50 00 00 00 00 00 00 00 FF 00 02 00
40    00 00 00 00 00 00 00 00 00 00 00 00 00 12 00 00
50    0D 00 00 00 43 10 C7 11 00 00 00 00 00 00 00 00
60    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0    00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00

```

```

Description      PCI to ISA Bridge
Location         bus 0 (0x00), device 31 (0x1F), function 0 (0x00)

```

Common header

```

Vendor ID       0x8086
Model ID        0x2919
Revision ID     0x03
PI              0x00
SubClass        0x01
BaseClass       0x06
Cache Line      0x00
Latency         0x00
Header          0x80

```

PCI header

```

Subvendor ID    0x1043
Subsystem ID    0x11C7
Int. Line       0x00
Int. Pin        0x00

```

PCI capability

```

Caps class      Vendor Dependant
Caps offset     0xE0

```

PCI registers

```

      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00    86 80 19 29 07 00 10 02 03 00 01 06 00 00 80 00
10    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20    00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30    00 00 00 00 E0 00 00 00 00 00 00 00 00 00 00 00
40    01 08 00 00 80 00 00 00 01 05 00 00 10 00 00 00
50    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60    8A 85 86 83 D0 00 00 00 80 87 84 8A F8 00 00 00
70    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80    00 00 02 0C 00 00 00 00 00 00 00 00 51 02 0C 00
90    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0    BC 0A 00 00 39 00 80 00 01 1C 0A 00 00 03 00 C0
B0    00 00 F0 00 00 00 00 00 00 00 81 00 00 00 00 00
C0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0    33 22 11 00 67 45 00 00 C0 C0 00 00 00 00 00 00

```

```

E0 09 00 0C 10 20 02 24 03 64 00 00 00 00 00 00
F0 01 C0 D1 FE 00 00 00 00 86 0F 04 00 00 00 00

```

```

Description      Serial ATA Controller
Location         bus 0 (0x00), device 31 (0x1F), function 2 (0x02)

```

```

Common header
  Vendor ID      0x8086
  Model ID       0x2929
  Revision ID    0x03
  PI             0x01
  SubClass       0x06
  BaseClass      0x01
  Cache Line     0x00
  Latency        0x00
  Header         0x00

```

```

PCI header
  Address 0 (port) 0x0000BC00
  Address 1 (port) 0x0000B880
  Address 2 (port) 0x0000B800
  Address 3 (port) 0x0000B480
  Address 4 (port) 0x0000B400
  Address 5 (memory) 0xFDEFB000
  Subvendor ID    0x1043
  Subsystem ID   0x11C7
  Int. Line       0x13
  Int. Pin        0x02

```

```

PCI capability
  Caps class      Message Signalled Interrupts
  Caps offset     0x80

```

```

PCI capability
  Caps class      Power Management
  Caps offset     0x70
  Caps version    1.2

```

```

PCI capability
  Caps class      0x12
  Caps offset     0xA8

```

```

PCI capability
  Caps class      0x13
  Caps offset     0xB0

```

```

PCI registers
  00 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
  00 86 80 29 29 07 00 B0 02 03 01 06 01 00 00 00 00
  10 01 BC 00 00 81 B8 00 00 01 B8 00 00 81 B4 00 00
  20 01 B4 00 00 00 B0 EF FD 00 00 00 00 43 10 C7 11
  30 00 00 00 00 80 00 00 00 00 00 00 00 13 02 00 00
  40 00 80 00 80 00 00 00 00 00 00 00 00 00 00 00 00
  50 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
  60 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
  70 01 A8 03 40 08 00 00 00 00 00 00 00 00 00 00 00
  80 05 70 08 00 00 00 00 00 00 00 00 00 00 00 00 00
  90 60 00 33 83 93 01 00 1C 00 00 00 00 0C 00 00 00
  A0 00 00 00 00 00 00 00 00 00 12 B0 10 00 48 00 00 00
  B0 13 00 06 03 00 00 00 00 00 00 00 00 00 00 00 00
  C0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
  D0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
  E0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
  F0 00 00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00

```



```

E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
100 01 00 01 14 00 00 10 00 00 00 10 00 11 20 06 00
110 00 00 00 00 00 00 00 00 00 B4 00 00 00 01 00 00 04
120 0F 0D 00 00 08 00 07 02 00 00 00 00 00 00 00 00
130 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

Description      Ethernet Controller
Location         bus 5 (0x05), device 0 (0x00), function 0 (0x00)
Common header

```

```

Vendor ID       0x10EC
Model ID        0x8168
Revision ID     0x02
PI              0x00
SubClass        0x00
BaseClass       0x02
Cache Line      0x08
Latency         0x00
Header          0x00

```

PCI header

```

Address 0 (port) 0x0000E800
Address 2 (memory) 0xFCFFF000
Address 4 (memory) 0xFCFE0000
Subvendor ID     0x1043
Subsystem ID     0x16D5
Int. Line        0x11
Int. Pin         0x01

```

PCI capability

```

Caps class      Power Management
Caps offset     0x40
Caps version    1.2

```

PCI capability

```

Caps class      Message Signalled Interrupts
Caps offset     0x50

```

PCI capability

```

Caps class      PCI Express
Caps offset     0x70
Device type     PCI-E Endpoint Device
Port            0
Version         1.0
Link width      1x (max 1x)

```

PCI capability

```

Caps class      MSI-X
Caps offset     0xB0

```

PCI capability

```

Caps class      Virtual Product Data
Caps offset     0xD0

```

Extended capabilities

```

Caps class      Advanced Error Reporting
Caps offset     0x100
Caps class      Virtual Channel
Caps offset     0x140
Caps class      Device Serial Number
Caps offset     0x160

```

PCI registers

```

      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00    EC 10 68 81 07 00 10 00 02 00 00 02 08 00 00 00
10    01 E8 00 00 00 00 00 00 0C F0 FF FC 00 00 00 00

```

```

20  0C 00 FE FC 00 00 00 00 00 00 00 00 43 10 D5 16
30  00 00 00 00 40 00 00 00 00 00 00 00 11 01 00 00
40  01 50 C3 FF 08 00 00 00 00 00 00 00 00 00 00 00
50  05 70 82 00 00 00 00 00 00 00 00 00 00 00 00
60  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70  10 B0 01 02 C1 86 28 00 10 50 11 00 11 3C 07 00
80  41 00 11 10 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 10 00 00 00 10 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  11 D0 01 00 04 00 00 00 04 08 00 00 00 00 00
C0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0  03 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
100 01 00 01 14 00 00 00 00 00 00 00 30 20 06 00
110 01 10 00 00 00 00 00 00 A0 00 00 00 00 00 00
120 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
130 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

Description      OHCI FireWire Controller
Location         bus 6 (0x06), device 1 (0x01), function 0 (0x00)

```

Common header

```

Vendor ID       0x1180
Model ID        0x0832
Revision ID     0x05
PI              0x10
SubClass        0x00
BaseClass       0x0C
Cache Line      0x08
Latency         0x20
Header          0x80

```

PCI header

```

Address 0 (memory) 0xFEBFF800
Subvendor ID       0x1043
Subsystem ID       0x11C7
Int. Line          0x10
Int. Pin           0x01

```

PCI capability

```

Caps class        Power Management
Caps offset       0xDC
Caps version      1.1

```

PCI registers

```

00  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
10  80 11 32 08 06 00 10 02 05 10 00 0C 08 20 80 00
20  00 F8 BF FE 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 DC 00 00 00 00 00 00 10 01 02 04
40  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  00 80 A0 16 00 00 00 00 00 20 00 00 66 66 32 12
90  48 60 66 10 00 00 02 00 0F 80 00 00 00 01 18 00
A0  00 00 00 00 00 00 00 00 30 00 00 00 43 10 C7 11
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 02 04
C0  00 30 00 00 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 01 00 02 C8
E0  00 C0 00 48 00 00 00 00 00 00 00 00 00 00 00 00

```


F0 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Description System Device
Location bus 6 (0x06), device 1 (0x01), function 1 (0x01)

Common header
Vendor ID 0x1180
Model ID 0x0822
Revision ID 0x22
PI 0x00
SubClass 0x05
BaseClass 0x08
Cache Line 0x08
Latency 0x40
Header 0x80

PCI header
Address 0 (memory) 0xFEBFF400
Subvendor ID 0x1043
Subsystem ID 0x11C7
Int. Line 0x11
Int. Pin 0x02

PCI capability
Caps class Power Management
Caps offset 0x80
Caps version 1.1

PCI registers

	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
00	80	11	22	08	06	00	10	02	22	00	05	08	08	40	80	00
10	00	F4	BF	FE	00	00	00	00	00	00	00	00	00	00	00	00
20	00	00	00	00	00	00	00	00	00	00	00	00	43	10	C7	11
30	00	00	00	00	80	00	00	00	00	00	00	00	11	02	00	00
40	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
50	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
60	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
70	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
80	01	00	02	FE	00	40	00	48	00	00	00	00	00	00	00	00
90	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
A0	00	00	00	00	00	00	00	00	00	00	00	00	43	10	C7	11
B0	04	00	03	00	00	00	00	00	00	00	00	00	A0	00	00	00
C0	00	30	00	00	00	00	00	00	00	00	00	00	00	00	00	00
D0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
E0	A1	21	E0	01	00	00	00	00	40	00	00	00	00	00	00	00
F0	00	00	00	00	00	00	00	00	C0	00	20	00	00	00	00	00

Description System Device
Location bus 6 (0x06), device 1 (0x01), function 2 (0x02)

Common header
Vendor ID 0x1180
Model ID 0x0843
Revision ID 0x12
PI 0x00
SubClass 0x80
BaseClass 0x08
Cache Line 0x08
Latency 0x40
Header 0x80

PCI header
Address 0 (memory) 0xFEBFF000
Subvendor ID 0x1043

```

Subsystem ID          0x11C7
Int. Line             0x11
Int. Pin              0x02
PCI capability
Caps class            Power Management
Caps offset           0x80
Caps version          1.1

```

PCI registers

```

    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   80 11 43 08 06 00 10 02 12 00 80 08 08 40 80 00
10   00 F0 BF FE 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30   00 00 00 00 80 00 00 00 00 00 00 00 11 02 00 00
40   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   01 00 02 FE 00 40 00 48 00 00 00 00 00 00 00 00
90   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
B0   00 00 02 00 00 00 00 00 00 00 00 00 A0 00 00 00
C0   00 30 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   80 02 04 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 C0 00 20 00 00 00

```

```

Description          System Device
Location             bus 6 (0x06), device 1 (0x01), function 3 (0x03)

```

Common header

```

Vendor ID            0x1180
Model ID             0x0592
Revision ID          0x12
PI                   0x00
SubClass             0x80
BaseClass            0x08
Cache Line           0x08
Latency              0x40
Header               0x80

```

PCI header

```

Address 0 (memory)   0xFEBFEC00
Subvendor ID         0x1043
Subsystem ID         0x11C7
Int. Line            0x11
Int. Pin             0x02

```

PCI capability

```

Caps class            Power Management
Caps offset           0x80
Caps version          1.1

```

PCI registers

```

    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   80 11 92 05 06 00 10 02 12 00 80 08 08 40 80 00
10   00 EC BF FE 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30   00 00 00 00 80 00 00 00 00 00 00 00 11 02 00 00
40   00 00 02 00 00 00 00 00 00 00 02 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

```

80  01 00 02 FE 00 40 00 48 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 30 00 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 C0 00 00 00 00 00

```

```

Description          System Device
Location             bus 6 (0x06), device 1 (0x01), function 4 (0x04)

```

Common header

```

Vendor ID           0x1180
Model ID            0x0852
Revision ID         0x12
PI                  0x00
SubClass            0x80
BaseClass           0x08
Cache Line          0x08
Latency             0x40
Header              0x80

```

PCI header

```

Address 0 (memory)  0xFE8BF800
Subvendor ID        0x1043
Subsystem ID        0x11C7
Int. Line           0x11
Int. Pin            0x02

```

PCI capability

```

Caps class          Power Management
Caps offset         0x80
Caps version        1.1

```

PCI registers

```

00  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
80  80 11 52 08 06 00 10 02 12 00 80 08 08 40 80 00
10  00 E8 BF FE 00 00 00 00 00 00 00 00 00 00 00 00
20  00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 80 00 00 00 00 00 00 00 11 02 00 00
40  00 00 02 00 00 00 00 00 00 00 02 00 00 00 00 00
50  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  01 00 02 FE 00 40 00 48 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 30 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 C0 00 00 00 00 00 00 00

```

```

Description          SMBus Controller
Location             bus 0 (0x00), device 31 (0x1F), function 3 (0x03)

```

Common header

```

Vendor ID           0x8086
Model ID            0x2930
Revision ID         0x03
PI                  0x00
SubClass            0x05

```

```

BaseClass          0x0C
Cache Line         0x00
Latency            0x00
Header             0x00
PCI header
Address 0 (memory) 0xFDEFAC00
Address 4 (port) 0x00000400
Subvendor ID       0x1043
Subsystem ID       0x11C7
Int. Line          0x06
Int. Pin           0x03
PCI registers
  00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  86 80 30 29 03 00 80 02 03 00 05 0C 00 00 00 00
10  04 AC EF FD 00 00 00 00 00 00 00 00 00 00 00 00
20  01 04 00 00 00 00 00 00 00 00 00 00 43 10 C7 11
30  00 00 00 00 00 00 00 00 00 00 00 00 06 03 00 00
40  01 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60  03 04 04 00 00 00 08 08 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  04 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 00 86 0F 04 00 00 00 00 00

```

DMI

SMBIOS Version 2.5

DMI BIOS

```

vendor             American Megatrends Inc.
version            205
date               06/05/2009
ROM size           1024 KB

```

DMI System Information

```

manufacturer       ASUSTeK Computer Inc.
product             X71Q
version             1.0
serial              100819630069
UUID                {F1DAAF80-6FB8-81DE-3E1E-00248C3070C2}
SKU                 unknown
family              unknown

```

DMI Baseboard

```

vendor             ASUSTeK Computer Inc.
model              X71Q
revision           1.0
serial             BSN12345678901234567

```

DMI System Enclosure

manufacturer	ASUSTeK Computer Inc.
chassis type	Notebook
chassis serial	CSN12345678901234567
DMI Processor	
manufacturer	Intel
model	Intel(R) Core(TM)2 Duo CPU T5870 @ 2.00GHz
clock speed	2000.0 MHz
FSB speed	200.0 MHz
multiplier	10.0x
DMI Port Connector	
designation	J3401 (internal)
designation	LAN (external)
port type	Network Port
connector	RJ-45
DMI Port Connector	
designation	J4501 (internal)
designation	VGA (external)
port type	Video Port
connector	DB-15 female
DMI Port Connector	
designation	J5201 (internal)
designation	USB1 (external)
port type	USB
connector	Access Bus (USB)
DMI Port Connector	
designation	J5201 (internal)
designation	USB2 (external)
port type	USB
connector	Access Bus (USB)
DMI Port Connector	
designation	J4800 (internal)
designation	HDMI (external)
DMI Port Connector	
designation	J5101 (internal)
designation	SATA 1 (external)
DMI Port Connector	
designation	J4202 (internal)
designation	FlashCard (external)
DMI Port Connector	
designation	J4301 (internal)
designation	ExpressCard (external)
DMI Port Connector	
designation	J6601 (internal)
designation	eSATA (external)
DMI Port Connector	
designation	J3703 (internal)
designation	Audio Out (external)

port type	Audio Port
connector	Mini Jack (headphones)
DMI Port Connector	
designation	J3801 (internal)
designation	Mic In (external)
port type	Audio Port
connector	Mini Jack (headphones)
DMI Port Connector	
designation	J2502 (internal)
designation	USB3 (external)
port type	USB
connector	Access Bus (USB)
DMI Port Connector	
designation	J4203 (internal)
designation	1394 (external)
port type	Firewire (IEEE P1394)
connector	IEEE 1394
DMI Port Connector	
designation	J5401 (internal)
designation	DockPort (external)
DMI Extension Slot	
designation	PEG
type	A5
populated	yes
DMI Extension Slot	
designation	PCIE1
type	A5
populated	no
DMI Extension Slot	
designation	PCIE2
type	A5
populated	no
DMI Extension Slot	
designation	PCIE3
type	A5
populated	no
DMI Physical Memory Array	
location	Motherboard
usage	System Memory
correction	None
max capacity	8 GB
max# of devices	2
DMI Memory Device	
designation	SODIMM0
format	SODIMM
type	DDR2
total width	64 bits
data width	64 bits

size 2 GB

DMI Memory Device
designation SODIMM1
format SODIMM
type DDR2
total width 64 bits
data width 64 bits
size 2 GB

Storage

Drive 0

Name ST9320320AS
Revision 0303
Serial 5SX51QNP
Capacity 298.1 GB
Type Fixed
Bus Type ATA (3)
Features SMART
Volume c:\, 149.0 GBytes (54.8 percent available)
Volume d:\, 137.3 GBytes (99.5 percent available)

USB Devices

USB Device Périphérique d'interface utilisateur USB,
class=0x00, subclass=0x00, vendor=0x046D, product=0xC058
USB Device Périphérique de stockage de masse USB, class=0x00,
subclass=0x00, vendor=0x0951, product=0x1666
USB Device Périphérique USB composite, class=0xEF,
subclass=0x02, vendor=0x04F2, product=0xB012

Graphics

Number of adapters 1

Graphic APIs

API D3D
API Intel I/O

Display Adapters

Display adapter 0

ID 0x4000000
Name Mobile Intel(R) 4 Series Express Chipset Family
Board Manufacturer ASUSTeK Computer Inc.
PCI device bus 0 (0x0), device 2 (0x2), function 0 (0x0)
Vendor ID 0x8086 (0x1043)
Model ID 0x2A42 (0x1862)
Revision ID 0x9
Performance Level 0

Win32_VideoController AdapterRAM = 0x50D93800 (1356412928)
Win32_VideoController DriverVersion = 8.15.10.2302
Win32_VideoController DriverVersion = 8.15.10.2302
Win32_VideoController DriverDate = 02/11/2011
Win32_VideoController DriverDate = 02/11/2011

Monitor 0

Model ()
ID LPLA101
Serial
Manufacturing Date Week 0, Year 2007
Size 17.2 inches
Max Resolution 1440 x 900 @ 59 Hz
Horizontal Freq. Range 0-0 kHz
Vertical Freq. Range 0-0 Hz
Max Pixel Clock 0 MHz
Gamma Factor 2.2

Software

Windows Version Microsoft Windows Vista (6.0) Business
Edition 32-bit Service Pack 2 (Build 6002)
DirectX Version 11.0

Register Spaces

Register space PCI
Register space PCI #2
Register space PCI Express, base address = 0x0E000000
Register space SMBus, base address = 0x0400