

Assistant de reporting des problèmes

C:/Users/amiar/Desktop/amdprwreport.xml
samedi juin 15 2019 12:54:03

Client

Nom
Adresse email
No référence AMD 0

Système d'exploitation

Description du SE Windows 10 Pro 64bit Version 10.0 Build 17763 Service Pack 0.0
Répertoire Windows C:\WINDOWS
Mode administratif YES
Résolution 1920 X 1080

Matériel système

Carte mère

Mémoire 8149
Hyperthreading No
Type de carte
Numéro de série
Fabricant
Version SMBIOS F24
Version du BIOS 3.0

Processeur

Nombre de processeurs
physiques 1
Nombre de processeurs
logiques 4

Intel(R) Core(TM) i5-6500 CPU @ 3.20GHz

Fabricant du processeur GenuineIntel
Vitesse d'horloge 3201

Périphérique graphique

Radeon RX 570 Series

Fabricant Advanced Micro Devices, Inc.
ID du périphérique 0x67df

ID du fournisseur	0x1002
ID du sous-système	0x341b
ID du fournisseur du sous-système	0x1462
Information d'emplacement	bus 1 device 0 function 0
Type de bus	PCIE
Largeur maximale de liaison	PCI Express X16
Version du BIOS	015.050.000.001
Date du BIOS	11/09/17
Numéro de pièce	113V34122-F3
Hyper Memory	No
Type de mémoire	GDDR5
Fabricant du pilote	Advanced Micro Devices, Inc.
Version du package du pilote	19.10.15.09-190604a-343273E-RadeonSoftwareAdrenalin2019
Version du pilote 2D	
Version de OpenGL	
Version Direct3D	

Périphériques AMD

High Definition Audio Controller

ID du périphérique	0xaaaf0
ID du fournisseur	0x1002
ID du sous-système	0xaaaf0
ID du fournisseur du sous-système	0x1462
Information d'emplacement	bus 1 device 0 function 1
Type de bus	PCI
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.1

Autres périphériques

Intel(R) 100 Series/C230 Chipset Family SATA AHCI Controller

ID du périphérique	0xa102
ID du fournisseur	0x8086
ID du sous-système	0xb005
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 17 function 0
Type de bus	PCI
Fabricant du pilote	Intel Corporation

Version du package du pilote	15.9.6.1044
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Intel(R) Ethernet Connection (2) I219-V

ID du périphérique	0x15b8
ID du fournisseur	0x8086
ID du sous-système	0xe000
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 1f function 6
Type de bus	PCI
Fabricant du pilote	Intel
Version du package du pilote	12.18.8.9

Intel(R) 100 Series/C230 Series Chipset Family LPC Controller (B150) - A148

ID du périphérique	0xa148
ID du fournisseur	0x8086
ID du sous-système	0x5001
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 1f function 0
Type de bus	PCI
Fabricant du pilote	INTEL
Version du package du pilote	10.1.1.45

Synaptics SMBus Driver

ID du périphérique	0xa123
ID du fournisseur	0x8086
ID du sous-système	0x5001
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 1f function 4
Type de bus	PCI
Fabricant du pilote	Synaptics
Version du package du pilote	19.5.10.20

USB xHCI Compliant Host Controller

ID du périphérique	0xa12f
ID du fournisseur	0x8086
ID du sous-système	0x5007
ID du fournisseur du sous-système	0x1458

Information d'emplacement	bus 0 device 14 function 0
Type de bus	PCI
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.1

Intel(R) Management Engine Interface

ID du périphérique	0xa13a
ID du fournisseur	0x8086
ID du sous-système	0x1c3a
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 16 function 0
Type de bus	PCI
Fabricant du pilote	Intel
Version du package du pilote	1904.12.0.1208

High Definition Audio Controller

ID du périphérique	0xa170
ID du fournisseur	0x8086
ID du sous-système	0xa182
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 1f function 3
Type de bus	PCI
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.1

Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #5 - A114

ID du périphérique	0xa114
ID du fournisseur	0x8086
ID du sous-système	0x5001
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 0 function 0
Type de bus	PCI
Fabricant du pilote	INTEL
Version du package du pilote	10.1.1.45

Intel(R) Xeon(R) E3 - 1200/1500 v5/6th Gen Intel(R) Core(TM) Host Bridge/DRAM Registers - 191F

ID du périphérique	0x191f
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ID du fournisseur	0x8086
ID du sous-système	0x5000
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 0 function 0
Type de bus	PCI
Fabricant du pilote	INTEL
Version du package du pilote	10.1.1.45

Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #11 - A11A

ID du périphérique	0xa11a
ID du fournisseur	0x8086
ID du sous-système	0x5001
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 0 function 0
Type de bus	PCI
Fabricant du pilote	INTEL
Version du package du pilote	10.1.1.45

Intel(R) Xeon(R) E3 - 1200/1500 v5/6th Gen Intel(R) Core(TM) PCIe Controller (x16) - 1901

ID du périphérique	0x1901
ID du fournisseur	0x8086
ID du sous-système	0x5000
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 0 function 0
Type de bus	PCI
Fabricant du pilote	INTEL
Version du package du pilote	10.1.1.45

Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #9 - A118

ID du périphérique	0xa118
ID du fournisseur	0x8086
ID du sous-système	0x5001
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 0 function 0
Type de bus	PCI
Fabricant du pilote	INTEL
Version du package du pilote	10.1.1.45

Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #12 - A11B

ID du périphérique	0xa11b
ID du fournisseur	0x8086
ID du sous-système	0x5001
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 0 function 0
Type de bus	PCI
Fabricant du pilote	INTEL
Version du package du pilote	10.1.1.45

Intel(R) 100 Series/C230 Series Chipset Family PMC - A121

ID du périphérique	0xa121
ID du fournisseur	0x8086
ID du sous-système	0x5001
ID du fournisseur du sous-système	0x1458
Information d'emplacement	bus 0 device 0 function 0
Type de bus	PCI
Fabricant du pilote	INTEL
Version du package du pilote	10.1.1.45

Périphérique d'entrée USB

ID du périphérique	0x0001
ID du fournisseur	0x258a
ID du sous-système	0x0000
ID du fournisseur du sous-système	0x0000
Information d'emplacement	bus 0 device 0 function 5
Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.379

Périphérique d'entrée USB

ID du périphérique	0xa09f
ID du fournisseur	0x04d9
ID du sous-système	0x0000
ID du fournisseur du sous-système	0x0000
Information d'emplacement	bus 0 device 0 function 5

Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.379

Périphérique d'entrée USB

ID du périphérique	0xa09f
ID du fournisseur	0x04d9
ID du sous-système	0x0000
ID du fournisseur du sous-système	0x0000
Information d'emplacement	bus 0 device 0 function 4
Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.379

Périphérique d'entrée USB

ID du périphérique	0xa09f
ID du fournisseur	0x04d9
ID du sous-système	0x0000
ID du fournisseur du sous-système	0x0000
Information d'emplacement	bus 0 device 0 function 3
Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.379

Périphérique d'entrée USB

ID du périphérique	0x05c4
ID du fournisseur	0x054c
ID du sous-système	0x0000
ID du fournisseur du sous-système	0x0000
Information d'emplacement	bus 0 device 0 function 1
Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.379

Périphérique USB composite

ID du périphérique	0x0001
ID du fournisseur	0x258a
ID du sous-système	0x0000
ID du fournisseur du sous-système	0x0000
Information d'emplacement	bus 0 device 0 fonction 4
Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.1

Périphérique USB composite

ID du périphérique	0xa09f
ID du fournisseur	0x04d9
ID du sous-système	0x0000
ID du fournisseur du sous-système	0x0000
Information d'emplacement	bus 0 device 0 fonction 3
Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.1

Périphérique d'entrée USB

ID du périphérique	0x0001
ID du fournisseur	0x258a
ID du sous-système	0x0000
ID du fournisseur du sous-système	0x0000
Information d'emplacement	bus 0 device 0 fonction 4
Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	10.0.17763.379

Carte réseau sans fil USB 802.11n

ID du périphérique	0x3573
ID du fournisseur	0x148f
ID du sous-système	0x0000
	0x0000

ID du fournisseur du sous-système	
Information d'emplacement	bus 0 device 0 fonction a
Type de bus	USB
Largeur maximale de liaison	Unknown
Fabricant du pilote	Microsoft
Version du package du pilote	5.1.22.0

Logiciels installés

@BIOS B15.0630.1	
AMD Problem Report Wizard	3.1.723
AMD Settings	2017.0424.2119.36535
AMD Software	19.6.1
APP Center	
App Center	
Assassin's Creed Unity	
Battle.net	
Branding64	1.00.0001
Catalyst Control Center Next Localization BR	2017.0424.2119.36535
Catalyst Control Center Next Localization CHS	2017.0424.2119.36535
Catalyst Control Center Next Localization CHT	2017.0424.2119.36535
Catalyst Control Center Next Localization CS	2017.0424.2119.36535
Catalyst Control Center Next Localization DA	2017.0424.2119.36535
Catalyst Control Center Next Localization DE	2017.0424.2119.36535
Catalyst Control Center Next Localization EL	2017.0424.2119.36535
Catalyst Control Center Next Localization ES	2017.0424.2119.36535
Catalyst Control Center Next Localization FI	2017.0424.2119.36535
Catalyst Control Center Next Localization FR	2017.0424.2119.36535
Catalyst Control Center Next Localization HU	2017.0424.2119.36535
Catalyst Control Center Next Localization IT	2017.0424.2119.36535
Catalyst Control Center Next Localization JA	2017.0424.2119.36535
Catalyst Control Center Next Localization KO	2017.0424.2119.36535
Catalyst Control Center Next Localization NL	2017.0424.2119.36535
Catalyst Control Center Next Localization NO	2017.0424.2119.36535
Catalyst Control Center Next Localization PL	2017.0424.2119.36535
Catalyst Control Center Next Localization RU	2017.0424.2119.36535
Catalyst Control Center Next Localization SV	2017.0424.2119.36535
Catalyst Control Center Next Localization TH	2017.0424.2119.36535
Catalyst Control Center Next Localization TR	2017.0424.2119.36535
Driver Booster 6	
Dropbox	

Dropbox Update Helper	
EasyTune	
EdgeDeflector	
Epic Games Launcher	
Epic Games Launcher Prerequisites (x64)	1.0.0.0
Fast Boot	
Gigabyte Speed v10.10	10.10
Google Chrome	
Google Update Helper	
Hearthstone	
Launcher Prerequisites (x64)	
League of Legends	
Microsoft Visual C++ 2010 x64 Redistributable - 10.0.40219	10.0.40219
Microsoft Visual C++ 2010 x86 Redistributable - 10.0.40219	
Microsoft Visual C++ 2012 Redistributable (x64) - 11.0.61030	
Microsoft Visual C++ 2012 Redistributable (x86) - 11.0.61030	
Microsoft Visual C++ 2012 x64 Additional Runtime - 11.0.61030	11.0.61030
Microsoft Visual C++ 2012 x64 Minimum Runtime - 11.0.61030	11.0.61030
Microsoft Visual C++ 2012 x86 Additional Runtime - 11.0.61030	
Microsoft Visual C++ 2012 x86 Minimum Runtime - 11.0.61030	
Microsoft Visual C++ 2013 Redistributable (x64) - 12.0.30501	
Microsoft Visual C++ 2013 Redistributable (x86) - 12.0.30501	
Microsoft Visual C++ 2013 x64 Additional Runtime - 12.0.21005	12.0.21005
Microsoft Visual C++ 2013 x64 Minimum Runtime - 12.0.21005	12.0.21005
Microsoft Visual C++ 2013 x86 Additional Runtime - 12.0.21005	
Microsoft Visual C++ 2013 x86 Minimum Runtime - 12.0.21005	
Microsoft Visual C++ 2015 Redistributable (x86) - 14.0.24215	
Microsoft Visual C++ 2015 x86 Additional Runtime - 14.0.24215	
Microsoft Visual C++ 2015 x86 Minimum Runtime - 14.0.24215	
Microsoft Visual C++ 2017 Redistributable (x64) - 14.14.26429	
Microsoft Visual C++ 2017 x64 Additional Runtime - 14.14.26429	14.14.26429
Microsoft Visual C++ 2017 x64 Minimum Runtime - 14.14.26429	14.14.26429
NVIDIA PhysX	
Origin	
Portal 2	
Realtek High Definition Audio Driver	
Rocket League	
SIV	
Steam	
Steep	
Tom Clancy's The Division 2	
TrackMania ² Stadium	
Update for Windows 10 for x64-based Systems (KB4023057)	2.58.0.0

Update for Windows 10 for x64-based Systems (KB4480730)

2.51.0.0

Uplay

WhoCrashed 6.61

Display Log

Display Log Details

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*** Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz,
Min SCLK:0kHz, Min MCLK:0kHz} ^ [+00:200:905]
[Conn_Hotplug] [DVIDL-1] - Connected ^ [+00:000:998]
[Conn_Detect] [DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF
FF FF FF FF 00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30
1B 78 2E 35 81 A6 56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81
8F 95 00 95 0F A9 40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C
45 00 DD 0C 11 00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32
31 31 31 31 30 00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20
20 20 00 00 00 FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00
32 ^ [+00:001:994] [Conn_PHY_SIZE] [DVIDL-1] [X
Dim:480mm, Y Dim:270mm] ^ [+00:000:998]
[Conn_native_mode] [DVIDL-1] {1920x1080@60Hz} ^
[+00:000:997] [Conn_HDCP_VER] [DVIDL-1] 1.4 ^
[+00:000:998] [Conn_Displ_Year] [DVIDL-1] - [2011] ^
[+00:000:997] [Conn_Displ_Color_Depth] [DVIDL-1] - 888 ^
[+00:000:997] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:001:995] [Conn_Num_Display] 1 ^ [+00:407:908]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:998] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:009:973]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:952]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:036:901] *****Exiting
SetMode***** ^ [+00:013:359] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:244:463] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:001:994] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:001:995] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:002:030] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:001:987] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:997] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:998] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:997] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:002:992] [Conn_Num_Display] 1 ^ [+00:439:408]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,

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2200x1125@148500Khz} ^ [+00:027:925] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:013:964]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:951]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:038:896] *****Exiting
SetMode***** ^ [+00:361:541] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:656:077]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:622]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:620]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:062:500] *****Exiting
SetMode***** ^ [+00:085:403] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:847:902]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+16:866:100] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:083:770] [Conn_Signal_Type] [DVIDL-1] - DVISL ^
[+00:017:952] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:925] *****Exiting
SetMode***** ^ [+00:164:294] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:200:101]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:133:191] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:967] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:904] *****Exiting
SetMode***** ^ [+00:630:193] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:957] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:103] *****Exiting

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SetMode***** ^ [+00:215:160] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:781:748]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:083:988] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:017:952] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:037:920]
*****Exiting SetMode***** ^ [+00:162:571]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:332:911] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:649:165]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:952] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:580]
*****Exiting SetMode***** ^ [+00:265:462]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:199:376] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+27:374:730]
[Conn_Num_Display] 2 ^ [+00:000:998] [Conn_Hotplug]
[UNKNOWN] - Connected ^ [+00:001:994] [Conn_Detect]
[UNKNOWN] DDC Display: [EDID Block 0] 00 FF FF FF FF FF
FF 00 05 A4 01 00 01 00 00 00 14 1B 01 04 B1 4F 00 FF 00 A5 95
AA 54 4F A1 26 0A 50 54 00 00 00 01 01 01 01 01 01 01 01 01
01 01 01 01 01 01 64 00 40 A0 00 40 13 00 58 2C 45 00 55 50 21
00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 C6 ^
[+00:000:998] [Conn_PHY_SIZE] [UNKNOWN] [X
Dim:-2043mm, Y Dim:-1146333440mm] ^ [+00:000:997]
[Conn_native_mode] [UNKNOWN] {64x64@54Hz} ^
[+00:001:995] [Conn_Disp_Year] [UNKNOWN] - [2017] ^
[+00:000:997] [Conn_Disp_Color_Depth] [UNKNOWN] - 101010
^ [+00:000:997] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+27:958:245] [Conn_Num_Display] 1 ^ [+00:001:011]
[Conn_Hotplug] [UNKNOWN] - Disconnected ^ [+51:034:046]
[Conn_Num_Display] 2 ^ [+00:000:998] [Conn_Hotplug]
[UNKNOWN] - Connected ^ [+00:001:994] [Conn_Detect]
[UNKNOWN] DDC Display: [EDID Block 0] 00 FF FF FF FF FF
FF 00 05 A4 01 00 01 00 00 00 14 1B 01 04 B1 4F 00 FF 00 A5 95
AA 54 4F A1 26 0A 50 54 00 00 00 01 01 01 01 01 01 01 01 01
01 01 01 01 01 01 64 00 40 A0 00 40 13 00 58 2C 45 00 55 50 21
00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 C6 ^
[+00:000:998] [Conn_PHY_SIZE] [UNKNOWN] [X
Dim:-2043mm, Y Dim:-1146333440mm] ^ [+00:000:996]
[Conn_native_mode] [UNKNOWN] {64x64@54Hz} ^
[+00:001:995] [Conn_Disp_Year] [UNKNOWN] - [2017] ^
[+00:000:997] [Conn_Disp_Color_Depth] [UNKNOWN] - 101010
^ [+00:000:998] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+43:661:096] *****Entering SetMode***** ^
[+00:000:000] [Conn_Mode] [DVIDL-1] {1920x1080,

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1920x1080@60:1, 2200x1125@148500Khz} ^ [+00:007:978]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:014:961] *****Exiting
SetMode***** ^ [+00:052:366] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:955] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:901] *****Exiting
SetMode***** ^ [+00:109:708] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:019:946] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:924] *****Exiting
SetMode***** ^ [+00:346:053] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+08:132:627]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:017:952] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:034:906]
*****Exiting SetMode***** ^ [+00:131:648]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:199:468] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:623:361]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:009:973] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:904]
*****Exiting SetMode***** ^ [+00:147:606]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:549:219] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:019:025] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:147] *****Exiting
SetMode***** ^ [+00:196:476] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+11:761:272]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:004:986] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:036:876]
*****Exiting SetMode***** ^ [+00:880:090]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+43:998:935] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:956] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:020:944] *****Exiting
SetMode***** ^ [+00:163:071] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+11:402:472]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:013:963] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:036:901]

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*****Exiting SetMode***** ^ [+00:096:251]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+59:081:626] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:017:952] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:014:959] *****Exiting
SetMode***** ^ [+00:168:550] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:552:756]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:012:937] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:904]
*****Exiting SetMode***** ^ [+00:114:203]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+41:044:419] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:017:923] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:014:959] *****Exiting
SetMode***** ^ [+00:185:505] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+09:260:066]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:012:965] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:017:979]
*****Exiting SetMode***** ^ [+00:098:717]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:898:370] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:017:952] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:901] *****Exiting
SetMode***** ^ [+00:213:430] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+22:525:411]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+44:983:467] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:319]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:816] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:212]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:259] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+17:998:732] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:777]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:533:573] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:532:503]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:850:832] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:743]

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{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:114] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:095]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+28:732:682] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:266:793]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:433:117] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:350:065]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+51:015:707] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:733] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:850]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:299:894] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:784]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:882:756] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:449:798]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+03:032:700] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:823] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:806] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:414:487]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:084:828] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:016:954] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:904]
*****Exiting SetMode***** ^ [+00:248:337]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:148:534] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
^ [+00:017:952] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:037:006] *****Exiting
SetMode***** ^ [+00:712:126] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
^ [+00:016:926] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:929] *****Exiting
SetMode***** ^ [+00:163:563] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:233:472]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min

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MCLK:300000kHz} ^ [+02:182:175] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:084:272] [Conn_Signal_Type] [DVIDL-1] - DVISL ^
[+00:017:951] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:931] *****Exiting
SetMode***** ^ [+00:096:736] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:976] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:910] *****Exiting
SetMode***** ^ [+00:180:520] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:216:316]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:433:659] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:967:495] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:433:351]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+11:715:902] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:149:600] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:921]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:116:168] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:134]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+30:749:302] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:775]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:933:506] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:382:833]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:783:217] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:796]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:067:346] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:824]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+19:183:344] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:299:199]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:550:468] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:260]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+36:716:467] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:851]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min

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MCLK:300000kHz} ^ [+00:066:794] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:757]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:878]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:949:771] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:433]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:817:144] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:432:843]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+12:963:345] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:843] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:116:692] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:866:494]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:241] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:249:667]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:500:360] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+08:065:766]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:983:191]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:164] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:849:933]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:433:189] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:583:065]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:757]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:414] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:844]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:183:319] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:250:331]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:517:124] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:383:140]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:566:409] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:240]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+52:895:901] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:316:989]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:812] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:594]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:817] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+37:799:850] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:216:422]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:300:197] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:299:201]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:184:145] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:019]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+04:366:983] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:732]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:350:065] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:283:524]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+05:982:855] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:729]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:802] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+09:416:311]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:415:961]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:417:351] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:583:462]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:799] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:800]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:383] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:816:936]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:368] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:534:001]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:350:037] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:949:405]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:116:854] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:873] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+22:949:433]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:216:654] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:450:119]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:367:019] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:782:985]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:842]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:831] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:203]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:332] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+17:583:403]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:249:712] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:450:139]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:350:572] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:399:871]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:793] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:327]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+26:000:147] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:215:930]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:449:798] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:416:887]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+53:431:988] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:323]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:100:239] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+04:266:360] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:383:354] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:316:169]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:217:418] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:233:026]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+00:083:638] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:842]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:058] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:850]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:914] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:361]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:556] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+17:499:388] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:133:642]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+14:466:306] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:449:981]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+06:249:418] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:577]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:733] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:803] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+30:566:961]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:241] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:966:719]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:549:533] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:817:207]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:329] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:779] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+09:433:547]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:018] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:799:968]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:500:063] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:949:992]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:331] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:866:956]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:416:885] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+49:748:219]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:823]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:823] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:730]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+20:017:087]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:734] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:149:679]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:467:215] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:765:590]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:688]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+33:483:692] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:245]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:050:212] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:770]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:166:559] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:379] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:734]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+08:733:587] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:433:858]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:616:772] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:516:130]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+28:915:686] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:866]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:259] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+21:082:875]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:350:598] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:300:198]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:999:900] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:332]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:482:476] {DispCLK:156521kHz, Min

```

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:752]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:800] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:756]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:812] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+12:133:296]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:825] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:216] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:262]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:067:073] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:750:055]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:285] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:832:939]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:516:619] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+17:999:924]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:734] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:250:065] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:641:574]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:622]
[Conn_Signal_Type] [DVIDL-1] - DVI-DL ^ [+00:015:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:062:514] *****Exiting
SetMode***** ^ [+00:078:078] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:834:584]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+35:476:291] [Conn_Num_Display] 2 ^
[+00:141:054] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:995] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:998] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2046mm, Y Dim:281825296mm] ^
[+00:000:996] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:995] [Conn_Disp_Year]
[UNKNOWN] - [2017] ^ [+00:002:019]
[Conn_Disp_Color_Depth] [UNKNOWN] - 101010 ^
[+00:001:994] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+12:068:748] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+13:330:628] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+03:130:484] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+20:082:920]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:848]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:796] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:233:291]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:200:477] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:366:206]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:333:082] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+34:594:248]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:823] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:731] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:300:649]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:801] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:133:936]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:499:175] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+45:827:794]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:734]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:499:970] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:785]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:332:557] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:499:891]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+54:049:249] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:816]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+39:662:599] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:810]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:790] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+10:099:576] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:399:931] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:399:439]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+15:183:700] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:835] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:786]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+21:516:035] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:633:307] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:400:506]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+51:460:545] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:815]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:783:617] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:116:055] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:307]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

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MCLK:300000kHz} ^ [+57:429:934] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:633:272] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:149:928] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:484:034]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:161:983] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:779]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:819] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:633:159]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:127] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:250:292]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:533:050] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:481:566]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:285] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:771]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:800]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+13:216:091] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:752]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:183:244] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:482:848]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+25:043:668] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+45:655:701] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:031:242] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:031:243] *****Exiting
SetMode***** ^ [+00:156:213] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:015:623] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:046:863] *****Exiting
SetMode***** ^ [+58:597:778] {DispCLK:100000kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:159:727]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:100000kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:622]
[Conn_Signal_Type] [DVIDL-1] - DVI SL ^ [+00:015:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:015:621] *****Exiting
SetMode***** ^ [+00:015:622] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:000:000] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:031:242] *****Exiting
SetMode***** ^ [+00:171:836] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:224:723] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:002:006] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:986] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:998] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:997] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:997] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:001:001] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:994] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:002:992] [Conn_Num_Display] 1 ^ [+00:481:945]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:001:995] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:957]
[Conn_Signal_Type] [DVIDL-1] - DVI SL ^ [+00:017:986]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:035:868] *****Exiting
SetMode***** ^ [+00:497:670] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:264:590] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:999] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:001:994] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:001:013] [Conn_native_mode] [DVIDL-1]

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{1920x1080@60Hz} ^ [+00:001:021] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:975] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:980] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:001:994] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:002:022] [Conn_Num_Display] 1 ^ [+00:472:041]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:998] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:018:949]
[Conn_Signal_Type] [DVIDL-1] - DVI-DL ^ [+00:017:952]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:036:901] *****Exiting
SetMode***** ^ [+00:380:499] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:955] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:636:042]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:622]
[Conn_Signal_Type] [DVIDL-1] - DVI-DL ^ [+00:031:243]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:863] *****Exiting
SetMode***** ^ [+00:078:107] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:530:891]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+29:780:954] [Conn_Num_Display] 2 ^
[+00:008:976] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:996] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:995] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2044mm, Y Dim:297053552mm] ^
[+00:000:998] [Conn_native_mode] [UNKNOWN]

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{64x64@54Hz} ^ [+00:000:997] [Conn_Dispatch_Year]
 [UNKNOWN] - [2017] ^ [+00:000:997]
 [Conn_Dispatch_Color_Depth] [UNKNOWN] - 101010 ^
 [+00:001:995] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
 [+36:201:946] {DispCLK:156521kHz, Min SCLK:300000kHz,
 Min MCLK:300000kHz} ^ [+37:949:117] {DispCLK:156521kHz,
 Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:842]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:828]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:706] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+25:482:930]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:761] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:683:352]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:316:154] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+33:146:992]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:811] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:807]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:851] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:759]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+16:016:418] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:757]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:166:732] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:483:906]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:415:888] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:733] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:750:627]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:299:200] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:533:421]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:416:396] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+22:483:399]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:850] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:751]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+08:900:147] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:327]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:832:788] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:450:399]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+21:149:239] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:852] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:730] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+41:216:586]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:780] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:767:129]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:449:359] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:131:799]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+54:698:302]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:483:514]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:328]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:732] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:299:545] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:233:624]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:249:958] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:283:414]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:149:679] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:749]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:844] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:829]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:749] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:851]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+20:699:593] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:366:557] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:416:909]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+46:763:090] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:284]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+19:716:523]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:116:593]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:450:448] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:444:634]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:843]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:827] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:823]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:792] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:499:877]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:777] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:150:024]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:483:686] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+22:260:862]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+41:180:533]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:600:467] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:849]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:103] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+46:866:060] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:350:064] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:299:264]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+38:083:469] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:800]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:850] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:807]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:766] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:843]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+18:533:248] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:200:336]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:333:124] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:282:752]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+27:267:264] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:828] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+13:349:447]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:183:509] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:300:225]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:283:215] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+20:282:648]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:330]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:733:707] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:166:554]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:317:147] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:265:888]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+26:849:854] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:848]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:049] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:848]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:707] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:334:148]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:732] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:700:150]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:383:507] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:790:238]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+43:529:867]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:632:561] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:322]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+00:066:794] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:694]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+36:582:849] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:233:376]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:316:684] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:349:576]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:133:997] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:164]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+32:165:654]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:183:510] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:367:018]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:283:243] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:383:125]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:543] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+06:215:955] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:791]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:416:400] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:450:795]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+06:849:704] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:057]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:039] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+18:967:249]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:516:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:365:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:433:581]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:843] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:799]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:837]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:483:100] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:217:418]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:382:976] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:350:470]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+36:061:647] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:826]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:848] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:703] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:649:773]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:748] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:916:853]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:383:473] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+39:014:643]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:731] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+31:565:993]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+24:349:564] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:799:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+54:995:624] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:416:965]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+28:789:465] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:278:344]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:932:556] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:167:262]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:283:243] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:266:326]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:067:007] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:366:363]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:433:086] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:700:303]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:166:270] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:300:245]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:533:135] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:316:540]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:850:259] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:466:666]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:816:900] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:983:400]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:283:564] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:432:693]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:800:176] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:166:527]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:899:655] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:616:991]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:600:100] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:316:648]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:416:187] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:767:598]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:083:244] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:683:293]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:482:892] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:199:656]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:133:669] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:449:945]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:683:653] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:965:564]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+05:033:298] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+57:716:197]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:850:470] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:016:523]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+47:226:706] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+19:366:445]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+37:881:889] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+34:495:371]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+31:266:885] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:734]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+55:566:254] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:232:379]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:334:106] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:333:111]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+01:533:411] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:709]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:752]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+23:183:197] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:417:102] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:415:888]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+06:033:004] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:793]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:849] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:706]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:950:234]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:216:398] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:449:798]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:367:019] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:299:746]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:734] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:966:805] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:216:393]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:449:798] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:366:603]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+57:681:789] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:794]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:733:041] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:775]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:882:596] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:751]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+15:016:876] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:848] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:367]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:950:570] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:332:754]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:516:444] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:752]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+28:064:394] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:813]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:829] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:751]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:083:802]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:779] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:033:368]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:416:393] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+44:448:515]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:067:327] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:823] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:687]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:266:730] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:116:049] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:483:198]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:082:442] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:956]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:546]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:274] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:088]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:033:246] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:119]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:099:807] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:500:688]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+08:981:601] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:806]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+37:713:281] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:777]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:823] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+34:782:502] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:399:932] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:449:802]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+04:300:243] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:330]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:855] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:806]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+10:433:262] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:216:421]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:466:752] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:416:839]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+32:815:464] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:332]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:953] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:013]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:484:108] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:850:168] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:433:418]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:699:663] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:812]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:707] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:736] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:867:149]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:283] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:599:903]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:432:843] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+20:108:882]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+02:616:955] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:266:310]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:533:306] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:733:016]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:778] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:230:331]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:350:064]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:432:860]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:266:288] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:689]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+18:666:799]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:116:687] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:317:154]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:333:109] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:099:187]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:842] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:306]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+32:132:533]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:200:463] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:350:065]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:283:243] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:750:317]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:845] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:673]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+15:466:809] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:400:468] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:415:860]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+07:933:302] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:816]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:819] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:943] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:983:666]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:249:463] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:433:244]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:367:017] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:283:201]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:795] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:329]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:823]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:823] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:549:179]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:150:362] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:449:798]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:383:973] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:132:668]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:208]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:330]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:823] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:049:532]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:200:465] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:415:888]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:350:453] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:066:424]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:734]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:333:429] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:331]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:533:600] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:350:037]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+22:958:708] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:800]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:849]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:100:757] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:794]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:899:606] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:133:824] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:499:664]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+21:144:719] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:751]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+29:080:335] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:780] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:775]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+04:233:697] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:300:197] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:250:331]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+23:255:272] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:734]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:816:513] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:283]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:350:314] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:501]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+48:465:656] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:067:103] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+00:083:317] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:311]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:300:315] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:755]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:282:481] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:600:394]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+03:517:054] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:823]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:826] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:726]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+19:049:382] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:015:843] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:550:529]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+28:781:833] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+05:099:300] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:383:211] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:532:774]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:796:183] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+11:265:440] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+31:149:077]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:449:972] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+49:999:279]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:779:835] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+23:896:191]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:449:235] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:066:907] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:500:159]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+41:761:756] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:186] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:241]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+08:716:193] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:775]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:149:869] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:483:793]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+48:362:332] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:777]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+48:381:322] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:734] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:208]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+08:299:127] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:367:019] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:399:439]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+46:692:205] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:777]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+35:752:752] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+37:115:723]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:689]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:383:863]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:433:841]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:399:441] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+12:858:956]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:733] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+26:147:781]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+05:616:067] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:116:854] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+40:048:898]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:284:241]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:399:438] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:364:206]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:749] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:799]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:800] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+04:866:942] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:750:275] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:382:475]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+36:847:502] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+31:315:209] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:737]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+34:417:039] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:299:201] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:350:742]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+42:409:470] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:777]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+12:016:315] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:336]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:749:565] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:366:996]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+59:565:241] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:758]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+36:400:808] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:166:717]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:778] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:777]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+26:116:557] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:266:286] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:899:422]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:708]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+28:466:705] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:801]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:282:759] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:366:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:467:239] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:606] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:761] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+08:132:555]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:217:419] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:283:243]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:333:110] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:198:909]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:328]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:616] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+09:000:178]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:811] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:533:511]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:350:037] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+18:482:233]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+27:327:924]

```

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:882:903] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:819]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:825]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:761] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:499:382]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:383:245]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:582:940] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+42:766:136]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:385] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+58:728:762]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:031:243] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:031:243]
*****Exiting SetMode***** ^ [+00:218:698]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:015:622] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:046:892]
*****Exiting SetMode***** ^ [+53:881:175]
{DispCLK:100000kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:171:812] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:000:000] {DispCLK:100000kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:015:622] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:015:621] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:622]
*****Exiting SetMode***** ^ [+00:000:000]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:015:621] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:621]
*****Exiting SetMode***** ^ [+01:281:301]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:281:399] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+21:366:759]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+03:516:292] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:182:585]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+32:065:856] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+51:944:388]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+17:766:672] {DispCLK:156521kHz, Min

```

```

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+37:275:450]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+34:833:118] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+37:922:967]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+45:266:212] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+42:411:365]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+18:248:916] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+37:049:708]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ *** Log File Version: 2.0 ***
[+00:000:000] {DispCLK:0kHz, Min SCLK:0kHz, Min
MCLK:0kHz} ^ [+00:263:173] [Conn_Hotplug] [DVIDL-1] -
Connected ^ [+00:000:000] [Conn_Detect] [DVIDL-1]
PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF 00 26 CD
16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6 56 48 9A
24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9 40 B3 00
01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11 00 00 1E
00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30 00 00 00
FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00 FC 00 50
4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^ [+00:000:000]
[Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y Dim:270mm] ^
[+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:640:646]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:031:243]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:867] *****Exiting
SetMode***** ^ [+00:078:126] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:577:734]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+28:914:105] [Conn_Num_Display] 2 ^
[+00:000:997] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:998] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:997] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2043mm, Y Dim:1692956528mm] ^
[+00:001:995] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:997] [Conn_Dispatch_Year]
[UNKNOWN] - [2017] ^ [+00:000:998]

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[Conn_Disp_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:997] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:940] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:656:096]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:031:243]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:622]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:864] *****Exiting
SetMode***** ^ [+00:078:107] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:551:444]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+36:908:254] [Conn_Num_Display] 2 ^
[+00:001:996] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:997] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:994] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2044mm, Y Dim:-336812224mm] ^
[+00:000:997] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:997] [Conn_Disp_Year]
[UNKNOWN] - [2017] ^ [+00:001:000]
[Conn_Disp_Color_Depth] [UNKNOWN] - 101010 ^
[+00:001:992] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30

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00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:640:475]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:622]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:062:506] *****Exiting
SetMode***** ^ [+00:093:708] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:515:594]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+27:623:609] [Conn_Num_Display] 2 ^
[+00:000:998] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:997] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:002:991] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2043mm, Y Dim:2070947744mm] ^
[+00:000:998] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:997] [Conn_Dispatch_Year]
[UNKNOWN] - [2017] ^ [+00:000:998]
[Conn_Dispatch_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:997] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:671:717]
*****Entering SetMode***** ^ [+00:000:000]

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[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:031:243]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:622]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:884] *****Exiting
SetMode***** ^ [+00:078:115] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:546:482]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+30:916:210] [Conn_Num_Display] 2 ^
[+00:000:997] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:997] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:997] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2041mm, Y Dim:1859248144mm] ^
[+00:001:995] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:997] [Conn_Dis_Year]
[UNKNOWN] - [2017] ^ [+00:000:998]
[Conn_Dis_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:997] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+20:133:619] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+39:548:790] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:333:422]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:299:760] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+14:883:559]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:216:827] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:300:165]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:232:897] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:667:215]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:366:023] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:183:062]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:549:427] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:683:699]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:950:013] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+15:650:028]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:233:196] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:099:648]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:414:926] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+13:399:679]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min

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MCLK:300000kHz} ^ [+54:001:111] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+41:998:537] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:366:526]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:199:974] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:634]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+16:232:765] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:533:488] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:968]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:499:859] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:837]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:317:152] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:300:198] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:116:688] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+05:333:539] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:712]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:486:500] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:080:811]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:333:081] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:250:358] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:149:572]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:119:709] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:063:833]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:133:634] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:852]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:549:537] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:993]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:666:460] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:705]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:700:128] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:315:889] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:727]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:099:319] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:731]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:566:530] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:331]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:033:453] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:688]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:383:484] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:188]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:299:194] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:519]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:449:798] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:751]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:700:105] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:734]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:933:707] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:883]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:283:242] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:734]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:734:382] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:097]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:883:727] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:706]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:116:702] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:789]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:533:521] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:823]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:133:646] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:116:858] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:300:624] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:238]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:149:129] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:743]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:149:601] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:084:089]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:233:397] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:711]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:100:198] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:214]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:866] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:244]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:133:487] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:481]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:583:695] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:200:105] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+41:066:895] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+25:724:967]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+14:116:392] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:500:265]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+17:232:961] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+15:012:721]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:031:242] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:046:882]
*****Exiting SetMode***** ^ [+00:203:059]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:062:486]
*****Exiting SetMode***** ^ [+30:456:510]
{DispCLK:100000kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:190:945] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:000:000] {DispCLK:100000kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:015:621] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:015:622] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:621]
*****Exiting SetMode***** ^ [+00:343:669]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:015:622] {DispCLK:156521kHz,

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Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:046:863]
*****Exiting SetMode***** ^ [+00:234:321]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+39:675:373] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{720x400, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:017:951] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:037:899] *****Exiting
SetMode***** ^ [+00:565:487] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{720x400, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:012:966] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:904] *****Exiting
SetMode***** ^ [+00:047:872] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:955] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:925] *****Exiting
SetMode***** ^ [+00:084:752] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:011:967] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:907] *****Exiting
SetMode***** ^ [+01:198:842] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:415:926]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:954] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:012:965]
*****Exiting SetMode***** ^ [+00:137:633]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+44:143:504] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:005:985] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:037:898] *****Exiting
SetMode***** ^ [+00:061:835] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:954] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:929] *****Exiting
SetMode***** ^ [+00:104:722] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:009:977] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:895] *****Exiting
SetMode***** ^ [+00:163:558] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:964:872]
*****Entering SetMode***** ^ [+00:000:997]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:954] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:021:270]
*****Exiting SetMode***** ^ [+00:212:464]

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{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+24:772:490] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:009:993] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:037:877] *****Exiting
SetMode***** ^ [+00:061:836] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:966] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:916] *****Exiting
SetMode***** ^ [+00:108:688] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:007:009] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:904] *****Exiting
SetMode***** ^ [+00:130:623] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:248:072]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:017:952] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:012:965]
*****Exiting SetMode***** ^ [+00:170:544]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+20:533:502] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:015:940] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:924] *****Exiting
SetMode***** ^ [+00:063:903] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:955] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:037:921] *****Exiting
SetMode***** ^ [+00:084:775] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:009:976] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:903] *****Exiting
SetMode***** ^ [+00:147:577] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+56:956:093]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:017:952] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:013:963]
*****Exiting SetMode***** ^ [+00:219:413]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+30:296:968] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:167:442]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:500:000]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min

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MCLK:300000kHz} ^ [+02:683:381] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:382:989]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:266:295] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:599:858]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:747] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:182:512]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+14:283:308] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:483:706]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:783:512] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:233:376]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:733] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:349:705]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:182:512] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+49:249:983]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:400:616] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:815:541]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+11:366:912] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:550:124]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+39:759:779] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:933:084]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:449:688] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:699:218]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:446:210] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:666:419]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+14:283:190] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:365]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:400:585] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:266:057]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+05:999:881] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:250:193]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:683:372] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:549:506]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+05:100:537] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:416:884]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:115:691] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+37:358:843]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:915:730] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:780]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+04:200:390] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:449:856]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:149:515] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:250:497]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:383:232] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:382:975]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+04:716:610] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+44:466:071]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+50:632:759] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:999:342]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+07:650:430] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:616:751]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:779] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+14:266:554]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:100:731] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:332:647]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:733] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+08:316:551]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:300:947] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+27:595:791]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:883:394]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+50:148:055] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+45:665:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+30:848:576] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:849:185] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+11:467:376]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:049:866] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:630:496]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:616:402]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+53:499:029] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:917]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+06:199:067] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:533:075]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:767:039] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+13:732:648]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+14:765:806]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1280x720, 1280x1024@60:0,
1688x1066@108000Khz} ^ [+00:083:285] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:017:951] {DispCLK:144000kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:034:907]
*****Exiting SetMode***** ^ [+01:146:897]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:083:776] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:016:954] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:037:899]
*****Exiting SetMode***** ^ [+00:313:163]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:333:436] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:882:424]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:583:190] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+17:516:067]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+20:082:619] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:776:958]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+36:391:060] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+27:382:709]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:650:305] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+55:785:828]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:683:242] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+56:079:802]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:483:678] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:036:617]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:959:430] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+50:766:548]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:183:836]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min

```

MCLK:300000kHz} ^ [+00:316:154] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+33:946:060]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:831]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:811] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:735]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+11:949:482]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:283:602]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:566:300] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:266:175]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:893] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:285]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+03:983:176] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:317:152]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:533:154] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:482:630]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+27:460:626] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:750]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:938] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+05:582:791] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:283:247] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:516:618]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+11:362:517] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:824]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:732] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:566:566] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:775]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:499:460] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:583:947]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+10:162:452] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:031]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+30:263:239] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+26:933:372]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+38:733:425] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:232:873]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:399:930] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:300:198]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+52:065:914] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:828]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:794] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:732]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:849:382]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:100:667] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:666:725]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:366:021] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+50:063:585]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:819] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:253]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:831] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:703]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:716:014] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:116:467] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:499:664]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:850:049] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:328]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:116] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:433:355] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:383:973]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:566:305] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:752]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+22:682:150] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:298]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:842] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:832]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:100:701] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:843]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:382:389] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:767]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:100:202] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:751]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+20:778:279] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:832]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:829] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:705]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:600:358]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:006] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:167:232]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:533:573] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:709:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:771] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+15:897:774]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+36:375:106] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:577]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:242]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:848] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+12:499:486]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:300:030]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:565:771] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:950:356]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+26:132:753] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:700:326]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:216:421] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:916:573]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+16:049:744] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:617:079]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:779] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:400:492]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:149:971] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:062]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:667:161] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:799:663]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:149:600] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:333:970]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:399:494] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:183:077]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:133:642] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:699:990]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:766:948] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+34:997:964]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:992]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+12:966:326] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:688]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:466:137] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:750:565]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+14:016:204] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:114]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:049:867] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:917:018]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:482:708] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:600:410]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:683:192] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+09:128:146]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:751] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+14:913:580]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:550:715] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:791]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:100:732] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+13:765:703] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:183:509]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:332:463] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:317:151]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+07:533:357] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:848]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:684]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+23:083:239] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:166:065]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:333:287] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:316:345]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+05:367:359] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:859]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:116:661] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:700:628]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:183:158] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:333:736]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:333:110] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+59:663:639]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:688]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+07:999:350] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:286]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:200:551] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:666:741]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+37:232:281] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:820]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:583:157] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:049:020] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:450:795]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+23:979:954] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:705]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+21:033:319] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+31:149:758]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:750] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:082:376]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+37:381:171] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:329]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:084:086] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:058] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:133:224]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:774] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:249:464]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:433:901] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+24:696:389]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min


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MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+17:916:488]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:148:770] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:814]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:341]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:966:747]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:779] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:400:779]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:516:024] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+32:078:958]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:752] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+52:933:626]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:045:214] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:716:928]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:533:373] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:675:484]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:031:242] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:031:243]
*****Exiting SetMode***** ^ [+00:171:856]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:015:621] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:046:843]
*****Exiting SetMode***** ^ [+27:340:303]
{DispCLK:100000kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:191:006] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:000:000] {DispCLK:100000kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:015:601] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:015:621] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:621]
*****Exiting SetMode***** ^ [+00:000:000]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:015:621] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:622]
*****Exiting SetMode***** ^ [+00:468:640]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ *** Log File Version: 2.0 ***
[+00:000:000] {DispCLK:0kHz, Min SCLK:0kHz, Min

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MCLK:0kHz} ^ [+00:249:920] [Conn_Hotplug] [DVIDL-1] -
Connected ^ [+00:000:000] [Conn_Detect] [DVIDL-1]
PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF 00 26 CD
16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6 56 48 9A
24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9 40 B3 00
01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11 00 00 1E
00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30 00 00 00
FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00 FC 00 50
4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^ [+00:000:000]
[Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y Dim:270mm] ^
[+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:656:123]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:596]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:864] *****Exiting
SetMode***** ^ [+00:109:349] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:546:512]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+37:578:261] [Conn_Num_Display] 2 ^
[+00:197:845] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:008:977] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:005:499] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2043mm, Y Dim:252365728mm] ^
[+00:019:483] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:011:023] [Conn_Disp_Year]
[UNKNOWN] - [2017] ^ [+00:012:984]
[Conn_Disp_Color_Depth] [UNKNOWN] - 101010 ^
[+00:001:995] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+22:603:342] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+12:655:087] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+18:698:981]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:483:964] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:149:178]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+36:649:744] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:216:967]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+12:299:787] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:699:825]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:799:752] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+08:216:297]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+30:858:722] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:432:831]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+30:200:278] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+31:599:061]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:500:329] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+31:865:858]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+58:063:188] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:216:366]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+26:408:178] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+29:346:804]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+56:295:752] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:238]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:766:712] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:948] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:657:016]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:600]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:615]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:062:509] *****Exiting
SetMode***** ^ [+00:078:083] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:565:111]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+29:127:536] [Conn_Num_Display] 2 ^
[+00:000:997] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:995] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:997] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2048mm, Y Dim:2141145888mm] ^
[+00:000:998] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:994] [Conn_Dis_Year]
[UNKNOWN] - [2017] ^ [+00:000:997]
[Conn_Dis_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:998] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:960] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dis_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dis_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:642:335]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:622]
[Conn_Signal_Type] [DVIDL-1] - DVI SL ^ [+00:015:640]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:844] *****Exiting
SetMode***** ^ [+00:093:752] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:553:819]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ *** Log File Version: 2.0 ***
[+00:000:000] {DispCLK:0kHz, Min SCLK:0kHz, Min
MCLK:0kHz} ^ [+00:249:940] [Conn_Hotplug] [DVIDL-1] -
Connected ^ [+00:000:000] [Conn_Detect] [DVIDL-1]
PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF 00 26 CD
16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6 56 48 9A
24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9 40 B3 00
01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11 00 00 1E

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00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30 00 00 00
FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00 FC 00 50
4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^ [+00:000:000]
[Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y Dim:270mm] ^
[+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:640:461]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:615]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:650]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:868] *****Exiting
SetMode***** ^ [+00:093:695] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:949:966]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+32:423:205] [Conn_Num_Display] 2 ^
[+00:001:993] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:998] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:995] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2041mm, Y Dim:560401792mm] ^
[+00:000:997] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:995] [Conn_Disp_Year]
[UNKNOWN] - [2017] ^ [+00:000:997]
[Conn_Disp_Color_Depth] [UNKNOWN] - 101010 ^
[+00:001:994] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+36:684:029] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+41:604:339] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:200:668]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:665:701] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:000:371]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:967:108] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+48:381:062]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+12:733:115] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+26:001:003]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:779] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:775]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+43:916:206] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:316:154]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+10:515:728] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:834] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:720]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:600:597] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:751]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:783:976] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:399:439]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+36:212:338] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:327]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:825] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:450:460]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:284] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:149:938]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:516:490] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:179:235]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:329]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:687]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+06:384:047] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:632]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:183:935] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:500:170]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+18:745:615] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:327]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:775] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:825]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:716:691]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:299:815]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+00:482:708] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:617:229]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:824]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:962] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:117:196]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+12:883:235] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:449:196]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:633:793] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:516:909]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+30:412:032] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:133:775] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:315:948]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:775] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:382:931]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:550:527] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+51:431:474]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:769] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+44:206:367]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:405] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:316:433] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:300:197]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+35:291:399] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:073]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:265]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:482:974] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:266:274] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:482:720]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:716:877] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]

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{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:746]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:916:968] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:833:526]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:066:023] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:299:626]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+25:343:984] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:766]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+54:945:026] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:816:786]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+16:016:241] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:334:133]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:332:640] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+14:666:687]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:827]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:760] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:766:360]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:796] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:640:454]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min

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SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:650]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:594]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:872] *****Exiting
SetMode***** ^ [+00:078:136] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:577:715]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+37:747:932] [Conn_Num_Display] 2 ^
[+00:084:953] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:995] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:996] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2045mm, Y Dim:-184614896mm] ^
[+00:001:026] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:985] [Conn_Dispatch_Year]
[UNKNOWN] - [2017] ^ [+00:001:995]
[Conn_Dispatch_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:998] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:940] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:650:526]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:031:244]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:629]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:925] *****Exiting
SetMode***** ^ [+00:078:094] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:577:736]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+31:870:991] [Conn_Num_Display] 2 ^
[+00:001:994] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:998] [Conn_Detect] [UNKNOWN] DDC Display: [EDID

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Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:994] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2044mm, Y Dim:-1267676848mm] ^
[+00:000:997] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:995] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:000:998]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:997] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:673:650]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:621]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:622]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:876] *****Exiting
SetMode***** ^ [+00:125:021] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:532:321]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+34:941:313] [Conn_Num_Display] 2 ^
[+00:022:939] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:994] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:995] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2046mm, Y Dim:-1038936912mm] ^
[+00:005:984] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:996] [Conn_Displ_Year]

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[UNKNOWN] - [2017] ^ [+00:001:993]
[Conn_Disp_Color_Depth] [UNKNOWN] - 101010 ^
[+00:003:989] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+32:847:486] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+10:949:860] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+21:475:605]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1600x900, 1600x1200@60:0,
2160x1250@162000Khz} ^ [+00:083:777] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:017:952] {DispCLK:171428kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:037:863]
*****Exiting SetMode***** ^ [+01:179:162]
{DispCLK:171428kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+05:215:320] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:083:776] [Conn_Signal_Type] [DVIDL-1] - DVISL ^
[+00:017:960] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:929] *****Exiting
SetMode***** ^ [+01:182:086] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+12:765:284]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1600x900, 1600x1200@60:0,
2160x1250@162000Khz} ^ [+00:082:788] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:017:943] {DispCLK:171428kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:904]
*****Exiting SetMode***** ^ [+01:165:128]
{DispCLK:171428kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+26:247:717] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1680x1050, 1680x1050@60:0, 2240x1089@146250Khz} ^
[+00:083:776] [Conn_Signal_Type] [DVIDL-1] - DVISL ^
[+00:017:951] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:905] *****Exiting
SetMode***** ^ [+00:214:498] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:151:327]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+54:768:206] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:083:283] [Conn_Signal_Type] [DVIDL-1] - DVISL ^
[+00:017:952] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:932] *****Exiting
SetMode***** ^ [+00:249:458] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+35:312:615]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:966:469] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:329]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:824] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:732]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:065:823] {DispCLK:156521kHz, Min

```

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+35:016:350]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:734] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:383:975]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:432:842] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+35:203:152]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:983:367] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+37:032:881]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:735] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+27:333:128] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:450:076] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:517:317]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+13:281:759] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:758]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+16:946:033] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:566:122]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:283:896] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:186]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:067:073] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:600]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:285] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:839]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+36:582:343] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:417:408] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:432:864]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+48:488:542] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:730]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+50:144:655] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:083:464]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:516:476] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:777]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+00:065:852] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:794]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:833:019] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:267:285]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:415:800] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:317:159]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+42:933:109] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:328]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:067:199] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:796]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:237] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:324]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+29:699:528] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:952]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:383:483] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:416:886]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+17:573:116] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:777]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:663:622] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:633:478]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:982:570] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:159]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:736] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:399:927]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:350:122] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+58:422:886]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:330] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+33:441:493]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+25:382:679] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+26:967:300]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+39:066:028] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+33:949:725]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:183:054] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:000:563]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:820] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+19:882:742]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:316:598] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:066:827]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:933:363] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:013]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+32:465:464] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:800:084]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+31:416:013]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:733] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:966:486]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+08:317:235] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:049:867]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+38:232:862] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:299:972]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:049:867] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+26:349:665]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+03:233:976] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:482:710] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:567:125]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:315] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:316:317]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+15:266:102] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+26:266:336]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+31:132:977] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:050:731]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:899:599] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:833:707]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+26:332:453] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:049:867]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:233:065] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:267:442]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:766:275] {DispCLK:156521kHz, Min

SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:599:325]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+29:700:169]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+28:866:779] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+11:216:326]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:200:304] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+51:233:267] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+19:183:125]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+06:765:993] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:517:024]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+49:946:682] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:240]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:782:908] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:333:109]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:783:697] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+45:716:431]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:800:220] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:796:199]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:933:850] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+20:683:145]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+02:400:185] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:983:347]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+24:616:182]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+08:966:886] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:300:043]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:065:823] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+37:769:422]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+08:583:533] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:182:742]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+06:933:562] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:265:906]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+26:687:913] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+20:094:961]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

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MCLK:300000kHz} ^ [+57:861:967] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:316:970]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:316:578]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ *** Log File Version: 2.0 ***
[+00:000:000] {DispCLK:0kHz, Min SCLK:0kHz, Min
MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug] [DVIDL-1] -
Connected ^ [+00:000:000] [Conn_Detect] [DVIDL-1]
PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF 00 26 CD
16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6 56 48 9A
24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9 40 B3 00
01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11 00 00 1E
00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30 00 00 00
FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00 FC 00 50
4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^ [+00:000:000]
[Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y Dim:270mm] ^
[+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:640:456]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:621]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:646]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:840] *****Exiting
SetMode***** ^ [+00:078:106] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:577:756]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+55:833:454] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:046:884] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:046:941] *****Exiting
SetMode***** ^ [+03:350:866] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:031:264] [Conn_Signal_Type] [DVIDL-1] - DVISL ^
[+00:015:472] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:062:674] *****Exiting
SetMode***** ^ [+00:140:408] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+40:622:469]
[Conn_Num_Display] 2 ^ [+00:001:995] [Conn_Hotplug]
[UNKNOWN] - Connected ^ [+00:000:997] [Conn_Detect]
[UNKNOWN] DDC Display: [EDID Block 0] 00 FF FF FF FF FF

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FF 00 05 A4 01 00 01 00 00 00 14 1B 01 04 B1 4F 00 FF 00 A5 95
AA 54 4F A1 26 0A 50 54 00 00 00 01 01 01 01 01 01 01 01 01
01 01 01 01 01 01 64 00 40 A0 00 40 13 00 58 2C 45 00 55 50 21
00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
10 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 C6 ^
[+00:001:995] [Conn_PHY_SIZE] [UNKNOWN] [X
Dim:-2041mm, Y Dim:-5396064mm] ^ [+00:003:989]
[Conn_native_mode] [UNKNOWN] {64x64@54Hz} ^
[+00:010:971] [Conn_Dispatch_Year] [UNKNOWN] - [2017] ^
[+00:001:995] [Conn_Dispatch_Color_Depth] [UNKNOWN] - 101010
^ [+00:000:997] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+39:728:404] *****Entering SetMode***** ^
[+00:000:000] [Conn_Mode] [DVIDL-1] {1920x1080,
1920x1080@60:1, 2200x1125@148500Khz} ^ [+00:005:984]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:014:960] *****Exiting
SetMode***** ^ [+00:068:815] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:955] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:905] *****Exiting
SetMode***** ^ [+00:191:488] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:005:985] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:906] *****Exiting
SetMode***** ^ [+00:131:648] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:813:843]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:954] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:957]
*****Exiting SetMode***** ^ [+00:334:107]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+11:905:206] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:011:967] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:906] *****Exiting
SetMode***** ^ [+00:064:837] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:015:948] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:906] *****Exiting
SetMode***** ^ [+00:110:213] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:005:984] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:037:203] *****Exiting
SetMode***** ^ [+00:113:063] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+48:030:757]
*****Entering SetMode***** ^ [+00:000:000]

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[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:017:952] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:012:965]
*****Exiting SetMode***** ^ [+00:220:427]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:617:964] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:014:961] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:027:955] *****Exiting
SetMode***** ^ [+00:037:900] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:017:921] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:947] *****Exiting
SetMode***** ^ [+00:089:717] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:007:008] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:875] *****Exiting
SetMode***** ^ [+00:146:629] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+34:465:341]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:955] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:016:956]
*****Exiting SetMode***** ^ [+00:167:608]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:319:166] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:012:966] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:018:948] *****Exiting
SetMode***** ^ [+00:047:896] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:961] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:875] *****Exiting
SetMode***** ^ [+00:167:552] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:012:996] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:903] *****Exiting
SetMode***** ^ [+00:097:740] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+45:248:519]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:955] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:020:944]
*****Exiting SetMode***** ^ [+00:180:028]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:452:753] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]

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{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:012:986] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:880] *****Exiting
SetMode***** ^ [+00:045:898] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:954] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:908] *****Exiting
SetMode***** ^ [+00:127:665] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:004:970] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:037:888] *****Exiting
SetMode***** ^ [+00:128:164] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:065:708]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:017:952] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:014:960]
*****Exiting SetMode***** ^ [+00:218:923]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:006:493] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:008:954] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:042:907] *****Exiting
SetMode***** ^ [+00:056:851] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:083:692] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:402] *****Exiting
SetMode***** ^ [+00:169:526] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:010:970] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:039:893] *****Exiting
SetMode***** ^ [+00:193:484] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+08:248:176]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:017:953] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:028:429]
*****Exiting SetMode***** ^ [+00:221:588]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:286:566] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:012:965] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:907] *****Exiting
SetMode***** ^ [+00:064:826] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:955] {DispCLK:156521kHz, Min SCLK:300000kHz,

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Min MCLK:300000kHz} ^ [+00:034:907] *****Exiting
SetMode***** ^ [+00:093:258] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:004:987] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:906] *****Exiting
SetMode***** ^ [+00:165:558] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:031:651]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:955] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:013:962]
*****Exiting SetMode***** ^ [+00:203:961]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+03:557:281] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:008:976] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:901] *****Exiting
SetMode***** ^ [+00:045:878] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:955] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:905] *****Exiting
SetMode***** ^ [+00:079:788] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:018:949] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:902] *****Exiting
SetMode***** ^ [+00:130:162] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+24:365:172]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:954] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:905]
*****Exiting SetMode***** ^ [+00:181:514]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+54:048:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:613]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:054] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:687]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+15:666:552] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:100:240]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:266:288] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:416:316]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:775] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:467]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+24:783:278] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:416:885] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+25:961:066]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+05:000:437] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:083:754] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+42:427:780]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+18:747:011] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+32:481:344]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+07:182:550] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:755]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:983:121] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+43:162:616]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+05:000:268] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+22:699:728]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+04:999:387] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+08:795:203]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+49:480:146] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+39:633:713]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+13:933:442] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:233:391]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:416:416] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+17:816:397]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+23:166:188] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:267:284]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:416:887] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+09:865:638]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:514] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:688]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min

MCLK:300000kHz} ^ [+05:100:150] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:283:838]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:432:828] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+46:331:702]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+11:682:184] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:825]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:367:019] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+23:366:141]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:776]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+43:333:006] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:216:422]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:333:108] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+57:471:964]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+03:913:809] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+11:249:761]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+16:000:108] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:813]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:432:888] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+12:966:542]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:822] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:687]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+06:316:390] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:919]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:617:369] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:258:243]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:779] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:688]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+05:400:085] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:283]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:200:667] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+44:780:708]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:775] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+25:100:266] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:167:495] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:532:653]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:709]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+12:216:591] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:350:064]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:533:655] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+32:246:226]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+25:431:176] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+18:899:418]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:457]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+06:417:433] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:823]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:716:912] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+46:619:747]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+21:863:937] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:616:543]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:477] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:456]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+37:149:344] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:735]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:399:930] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:833:467]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:775] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:300:959] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:778]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:583:469] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:151:482]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+05:061:373] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:330]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:351:060] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+29:912:701]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:066:821] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:116:688]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:666:852] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:200:268] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:216:683]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:779] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:775]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+12:532:895] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dis_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dis_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:656:097]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:620]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:622]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:907] *****Exiting
SetMode***** ^ [+00:096:903] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:562:171]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+30:711:386] [Conn_Num_Display] 2 ^
[+00:009:973] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:995] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:995] [Conn_PHY_SIZE]

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[UNKNOWN] [X Dim:-2044mm, Y Dim:661638160mm] ^
[+00:000:997] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:995] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:000:997]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:001:995] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+35:756:524] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:066:823] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+49:073:369]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:007:980] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:016:954]
*****Exiting SetMode***** ^ [+00:049:867]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:954] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:904]
*****Exiting SetMode***** ^ [+00:090:268]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:006:981] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:904]
*****Exiting SetMode***** ^ [+00:181:270]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+50:847:680] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:973] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:013:963] *****Exiting
SetMode***** ^ [+00:203:963] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:630:142]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:018:950] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:037:898]
*****Exiting SetMode***** ^ [+00:044:881]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:954] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:035:905]
*****Exiting SetMode***** ^ [+00:094:746]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ *** Log File Version: 2.0 ***
[+00:000:000] {DispCLK:0kHz, Min SCLK:0kHz, Min
MCLK:0kHz} ^ [+00:249:940] [Conn_Hotplug] [DVIDL-1] -
Connected ^ [+00:000:000] [Conn_Detect] [DVIDL-1]
PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF 00 26 CD
16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6 56 48 9A
24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9 40 B3 00
01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11 00 00 1E
00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30 00 00 00

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FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00 FC 00 50
4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^ [+00:000:000]
[Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y Dim:270mm] ^
[+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:647:788]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:000:000]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:643]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:062:466] *****Exiting
SetMode***** ^ [+00:078:108] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:546:512]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+06:862:086] [Conn_Num_Display] 2 ^
[+00:004:986] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:998] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:994] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2045mm, Y Dim:1583111072mm] ^
[+00:000:998] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:005:984] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:010:971]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:001:994] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:960] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:656:096]
*****Entering SetMode***** ^ [+00:000:000]

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[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:601]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:876] *****Exiting
SetMode***** ^ [+00:093:715] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:552:563]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+29:002:782] [Conn_Num_Display] 2 ^
[+00:003:988] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:995] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:994] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2047mm, Y Dim:474761552mm] ^
[+00:000:997] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:995] [Conn_Dispatch_Year]
[UNKNOWN] - [2017] ^ [+00:001:995]
[Conn_Dispatch_Color_Depth] [UNKNOWN] - 101010 ^
[+00:002:992] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:940] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Dispatch_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Dispatch_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:640:469]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:602]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:031:242]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:897] *****Exiting
SetMode***** ^ [+00:078:136] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:533:976]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+46:398:841] [Conn_Num_Display] 2 ^

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[+00:029:920] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:994] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:998] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2044mm, Y Dim:-1226211248mm] ^
[+00:001:995] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:997] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:001:994]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:009:974] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+19:710:840] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+10:333:693] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:466:516]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:300:199] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:149:847]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:733:513] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:979:932]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:160:559] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:005:984] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:907] *****Exiting
SetMode***** ^ [+00:064:334] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:954] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:038:402] *****Exiting
SetMode***** ^ [+00:070:811] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:940] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -

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888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:647:043]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:000:000]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:571]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:062:488] *****Exiting
SetMode***** ^ [+00:099:743] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:530:883]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+33:350:542] [Conn_Num_Display] 2 ^
[+00:055:850] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:995] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:995] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2043mm, Y Dim:1548968544mm] ^
[+00:001:994] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:997] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:001:995]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:997] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+20:711:255] *****Entering SetMode***** ^
[+00:000:000] [Conn_Mode] [DVIDL-1] {1920x1080,
1920x1080@60:1, 2200x1125@148500Khz} ^ [+00:033:909]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:032:912] *****Exiting
SetMode***** ^ [+35:336:493] {DispCLK:100000kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:220:412]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:001:994] {DispCLK:100000kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:002:991]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:952]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:015:958] *****Exiting
SetMode***** ^ [+03:031:497] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:268:671] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:035:904] *****Exiting
SetMode***** ^ [+00:081:782] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:940] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF

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00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:624:827]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:031:260]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:605]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:864] *****Exiting
SetMode***** ^ [+00:088:776] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:608:997]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ *** Log File Version: 2.0 ***
[+00:000:000] {DispCLK:0kHz, Min SCLK:0kHz, Min
MCLK:0kHz} ^ [+00:204:959] [Conn_Hotplug] [DVIDL-1] -
Connected ^ [+00:000:998] [Conn_Detect] [DVIDL-1]
PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF 00 26 CD
16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6 56 48 9A
24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9 40 B3 00
01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11 00 00 1E
00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30 00 00 00
FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00 FC 00 50
4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^ [+00:001:997]
[Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y Dim:270mm] ^
[+00:000:995] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:997] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:001:995] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:998] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:997] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:002:992] [Conn_Num_Display] 1 ^ [+00:438:827]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:997] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:016:954]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:952]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:036:905] *****Exiting
SetMode***** ^ [+01:329:345] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:232:380] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:996] [Conn_Detect]

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[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:001:995] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:998] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:001:994] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:001:995] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:001:995] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:997] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:004:986] [Conn_Num_Display] 1 ^ [+00:440:822]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:997] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:006:982]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:951]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:035:906] *****Exiting
SetMode***** ^ [+00:463:760] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:967] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:531:097]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:622]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:639]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:031:245] *****Exiting
SetMode***** ^ [+00:078:115] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:195:724]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+04:089:723] [Conn_Num_Display] 2 ^
[+00:002:992] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:995] [Conn_Detect] [UNKNOWN] DDC Display: [EDID

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Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:995] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2048mm, Y Dim:-2014823904mm] ^
[+00:000:997] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:994] [Conn_Dispatch_Year]
[UNKNOWN] - [2017] ^ [+00:002:993]
[Conn_Dispatch_Color_Depth] [UNKNOWN] - 101010 ^
[+00:004:986] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+23:590:211] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:107:712] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:008:976] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:927] *****Exiting
SetMode***** ^ [+00:064:807] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:954] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:906] *****Exiting
SetMode***** ^ [+00:073:804] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:007:978] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:907] *****Exiting
SetMode***** ^ [+00:497:668] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+50:714:888]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:017:953] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:014:959]
*****Exiting SetMode***** ^ [+00:151:107]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:267:299] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:249:178]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:103:722] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:012:965] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:027:925] *****Exiting
SetMode***** ^ [+00:038:899] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:016:953] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:034:414] *****Exiting
SetMode***** ^ [+00:077:792] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^ ***

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Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:965] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:541:191]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:624]
[Conn_Signal_Type] [DVIDL-1] - DVI-DL ^ [+00:015:622]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:031:241] *****Exiting
SetMode***** ^ [+00:093:729] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:582:490]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+50:426:568] [Conn_Num_Display] 2 ^
[+00:186:027] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:001:995] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:001:031] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2041mm, Y Dim:-1125022544mm] ^
[+00:001:982] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:001:995] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:001:995]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:998] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+36:003:306] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+01:633:374] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:853]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:150:625] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+09:566:257]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:099:737] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:366:807]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min

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MCLK:300000kHz} ^ [+03:397:357] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:065:851]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:133:615] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:349:885]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:285] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:950:529]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:616:006] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:835]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:132:645] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+11:783:297]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:383:986] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:483:432]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+48:298:593] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:779]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:133:014] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+06:183:665]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:777] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:015:809]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+52:148:085] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:821]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:116:196] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:150:173]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:305] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:099:583]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+41:097:114] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:842]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:116:694] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:166:364]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:199:598]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+57:198:721] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+26:943:307]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+54:383:094] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:082:777]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:100:763] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+24:282:681]

{DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:083:776] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:432:842]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+01:600:179] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:819]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:133:644] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+08:149:756]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:166:062] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:434:190]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+38:299:046] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:794]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:149:600] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+20:033:977]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:265:796] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:809]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+09:983:181] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:083:775]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:734] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+04:400:511]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:266:287] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:466:753]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+33:097:431] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:792]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:099:734] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+18:950:574]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:777] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:000:228]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+05:333:323] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:066:822]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:100:186] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:783:276]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:749:915]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+14:870:582] {DispCLK:156521kHz, Min
 SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:067:301]
 {DispCLK:156521kHz, Min SCLK:300000kHz, Min
 MCLK:300000kHz} ^ [+00:133:152] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+03:616:858]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:082:778] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:283:120]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+44:464:009] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+59:586:042]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:031:242] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:031:263]
*****Exiting SetMode***** ^ [+00:156:195]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:031:241]
*****Exiting SetMode***** ^ [+27:219:541]
{DispCLK:100000kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:128:992] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:000:000] {DispCLK:100000kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:015:622] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:015:622] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:622]
*****Exiting SetMode***** ^ [+00:000:000]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:031:250] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:613]
*****Exiting SetMode***** ^ [+01:265:327]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:880:154] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+21:849:909]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+34:698:457] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+40:133:240]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:366:699] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+40:781:204]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:516:622] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+13:115:119]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+08:232:190] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:066:637]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+25:898:181] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:084:769] [Conn_Signal_Type] [DVIDL-1] - DVISL ^
[+00:016:958] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:033:915] *****Exiting

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SetMode***** ^ [+00:149:952] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:216:589]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:115:718] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:017:952] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:033:909] *****Exiting
SetMode***** ^ [+00:815:565] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:165:558]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:017:951] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:034:906]
*****Exiting SetMode***** ^ [+00:131:312]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:249:964] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+16:651:104]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:049:374] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:499:526]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:033:416] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:668:215]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:049:867] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:183:487]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:731:923] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+56:033:369]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:033:327] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+28:649:327]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:765:242] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+42:899:686]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+55:031:356] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+48:965:258]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+01:266:894] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+15:999:615]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+11:332:908] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+53:448:560]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:083:805] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:017:948] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:032:885]
*****Exiting SetMode***** ^ [+00:150:576]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:232:403] {DispCLK:156521kHz, Min

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SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:099:733]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:954] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:037:705]
*****Exiting SetMode***** ^ [+00:679:155]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:016:848] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:034:933]
*****Exiting SetMode***** ^ [+00:348:042]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:183:298] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+52:749:409]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+03:316:299] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+30:715:320]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+07:965:186] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:449:238]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+02:282:796] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:115:657]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+09:550:515] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:415:113]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+58:778:002] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:048:869]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+34:582:319] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+38:149:759]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+17:249:135] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:033:909]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+45:875:349] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:850:229]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+10:066:183] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:203:457] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:998] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]

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{1920x1080@60Hz} ^ [+00:001:009] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:001:983] [Conn_Num_Display] 1 ^ [+00:454:793]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:997] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:013:963]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:480]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:037:878] *****Exiting
SetMode***** ^ [+01:695:747] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:228:389] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:998] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:997] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:998] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:001:994] [Conn_Num_Display] 1 ^ [+00:460:368]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:013:962]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:952]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:036:901] *****Exiting
SetMode***** ^ [+00:413:896] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]

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[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:677:460]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:000:000]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:624]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:864] *****Exiting
SetMode***** ^ [+00:109:349] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+02:974:940]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+58:184:324] [Conn_Num_Display] 2 ^
[+00:180:517] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:000] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:998] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2041mm, Y Dim:772742560mm] ^
[+00:000:000] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:000] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:000:996]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:000] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:940] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:687:312]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:621]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:650]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min

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MCLK:300000kHz} ^ [+00:062:458] *****Exiting
SetMode***** ^ [+00:093:754] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+05:360:304]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+41:247:952] [Conn_Num_Display] 2 ^
[+00:000:998] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:000] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:000] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2041mm, Y Dim:-1800650144mm] ^
[+00:000:000] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:997] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:000:000]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:000] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+21:312:174] *****Entering SetMode***** ^
[+00:000:000] [Conn_Mode] [DVIDL-1] {1024x768,
1024x768@60:0, 1344x806@65000Khz} ^ [+00:066:987]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:935]
{DispCLK:100000kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:019:968] *****Exiting
SetMode***** ^ [+00:134:118] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1024x768, 1024x768@60:0, 1344x806@65000Khz} ^
[+00:012:966] {DispCLK:100000kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:022:938] *****Exiting
SetMode***** ^ [+01:027:082] {DispCLK:100000kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:281:676]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:049:348] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:017:958] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:014:952]
*****Exiting SetMode***** ^ [+00:202:430]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+26:568:749] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:084:746] [Conn_Signal_Type] [DVIDL-1] - DVISL ^
[+00:016:954] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:041:915] *****Exiting
SetMode***** ^ [+00:192:484] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:157:068]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:009:006] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:034:876]
*****Exiting SetMode***** ^ [+00:182:512]

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{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:678:871] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:003:989] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:036:902] *****Exiting
SetMode***** ^ [+00:246:341] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+07:274:874]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:046:862] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:661]
*****Exiting SetMode***** ^ [+00:171:796]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:062:486]
*****Exiting SetMode***** ^ [+10:395:236]
{DispCLK:100000kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:187:436] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:000:000] {DispCLK:100000kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:015:621] [Conn_Signal_Type]
[DVIDL-1] - DVISL ^ [+00:015:720] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:031:144]
*****Exiting SetMode***** ^ [+00:000:000]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:015:621] {DispCLK:156521kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:015:622]
*****Exiting SetMode***** ^ [+00:546:747]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+35:663:715] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:078:094] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:031:242] *****Exiting
SetMode***** ^ [+00:765:445] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:015:623] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:046:865] *****Exiting
SetMode***** ^ [+41:210:776] {DispCLK:100000kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:170:510]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:100000kHz,
Min SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:031:242]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:600]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:015:621] *****Exiting
SetMode***** ^ [+00:343:669] *****Entering

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SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^
[+00:000:000] {DispCLK:156521kHz, Min SCLK:300000kHz,
Min MCLK:300000kHz} ^ [+00:046:864] *****Exiting
SetMode***** ^ [+00:218:699] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:198:469] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:997] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:001:009] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:994] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:990] [Conn_Num_Display] 1 ^ [+00:456:822]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:976] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:007:978]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:017:953]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:037:405] *****Exiting
SetMode***** ^ [+18:179:959] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ *** Log File
Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Disp_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Disp_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:640:504]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:031:214]

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[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:641]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:854] *****Exiting
SetMode***** ^ [+00:093:752] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:499:617]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+46:871:736] [Conn_Num_Display] 2 ^
[+00:054:853] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:000] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:997] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2048mm, Y Dim:-1876419520mm] ^
[+00:000:000] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:000] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:000:998]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:000] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:249:941] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:656:407]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:642]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:623]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:062:491] *****Exiting
SetMode***** ^ [+00:078:104] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+10:498:744]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+32:501:646] [Conn_Num_Display] 2 ^
[+00:006:983] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:997] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B

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01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:000] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2047mm, Y Dim:1911216464mm] ^
[+00:000:000] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:998] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:000:000]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:000] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^
[+16:839:590] *****Entering SetMode***** ^
[+00:000:000] [Conn_Mode] [DVIDL-1] {1920x1080,
1920x1080@60:1, 2200x1125@148500Khz} ^ [+00:067:819]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:016:954]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:026:929] *****Exiting
SetMode***** ^ [+00:140:461] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+00:217:108]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:865:142] *****Entering
SetMode***** ^ [+00:000:000] [Conn_Mode] [DVIDL-1]
{1920x1080, 1920x1080@60:1, 2200x1125@148500Khz} ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:218:697] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:661:793]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:624]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:015:621]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:871] *****Exiting
SetMode***** ^ [+00:093:721] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:547:206]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+45:234:407] [Conn_Num_Display] 2 ^
[+00:000:997] [Conn_Hotplug] [UNKNOWN] - Connected ^

```

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[+00:000:000] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:998] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2046mm, Y Dim:-2060571744mm] ^
[+00:000:000] [Conn_native_mode] [UNKNOWN]
{64x64@54Hz} ^ [+00:000:998] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:000:000]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:996] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^ ***
Log File Version: 2.0 *** [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:218:695] [Conn_Hotplug]
[DVIDL-1] - Connected ^ [+00:000:000] [Conn_Detect]
[DVIDL-1] PLE2208HDD: [EDID Block 0] 00 FF FF FF FF FF FF
00 26 CD 16 56 66 2B 00 00 34 15 01 03 80 30 1B 78 2E 35 81 A6
56 48 9A 24 12 50 54 BF EF 80 71 4F 81 80 81 8F 95 00 95 0F A9
40 B3 00 01 01 02 3A 80 18 71 38 2D 40 58 2C 45 00 DD 0C 11
00 00 1E 00 00 00 FF 00 31 31 30 38 32 31 35 32 31 31 31 31 30
00 00 00 FD 00 37 4C 1E 54 11 00 0A 20 20 20 20 20 20 00 00 00
FC 00 50 4C 45 32 32 30 38 48 44 44 0A 20 20 00 32 ^
[+00:000:000] [Conn_PHY_SIZE] [DVIDL-1] [X Dim:480mm, Y
Dim:270mm] ^ [+00:000:000] [Conn_native_mode] [DVIDL-1]
{1920x1080@60Hz} ^ [+00:000:000] [Conn_HDCP_VER]
[DVIDL-1] 1.4 ^ [+00:000:000] [Conn_Displ_Year] [DVIDL-1] -
[2011] ^ [+00:000:000] [Conn_Displ_Color_Depth] [DVIDL-1] -
888 ^ [+00:000:000] [Conn_PSR_Support] [DVIDL-1] - PSR:0 ^
[+00:000:000] [Conn_Num_Display] 1 ^ [+00:656:069]
*****Entering SetMode***** ^ [+00:000:000]
[Conn_Mode] [DVIDL-1] {1920x1080, 1920x1080@60:1,
2200x1125@148500Khz} ^ [+00:000:000] {DispCLK:0kHz, Min
SCLK:0kHz, Min MCLK:0kHz} ^ [+00:015:621]
[Conn_Signal_Type] [DVIDL-1] - DVISL ^ [+00:031:255]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+00:046:905] *****Exiting
SetMode***** ^ [+00:078:053] {DispCLK:156521kHz, Min
SCLK:300000kHz, Min MCLK:300000kHz} ^ [+01:531:297]
{DispCLK:156521kHz, Min SCLK:300000kHz, Min
MCLK:300000kHz} ^ [+27:853:747] [Conn_Num_Display] 2 ^
[+00:000:000] [Conn_Hotplug] [UNKNOWN] - Connected ^
[+00:000:998] [Conn_Detect] [UNKNOWN] DDC Display: [EDID
Block 0] 00 FF FF FF FF FF FF 00 05 A4 01 00 01 00 00 00 14 1B
01 04 B1 4F 00 FF 00 A5 95 AA 54 4F A1 26 0A 50 54 00 00 00
01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 01 64 00 40 A0 00
40 13 00 58 2C 45 00 55 50 21 00 00 00 00 00 00 10 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 C6 ^ [+00:000:000] [Conn_PHY_SIZE]
[UNKNOWN] [X Dim:-2047mm, Y Dim:708125872mm] ^
[+00:000:000] [Conn_native_mode] [UNKNOWN]

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{64x64@54Hz} ^ [+00:000:000] [Conn_Displ_Year]
[UNKNOWN] - [2017] ^ [+00:000:997]
[Conn_Displ_Color_Depth] [UNKNOWN] - 101010 ^
[+00:000:000] [Conn_PSR_Support] [UNKNOWN] - PSR:1 ^