

Table Edited et arrangement by Fred

The diagram shows a musical staff with four lines. The sequence of notes and rests is as follows:

- Measure 1: Note with flag at the first space (F), number 2 below.
- Measure 2: Note with flag at the second space (G), number 1 above.
- Measure 3: Note with flag at the third space (A), number 0 above.
- Measure 4: Note with flag at the fourth space (B), number 0 above.
- Measure 5: Rest.
- Measure 6: Note with flag at the first space (F), number 1 above.
- Measure 7: Note with flag at the second space (G), number 0 above.
- Measure 8: Note with flag at the third space (A), number 2 above.
- Measure 9: Note with flag at the fourth space (B), number 2 above.
- Measure 10: Note with flag at the first space (F), number 1 above.
- Measure 11: Note with flag at the second space (G), number 0 above.
- Measure 12: Note with flag at the third space (A), number 3 above.
- Measure 13: Note with flag at the fourth space (B), number 1 above.
- Measure 14: Note with flag at the first space (F), number 0 above.
- Measure 15: Note with flag at the second space (G), number 1 above.
- Measure 16: Note with flag at the third space (A), number 0 above.
- Measure 17: Rest.
- Measure 18: Note with flag at the first space (F), number 0 below.
- Measure 19: Note with flag at the second space (G), number 2 above.
- Measure 20: Note with flag at the third space (A), number 3 above.
- Measure 21: Note with flag at the fourth space (B), number 1 above.
- Measure 22: Note with flag at the first space (F), number 0 above.
- Measure 23: Note with flag at the second space (G), number 3 above.
- Measure 24: Note with flag at the third space (A), number 0 above.

Figure 1 illustrates the structure of a 16-bit instruction. The instruction is divided into three main fields: a 4-bit opcode field (bits 12-15), a 4-bit register index field (bits 8-11), and a 12-bit operand field (bits 0-7). The operand field is further divided into a 4-bit register index field (bits 4-7) and an 8-bit operand field (bits 0-3). The register index field is labeled 'H' and 'Po'. The operand field is labeled 'H' and 'Po'. The diagram shows the bit positions and the corresponding values for each field.

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