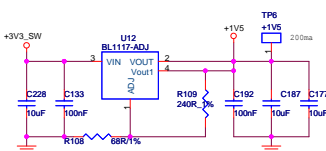
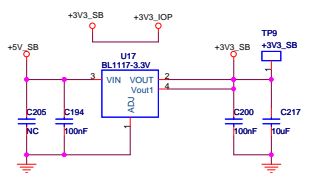
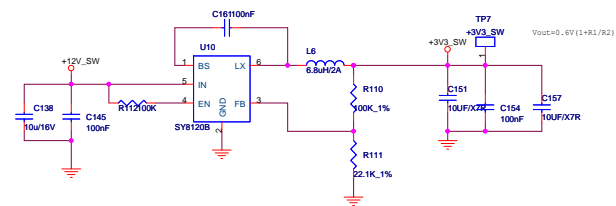
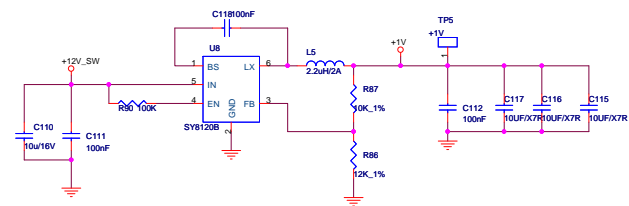
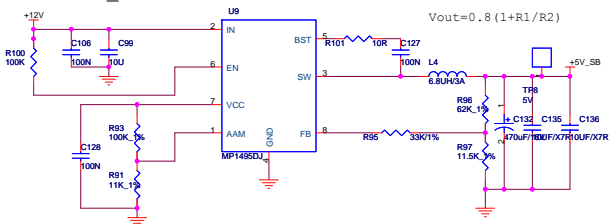
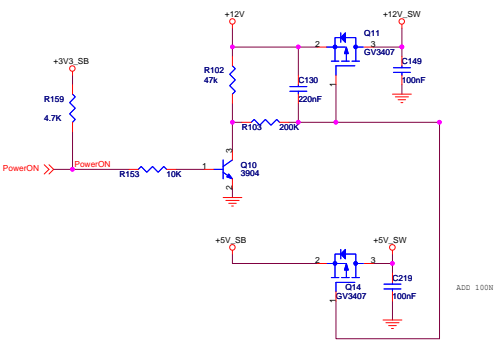
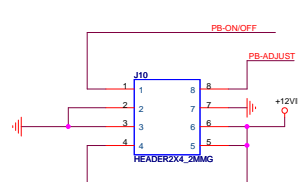
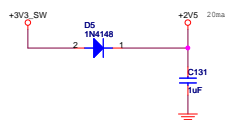
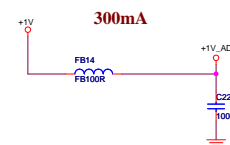


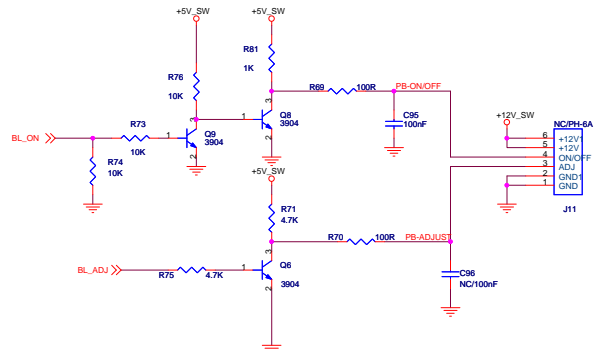
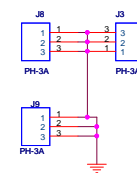
+5V_SB

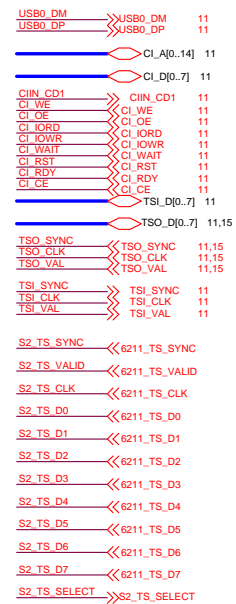
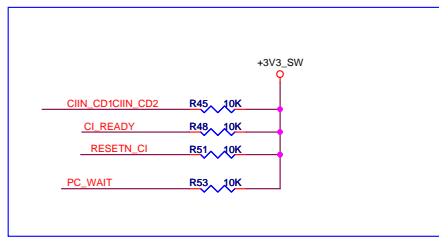
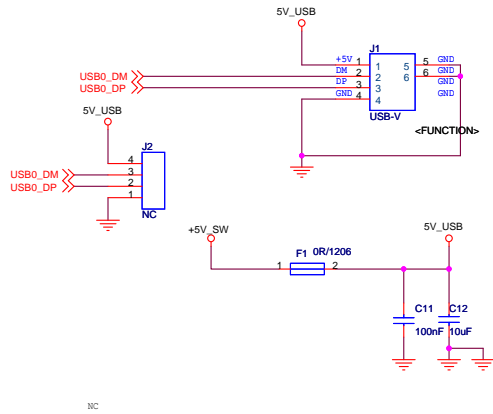
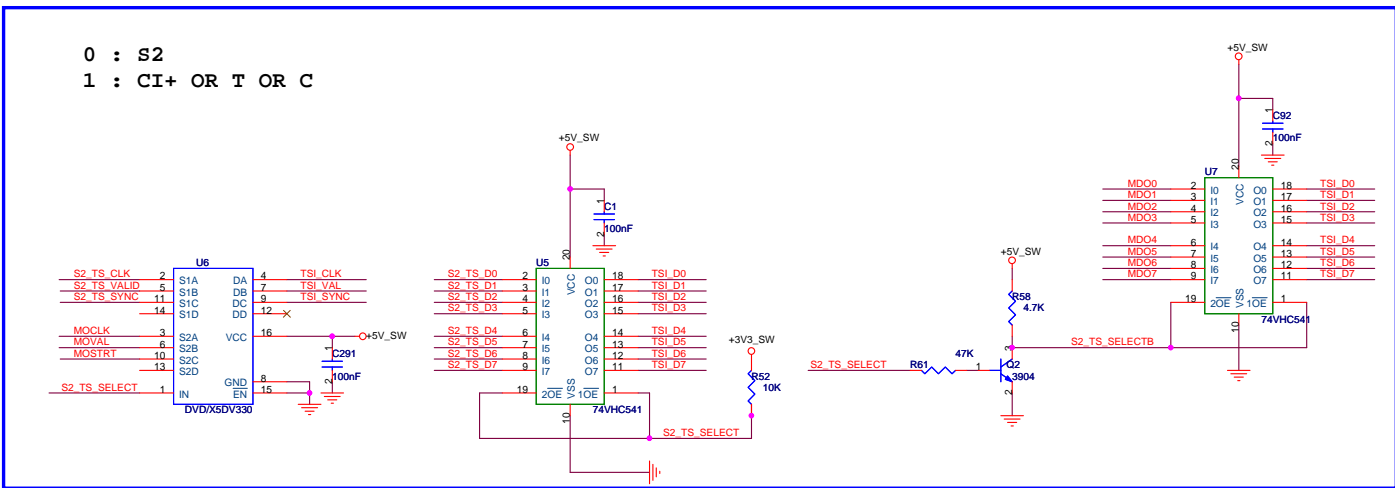
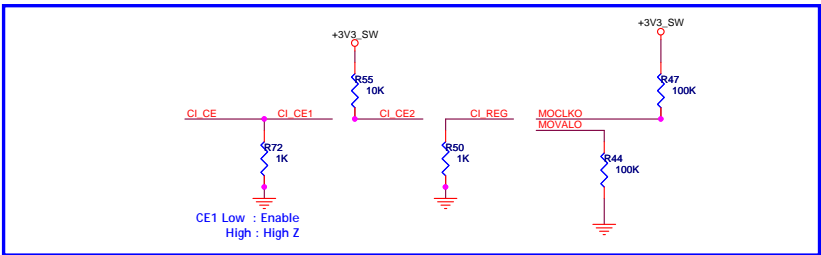
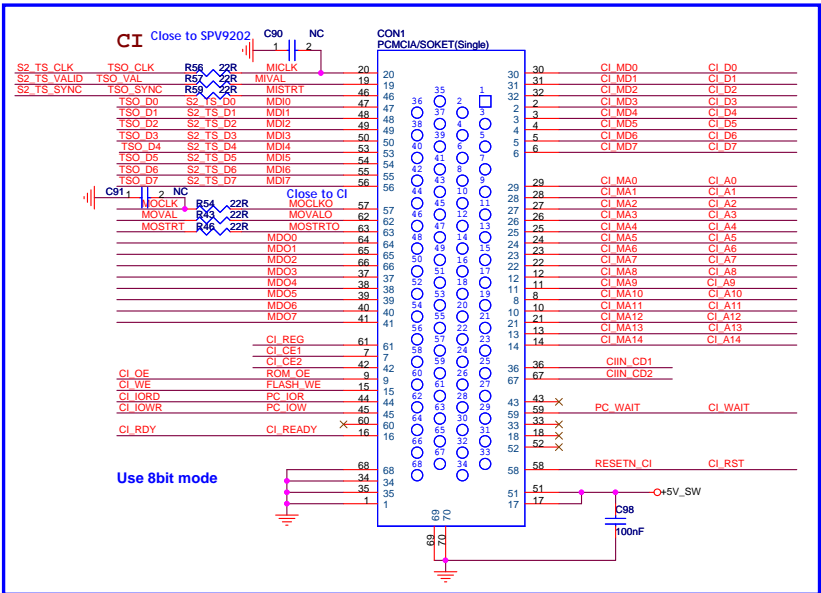


ADC Power

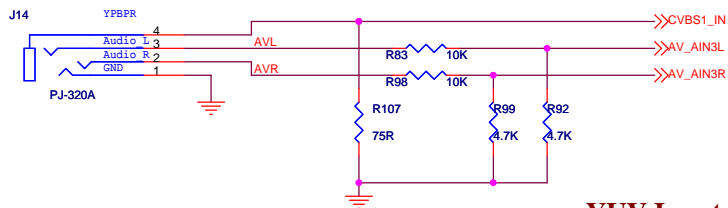


POWER CONNECTOR

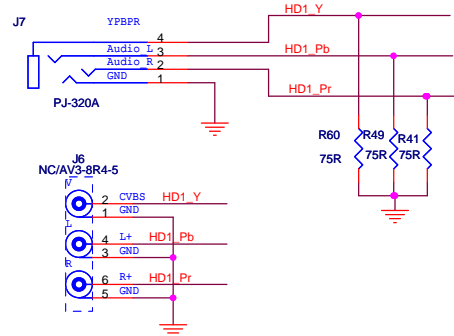




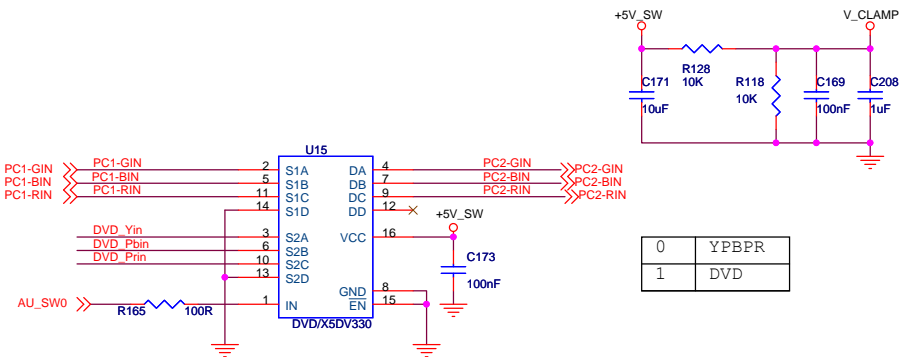
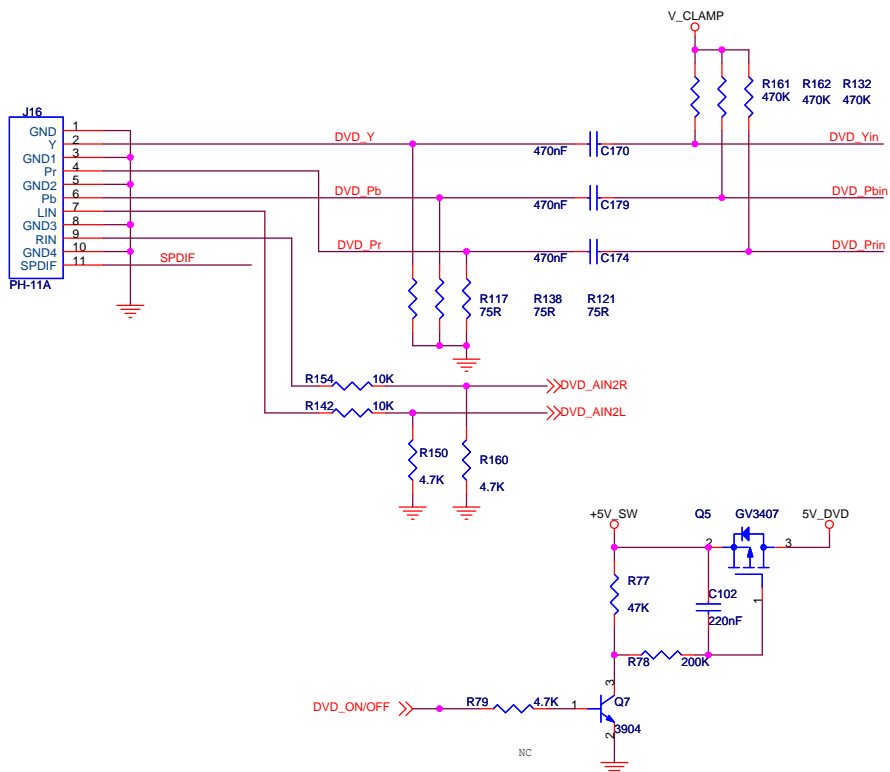
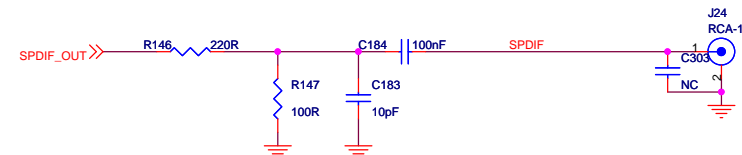
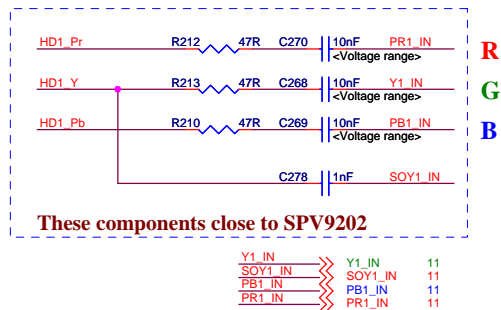
AV Input



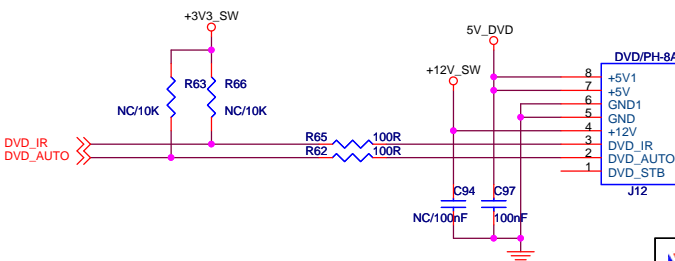
YPBPR

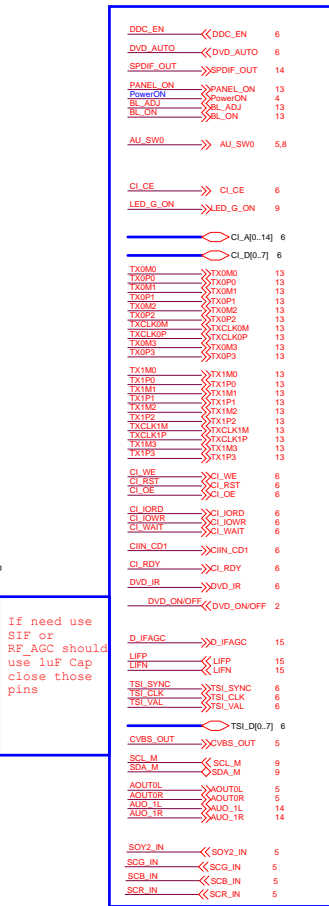
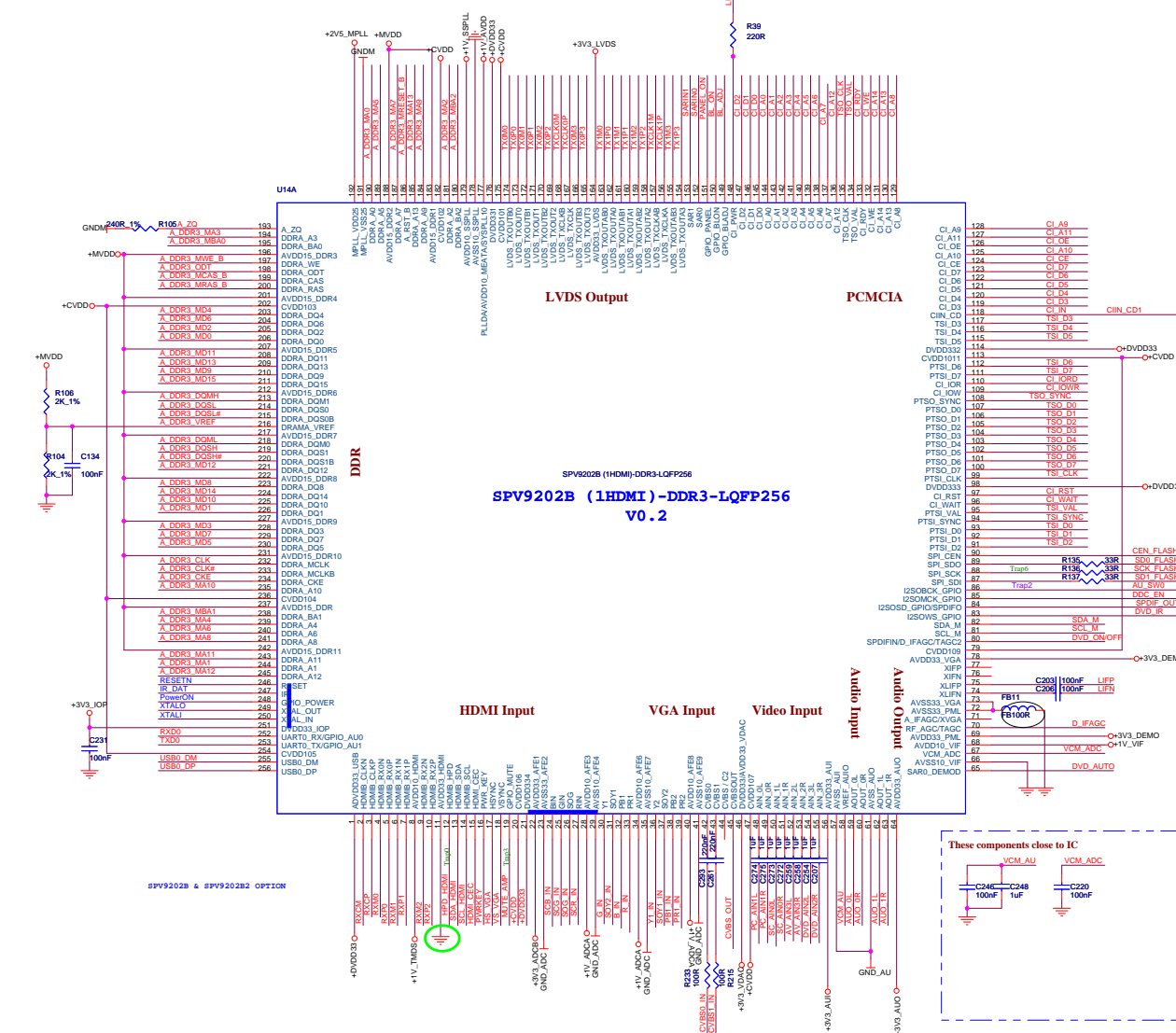
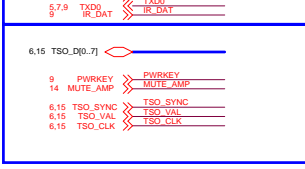
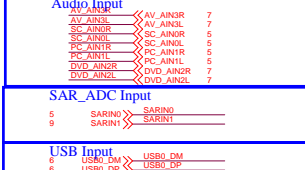
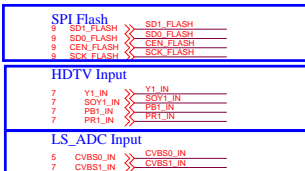
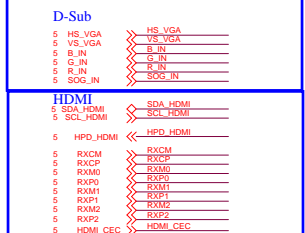
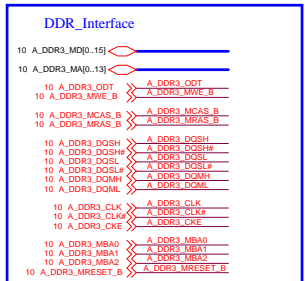
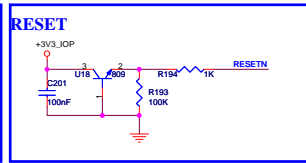
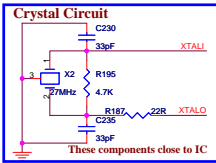
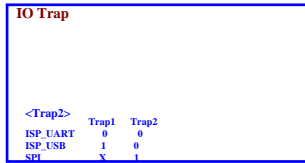


YUV Input



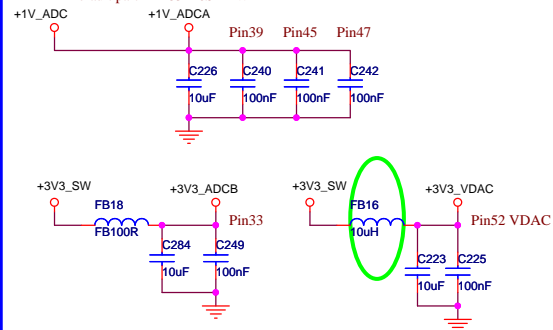
0	YPBPR
1	DVD



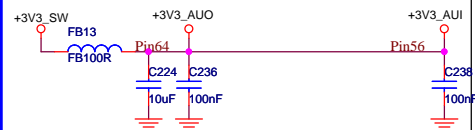


ADC POWER/GND (1.0V/3.3V)

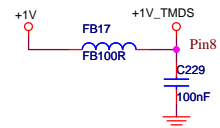
FB for ADC need $R_{dc} < 0.015$, $Z = 120/100\text{MHz}$, $I_{dc} > 3A$
Default part MHC3216S121W



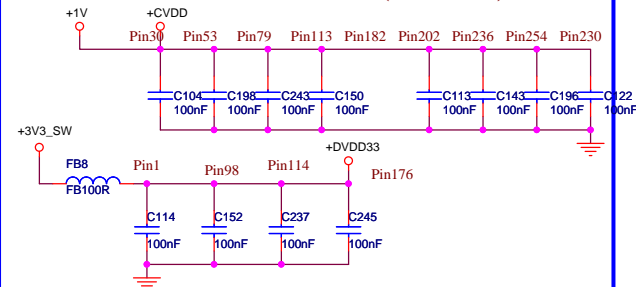
AUDIO POWER/GND (3.3V)



TMDS POWER/GND (1.0V/3.3V)

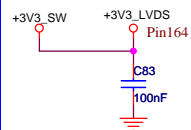


SOURCE POWER/GND (1.0V/3.3V)

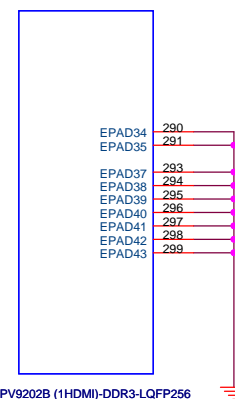


ALL FB except ADC, CVDD : need $R_{dc} < 0.2$, $Z = 300/100\text{MHz}$, $I_{dc} > 500\text{mA}$
Default part : MCB2012S301H

LVDS PWR

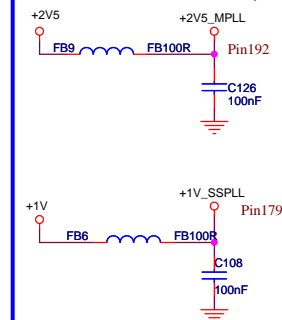


SPV9202E E-Pad

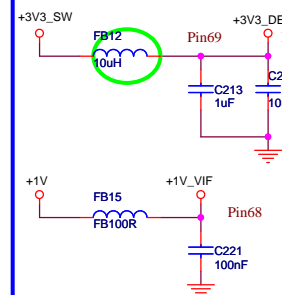


SPV9202B (1HDMI)-DDR3-LQFP256

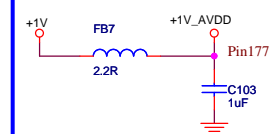
PLL POWER/GND (1.0V/2.5V)



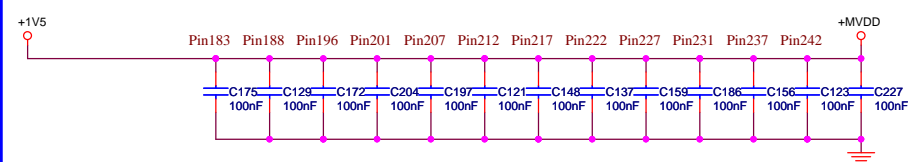
DEMO/VIF PWR



SYSTEM PLL

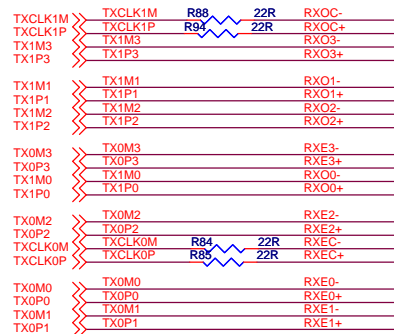


DDR-3 Power

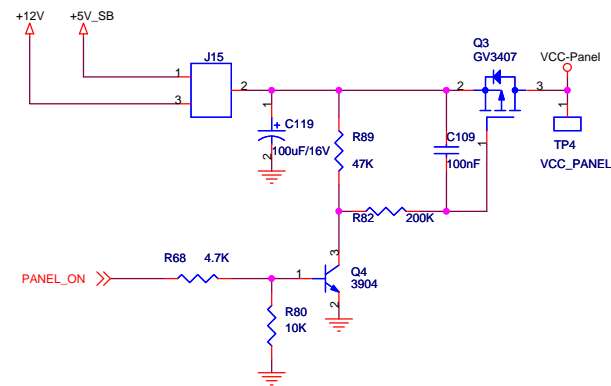
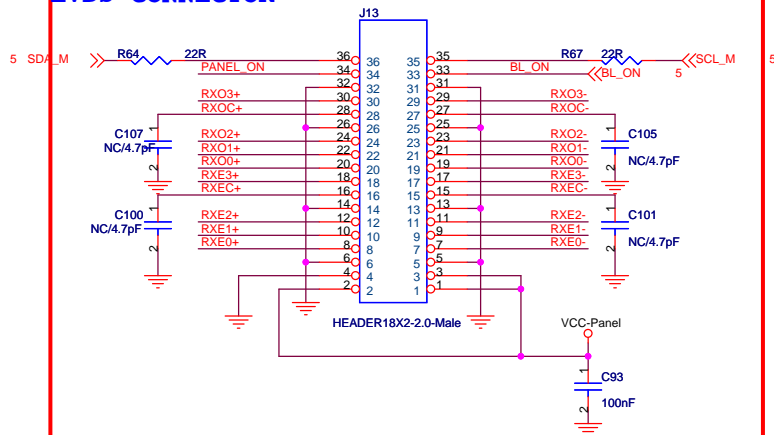


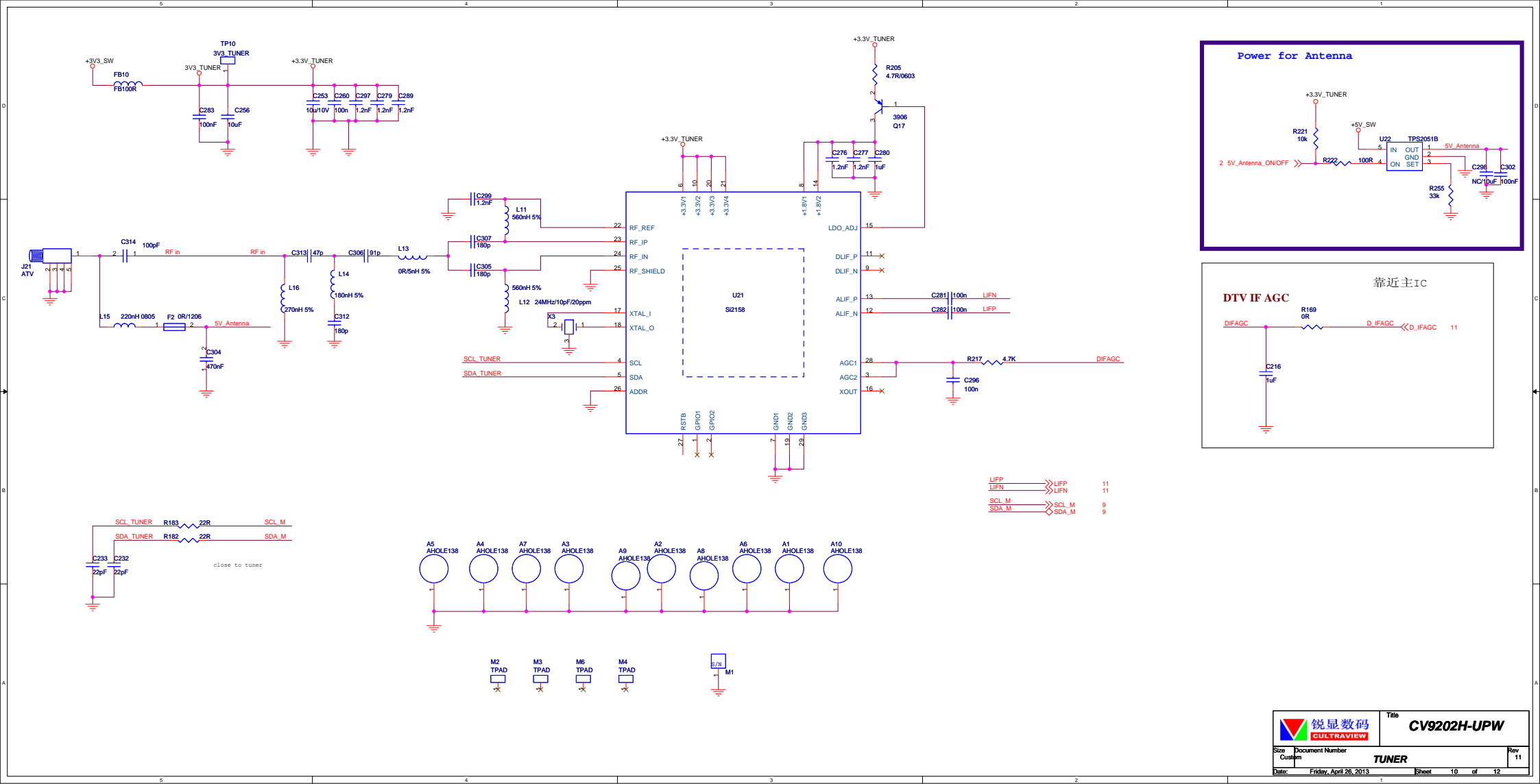
*Short these ground planes on PCB

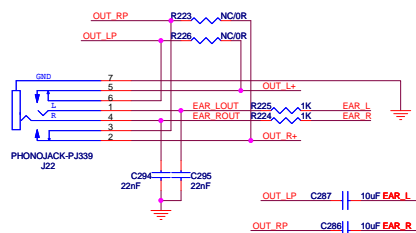
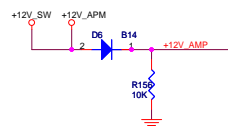
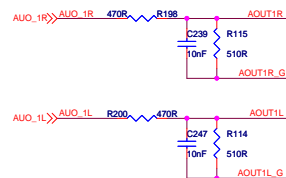




LVDS CONNECTOR

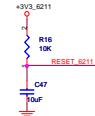






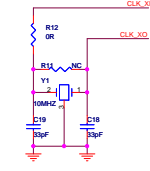
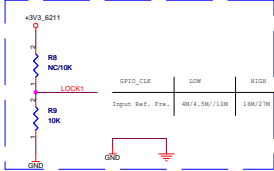
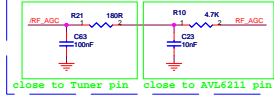
AVL6211 MAIN

RESTE Circuit

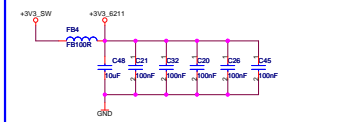


GPIO0 - S2 TS SELECT

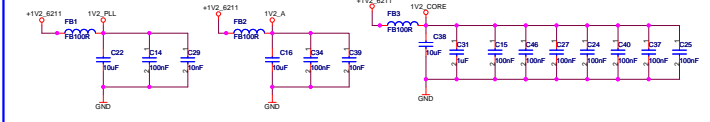
AGC CONTROL Circuit



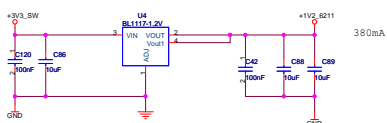
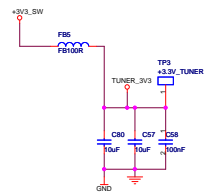
+3V3 Power Circuit(max 20mA)



+1V2 Power Circuit(max 400mA)



The Power of SPHE6211



G211_TS_SYNC << G211_TS_SYNC
 G211_TS_VALID << G211_TS_VALID
 G211_TS_CLK << G211_TS_CLK
 G211_TS_D0 << G211_TS_D0
 G211_TS_D1 << G211_TS_D1
 G211_TS_D2 << G211_TS_D2
 G211_TS_D3 << G211_TS_D3
 G211_TS_D4 << G211_TS_D4
 G211_TS_D5 << G211_TS_D5
 G211_TS_D6 << G211_TS_D6
 G211_TS_D7 << G211_TS_D7
 S2_TS_SELECT << S2_TS_SELECT
 SCL_M << SCL_M
 SDA_M << SDA_M

LNB_POER_CONTROL

