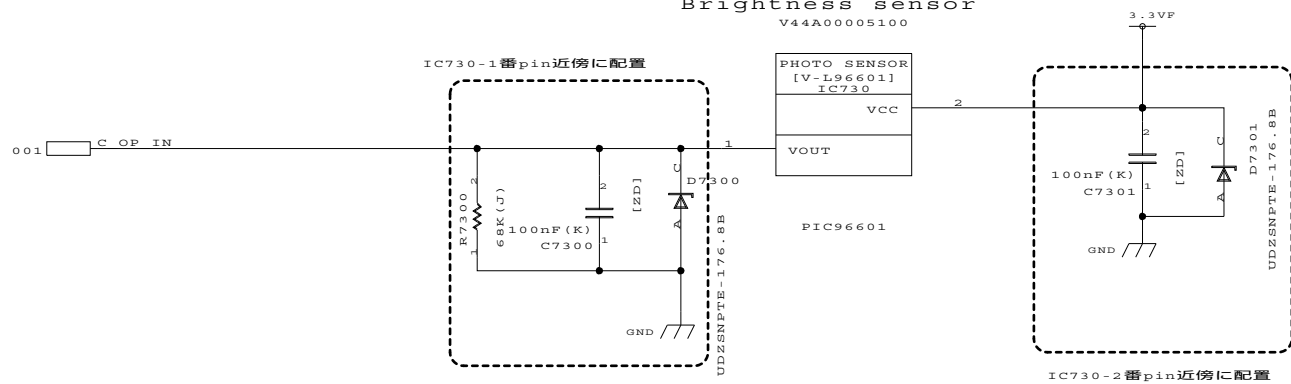


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T. IMURA	2010_LED_RMT	RMT	1	1	00	???

2011.07.26 11:45

Brightness sensor

V44A00005100



DESIGNED BY

T. IMURA

TITLE

2011_LED_RMT

FUNCTION

LED SENSOR

SH. NO.

2

PAGE NO.

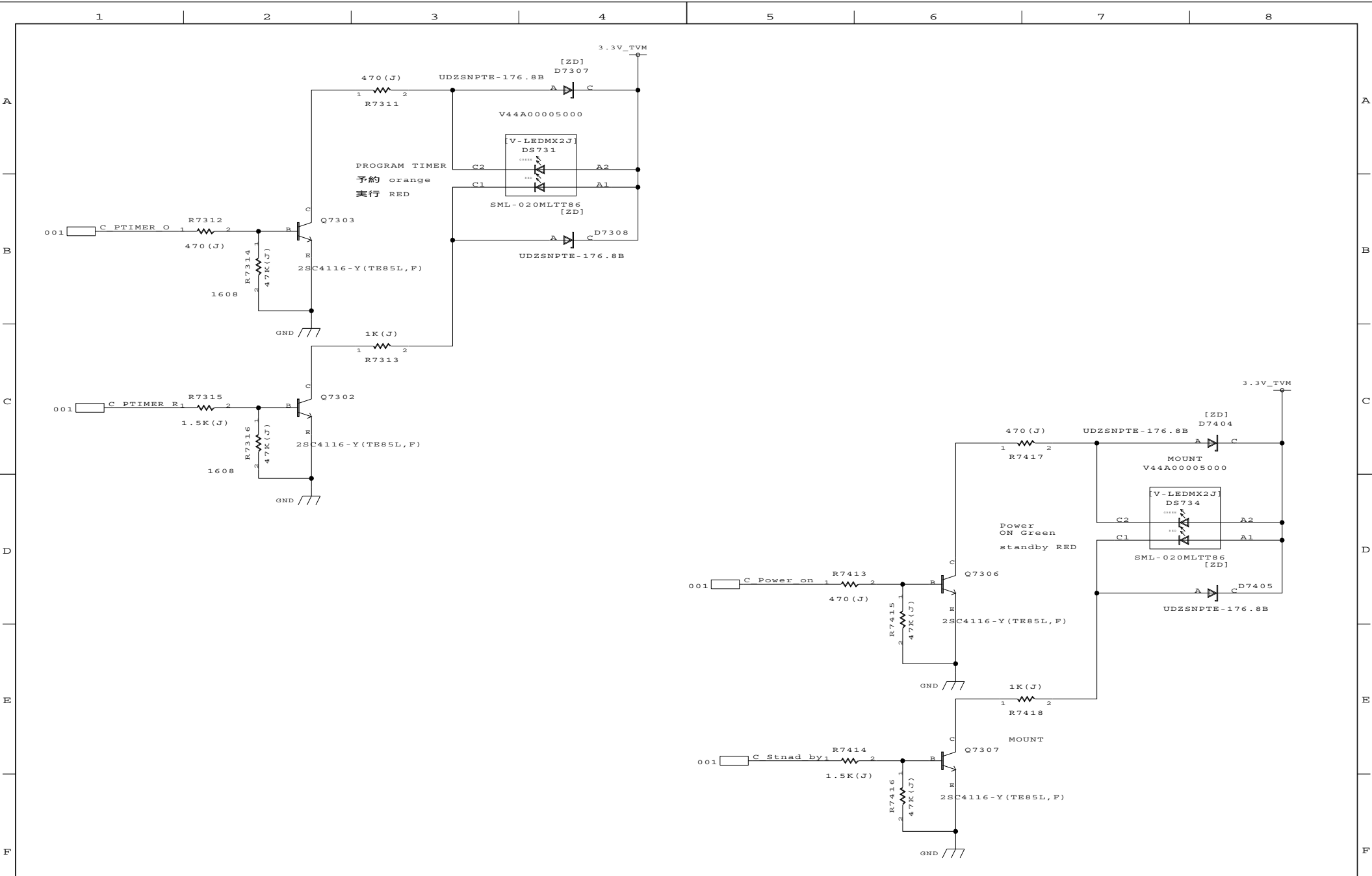
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REV. MARK

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DRAWING. NO.

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DESIGNED BY

T. IMURA

TITLE

2010_LED_RMT

FUNCTION

LED Driver

SH. NO.

3

PAGE NO.

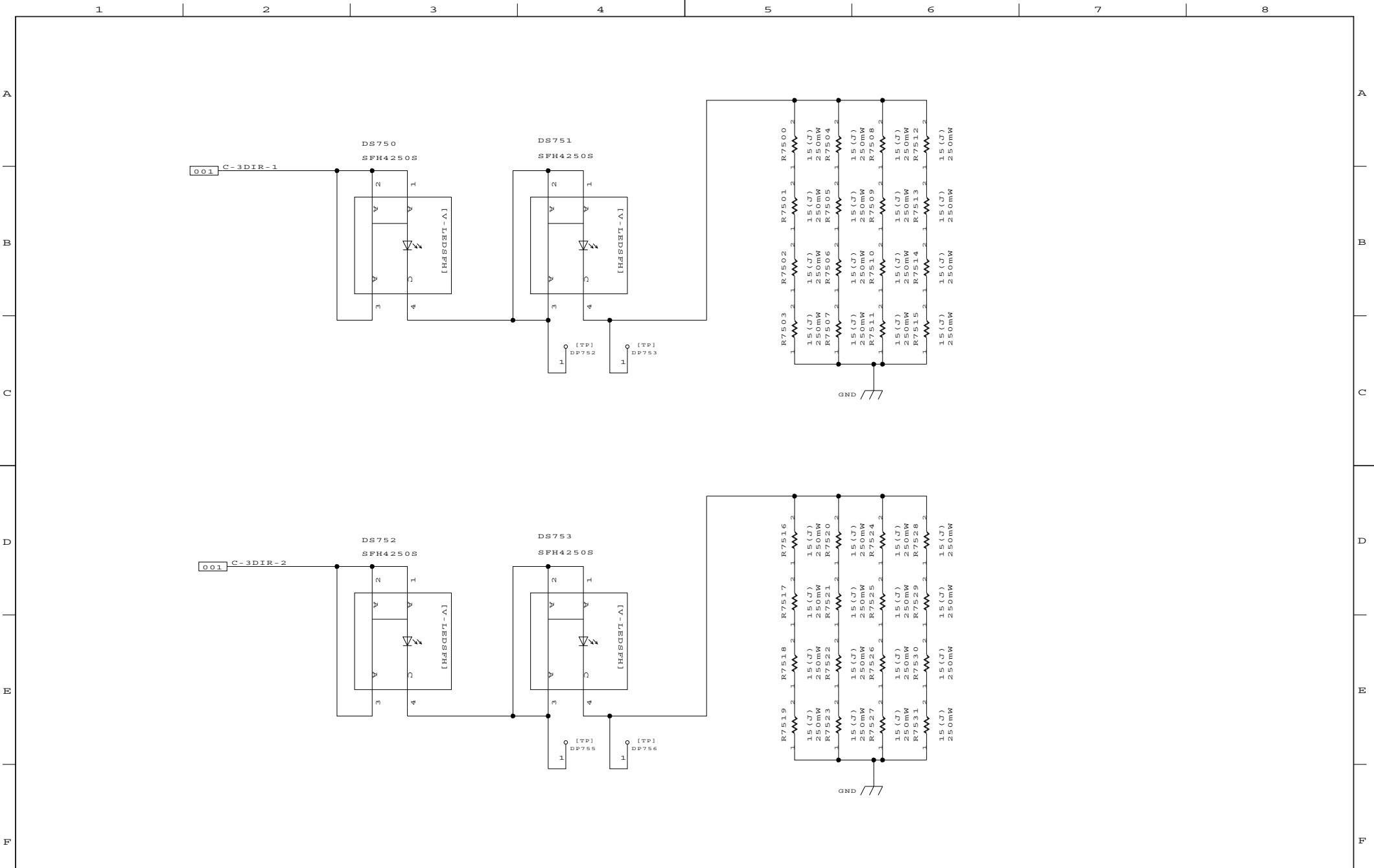
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REV. MARK

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DRAWING. NO.

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DESIGNED BY
M. SATO

TITLE
2011_LED_RMT

FUNCTION
3D-IR

SH. NO.
4

PAGE NO.
4

REV. MARK
00

DRAWING. NO.

SCHEMATIC DIAGRAMS

PRODUCT NAME TL838/868

UNIT NAME MAIN

UNIT NO. PE1000ZA

PWA NAME

PWA NO.

PWB NAME

PWB NO.

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
1	---	COVER SHEET	MP	26	129	DDR3 SDRAM	MP	51	658	-	MP	76	903	FRC POWER 1.26V	MP
2	101	MStar (Component Video)	MP	27	130	DDR3 Series Resistors	MP	52	660	HDMI CONNECTOR	MP	77	904	FRC POWER 1.5V	MP
3	102	MStar (CVBS, VIF)	MP	28	131	EBUS RESISTOR	MP	53	661	HDMI 5VPWR	MP	78	905	POWER 3.3VD	MP
4	103	MStar (Audio, SIF)	MP	29	132	SPI FLASH	MP	54	662	HDMI HPD(1)	MP	79	906	POWER 6V	MP
5	104	Mstar(CI)	MP	30	134	NAND FLASH	MP	55	663	HDMI HPD(2)	MP	80	914	PANEL I/F (POWER)	MP
6	105	Mstar(TS)	MP	31	150	ETHER PHY	MP	56	664	HDMI DDC	MP	81	950	POWER CONNECTOR	MP
7	106	Mstar(HDMI 1/2)	MP	32	151	RJ45 CONNECTOR	MP	57	665	3.3V_HDMI	MP	82	960	EMC CONTROL	MP
8	107	Mstar(HDMI 2/2)	MP	33	160	USB CONNECTOR(side)	MP	58	669	HDCP KEY ROM	MP	83			
9	108	MStar(LVDS 1/2)	MP	34	310	AUDIO TV OUTPUT	MP	59	700	LED/IR IF	MP	84			
10	109	MStar(LVDS 2/2)	MP	35	321	Headphone AMP	MP	60	701	5V LED/KEY	MP	85			
11	110	MStar (DDR A)	MP	36	360	OPTICAL OUTPUT	MP	61	704	TVM FAN CONTROL	MP	86			
12	111	MStar (DDR B)	MP	37	370	AUDIO D-AMP (MAIN)	MP	62	750	TUNER	MP	87			
13	112	MStar (DDR C)	MP	38	371	AUDIO D-AMP OUT (MAIN)	MP	63	755	TUNER DVB-S/S2	MP	88			
14	113	Mstar (NAND, FRC)	MP	39	373	AUDIO MUTE	MP	64	765	DEMOD DVB-T2/T/C	MP	89			
15	114	MStar (GPIO)	MP	40	600	EURO SCART	MP	65	770	DEMOD DVB-S2/S	MP	90			
16	115	Mstar (USB, MISC)	MP	41	601	FRONT AV	MP	66	775	DiSeqC, LNB POWER	MP	91			
17	116	MStar (Power 1/3)	MP	42	602	DSUB PC IN	MP	67	780	TUNER POWER	MP	92			
18	117	MStar (Power 2/3)	MP	43	603	EURO COMPONENT IN	MP	68	790	CI TS RESISTOR	MP	93			
19	118	MStar (Power 3/3)	MP	44	605	EURO TV OUT	MP	69	791	CI POWER/CONNECTOR	MP	94			
20	120	PERI (XTAL, STRAPS)	MP	45	606	AV INPUT1	MP	70	793	CI ADDRESS BUFFER	MP	95			
21	121	RESET, I2C	MP	46	607	AV INPUT2	MP	71	794	CI DATA RESISTOR	MP	96			
22	122	PERI (SERIAL)	MP	47	608	AV INPUT3	MP	72	795	CI CONTROL BUFFER	MP	97			
23	126	DDR3 SDRAM	MP	48	650	LVDS CONNECTOR	MP	73	796	CI TS OUTPUT BUFFER	MP	98			
24	127	DDR3 Damping Resistors	MP	49	651	LVDS CONNECTOR	MP	74	901	3.3V STANDBY	MP	99			
25	128	DDR3 Damping Resistors	MP	50	652	BL_CONTROL & DIMMING	MP	75	902	POWER 1.1V	MP	100			

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TITLE

TOTAL

PAGE NO.

REV. MARK

DRAWING. NO.

IWANO

OSANAI

MAIN UNIT

82

1

MP

PE1000ZA

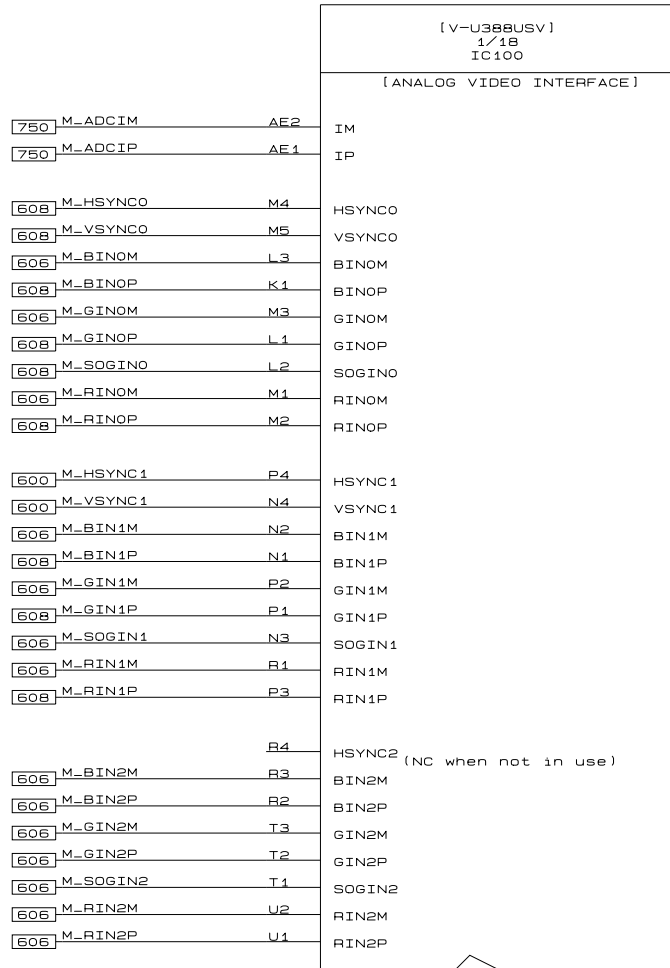
2011.07.11

18:54

基板へ渡す値

TOSHIBA CORPORATION

V46A00065300



MSD388USV-V6

DESIGNED BY

OSANAI

TITLE

MAIN UNIT

FUNCTION

MStar (Component Video)

SH. NO.

101

PAGE NO.

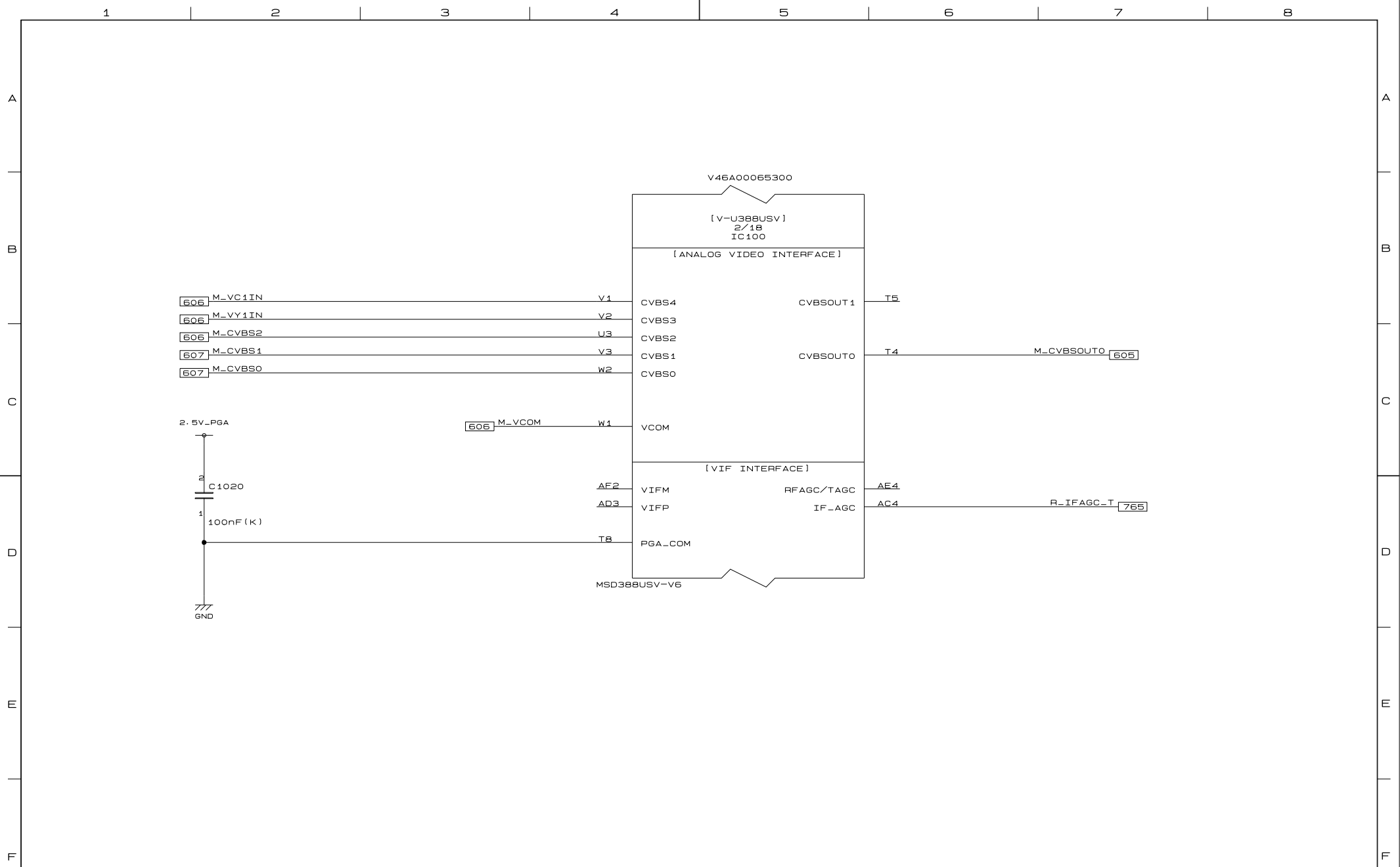
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REV. MARK

MP

DRAWING. NO.

PE1000ZA



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OSANAI

TITLE
MAIN UNIT

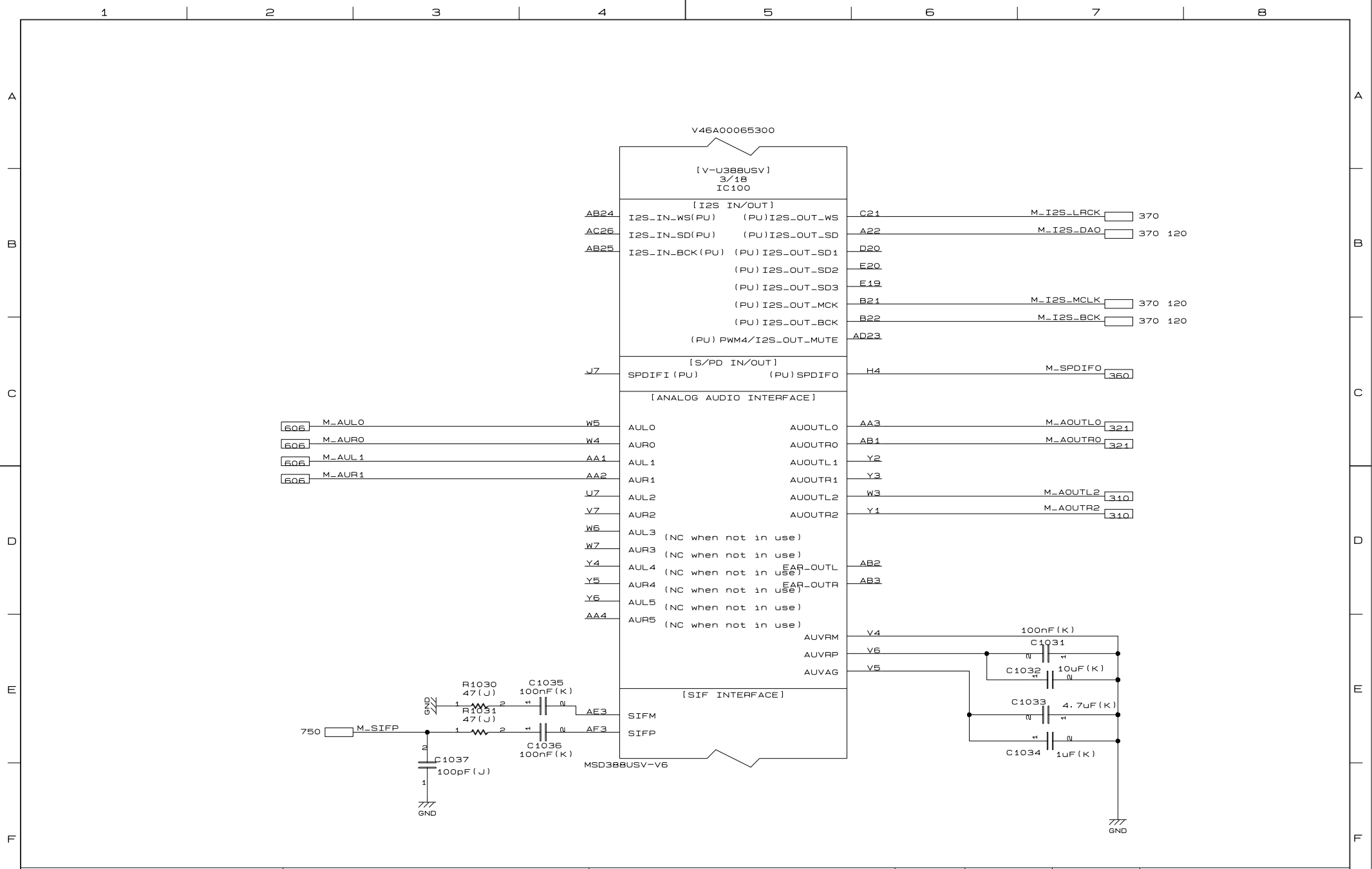
FUNCTION
MStar (CVBS, VIF)

SH. NO.
102

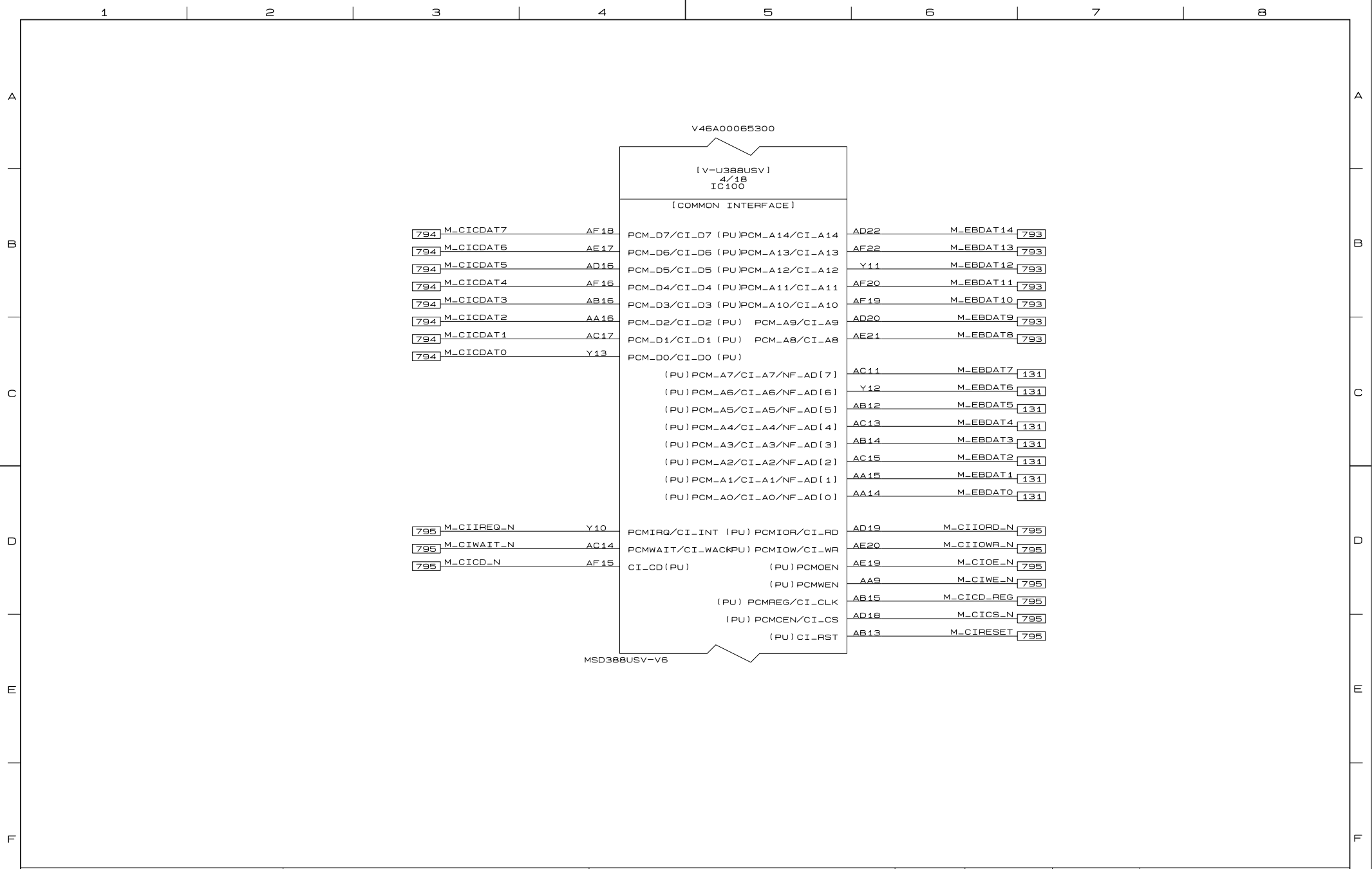
PAGE NO.
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REV. MARK
MP

DRAWING. NO.
PE1000ZA

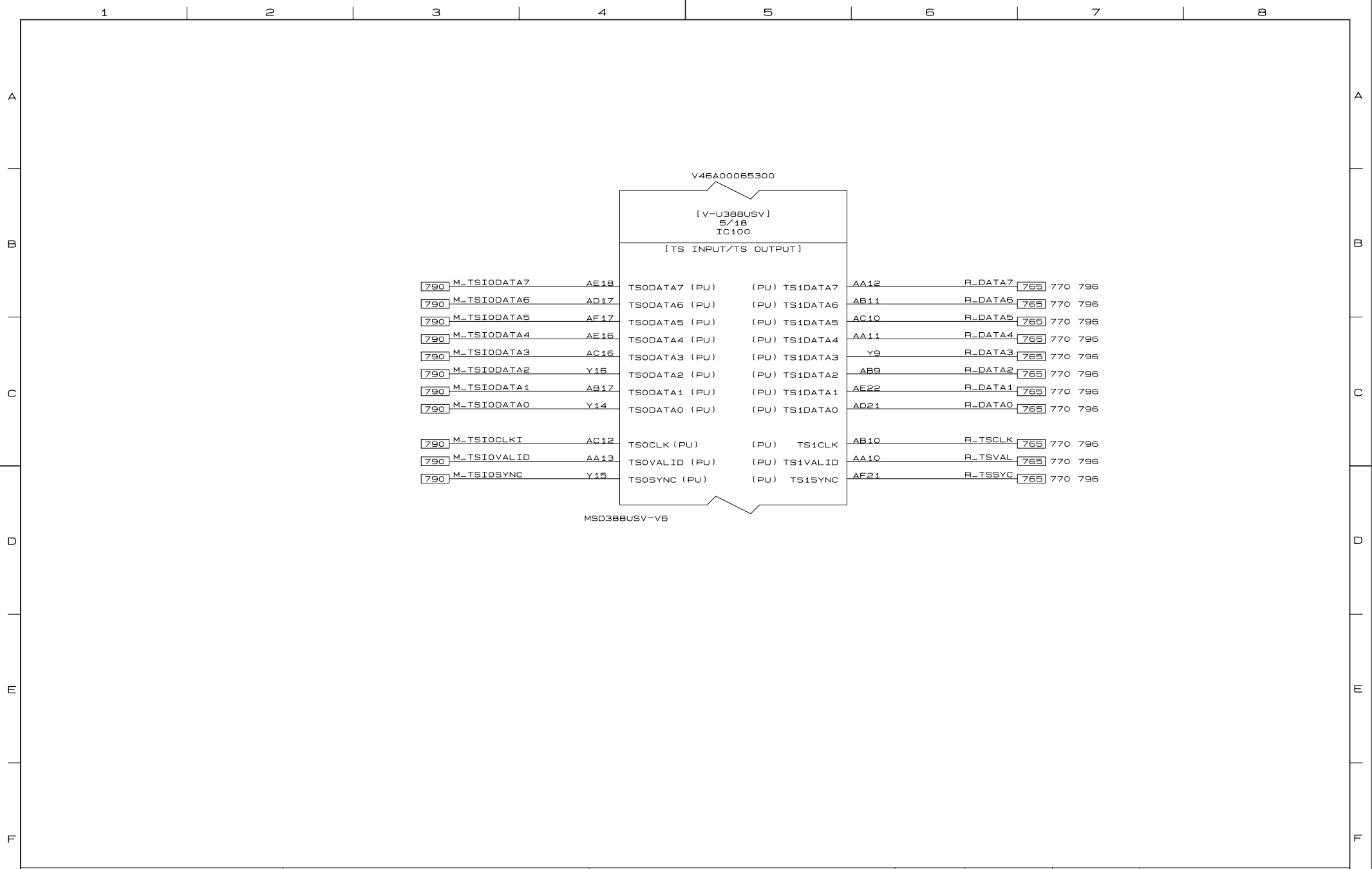


DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION MStar (Audio, SIF)	SH. NO. 103	PAGE NO. 4	REV. MARK MP	DRAWING. NO. PE1000ZA
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Pin	Signal Name	IC Pin	IC Label	IC Pin	Signal Name	IC Pin
794	M-CICDAT7	AF18	PCM_D7/CI_D7 (PU)PCM_A14/CI_A14	AD22	M-EBDAT14	793
794	M-CICDAT6	AE17	PCM_D6/CI_D6 (PU)PCM_A13/CI_A13	AF22	M-EBDAT13	793
794	M-CICDAT5	AD16	PCM_D5/CI_D5 (PU)PCM_A12/CI_A12	Y11	M-EBDAT12	793
794	M-CICDAT4	AF16	PCM_D4/CI_D4 (PU)PCM_A11/CI_A11	AF20	M-EBDAT11	793
794	M-CICDAT3	AB16	PCM_D3/CI_D3 (PU)PCM_A10/CI_A10	AF19	M-EBDAT10	793
794	M-CICDAT2	AA16	PCM_D2/CI_D2 (PU) PCM_A9/CI_A9	AD20	M-EBDAT9	793
794	M-CICDAT1	AC17	PCM_D1/CI_D1 (PU) PCM_A8/CI_A8	AE21	M-EBDAT8	793
794	M-CICDAT0	Y13	PCM_D0/CI_D0 (PU)			
			(PU)PCM_A7/CI_A7/NF_AD[7]	AC11	M-EBDAT7	131
			(PU)PCM_A6/CI_A6/NF_AD[6]	Y12	M-EBDAT6	131
			(PU)PCM_A5/CI_A5/NF_AD[5]	AB12	M-EBDAT5	131
			(PU)PCM_A4/CI_A4/NF_AD[4]	AC13	M-EBDAT4	131
			(PU)PCM_A3/CI_A3/NF_AD[3]	AB14	M-EBDAT3	131
			(PU)PCM_A2/CI_A2/NF_AD[2]	AC15	M-EBDAT2	131
			(PU)PCM_A1/CI_A1/NF_AD[1]	AA15	M-EBDAT1	131
			(PU)PCM_A0/CI_A0/NF_AD[0]	AA14	M-EBDAT0	131
795	M-CIREQ-N	Y10	PCMIRO/CI_INT (PU) PCMIOR/CI_RD	AD19	M-CIIRD-N	795
795	M-CIWAIT-N	AC14	PCMWAIT/CI_WACK(PU) PCMIOW/CI_WR	AE20	M-CIIOWR-N	795
795	M-CICD-N	AF15	CI_CD(PU)	AE19	M-CIOE-N	795
			(PU) PCMOEN	AA9	M-CIWE-N	795
			(PU) PCMWEN	AB15	M-CICD_REG	795
			(PU) PCMREG/CI_CLK	AD18	M-CICS-N	795
			(PU) PCMCEN/CI_CS	AB13	M-CIRESET	795
			(PU) CI_RST			

DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION Mstar(CI)	SH. NO. 104	PAGE NO. 5	REV. MARK MP	DRAWING. NO. PE1000ZA
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OSANAI

TITLE

MAIN UNIT

FUNCTION

Mstar (TS)

SH. NO.

105

PAGE NO.

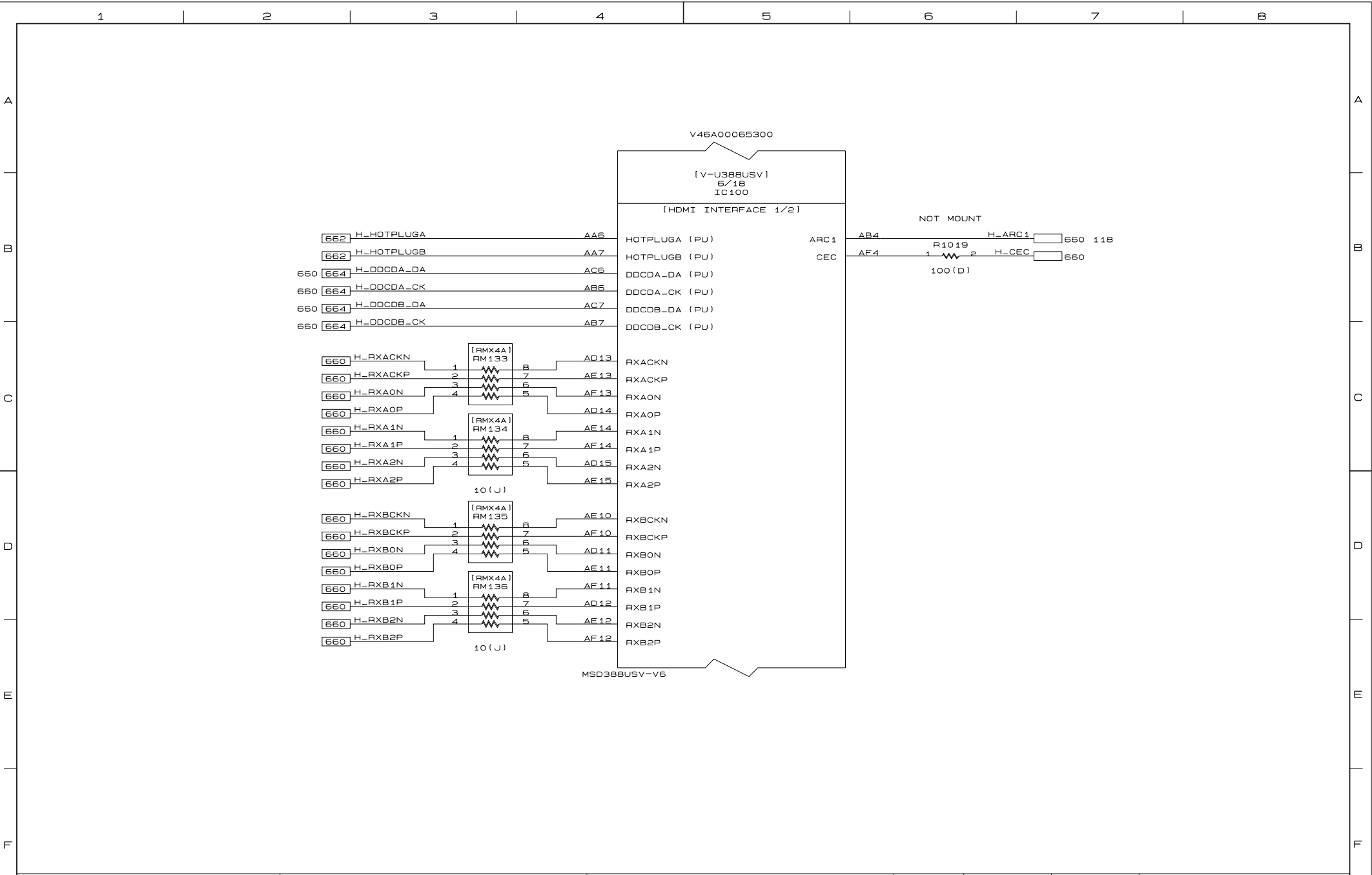
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REV. MARK

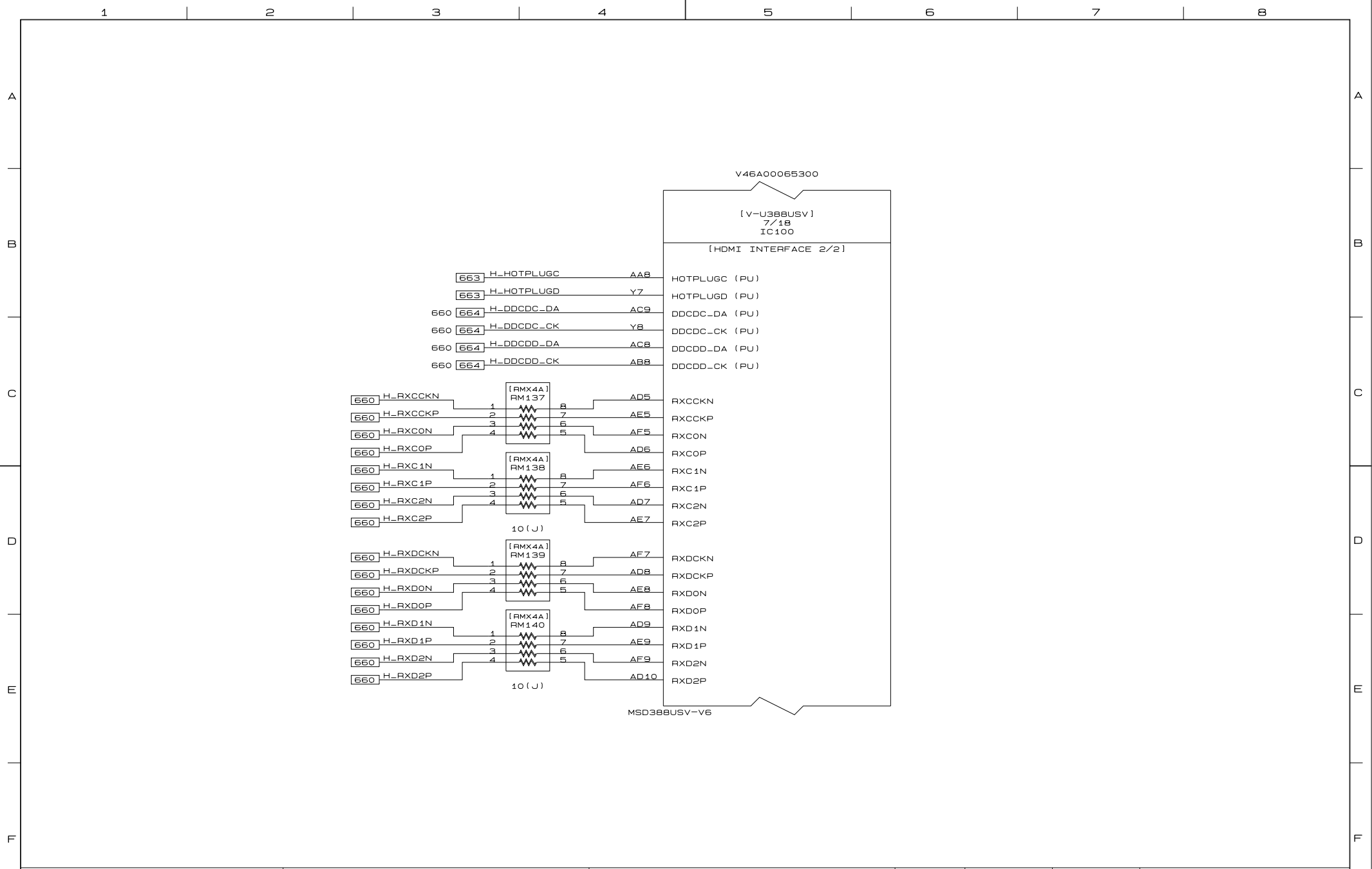
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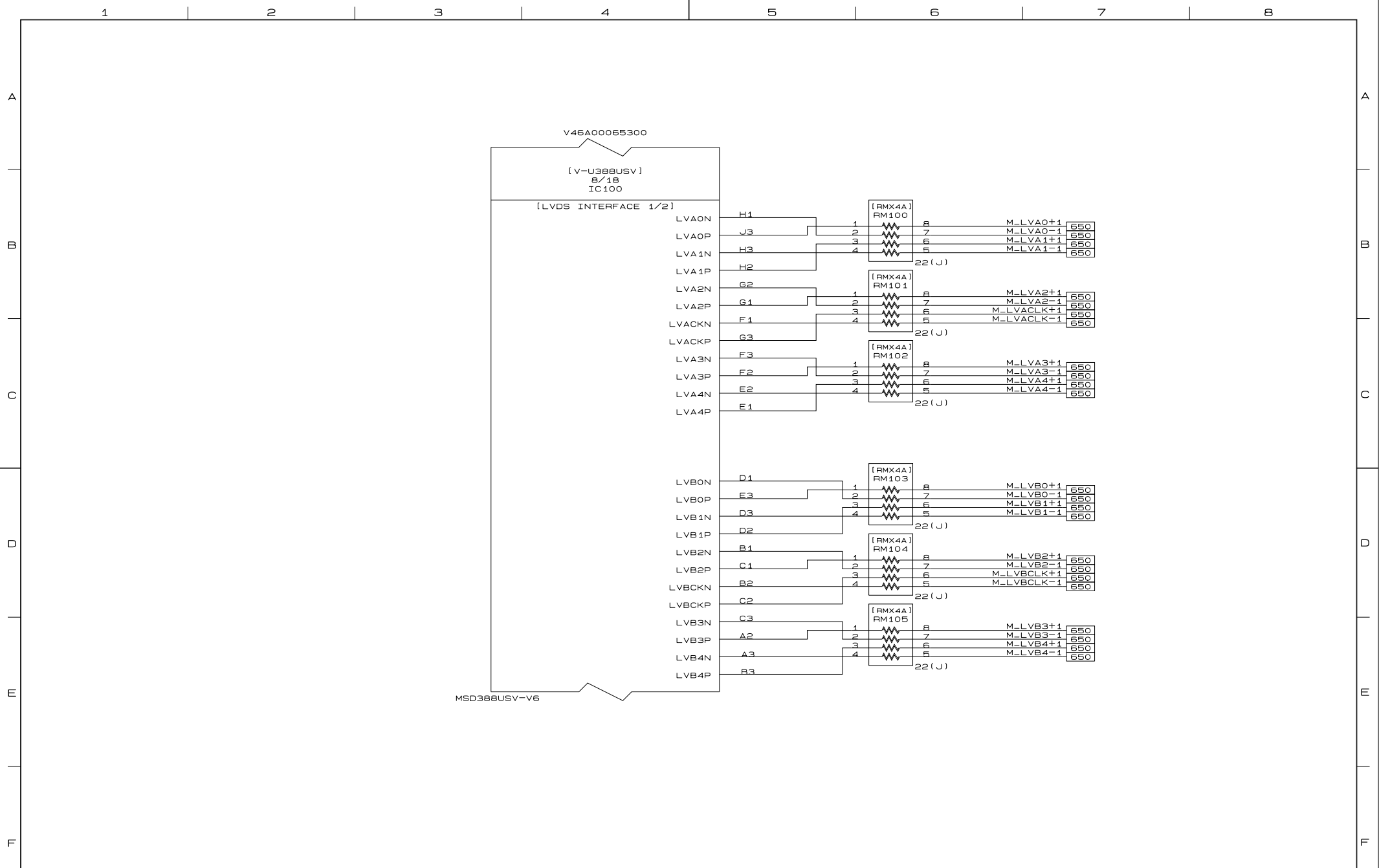
PE1000ZA



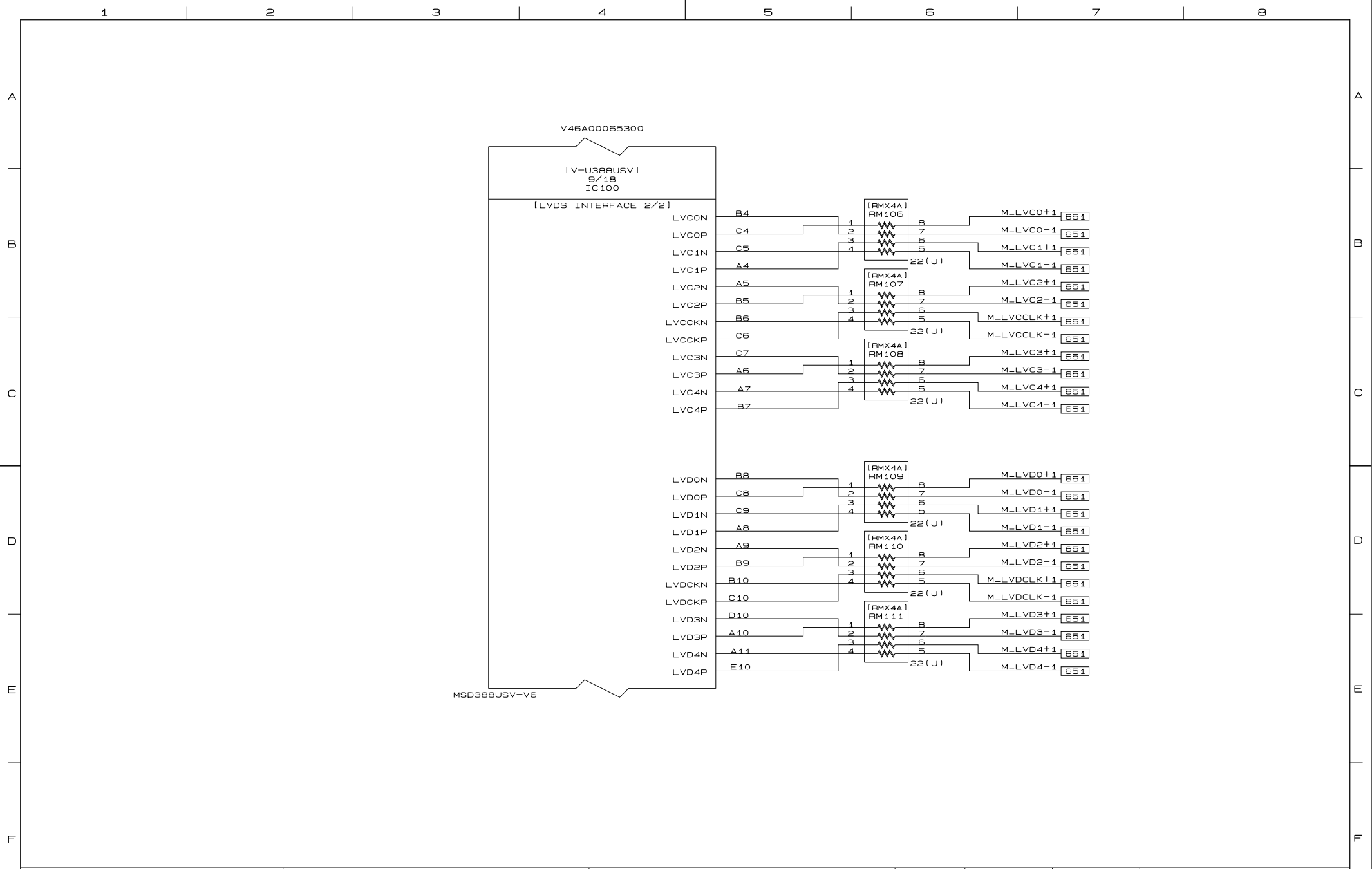
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION Mstar (HDMI 2/2)	SH. NO. 107	PAGE NO. 8	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION MStar (LVDS 1/2)	SH. NO. 108	PAGE NO. 9	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY
OSANAI

TITLE
MAIN UNIT

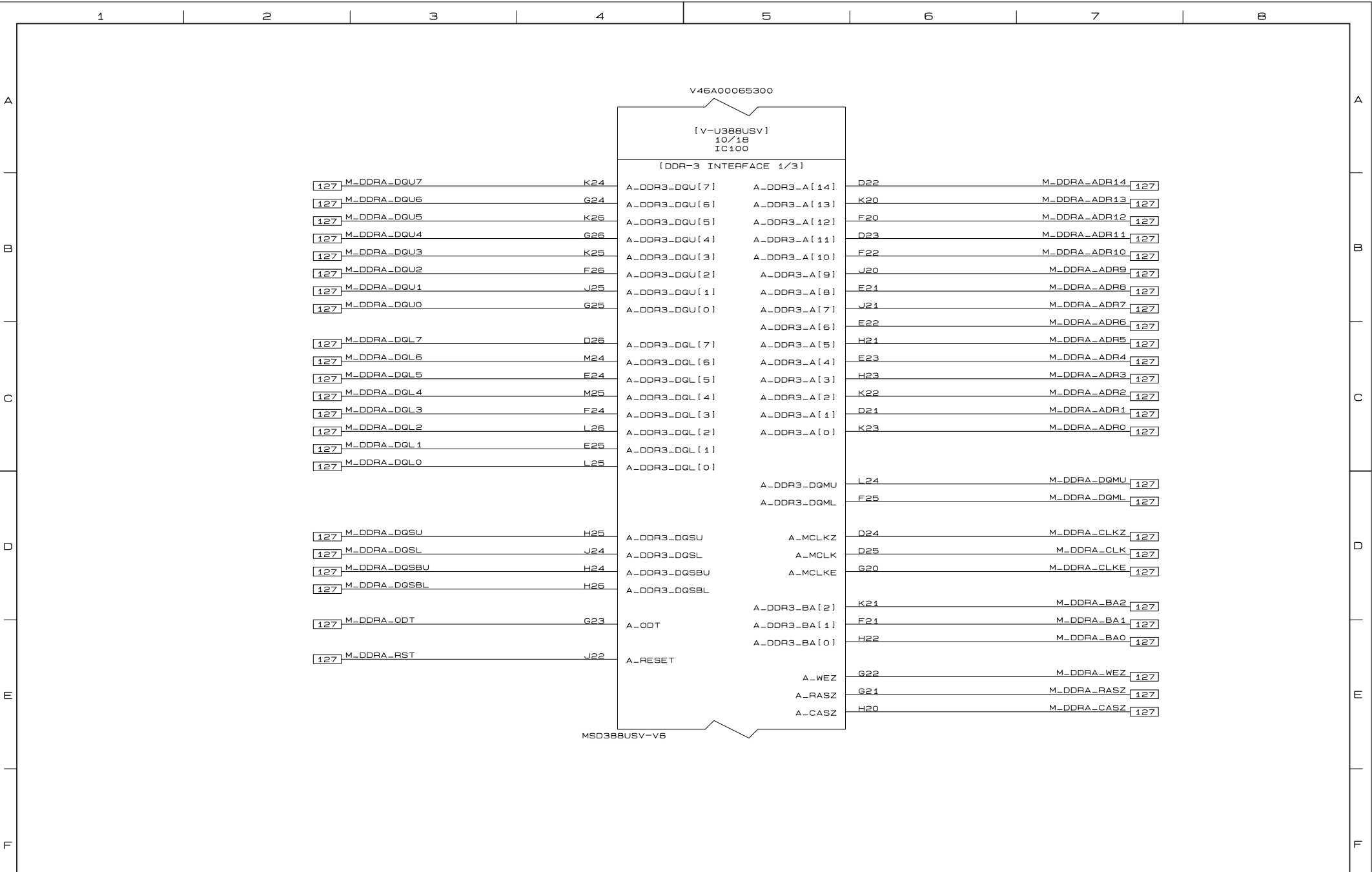
FUNCTION
MStar (LVDS 2/2)

SH. NO.
109

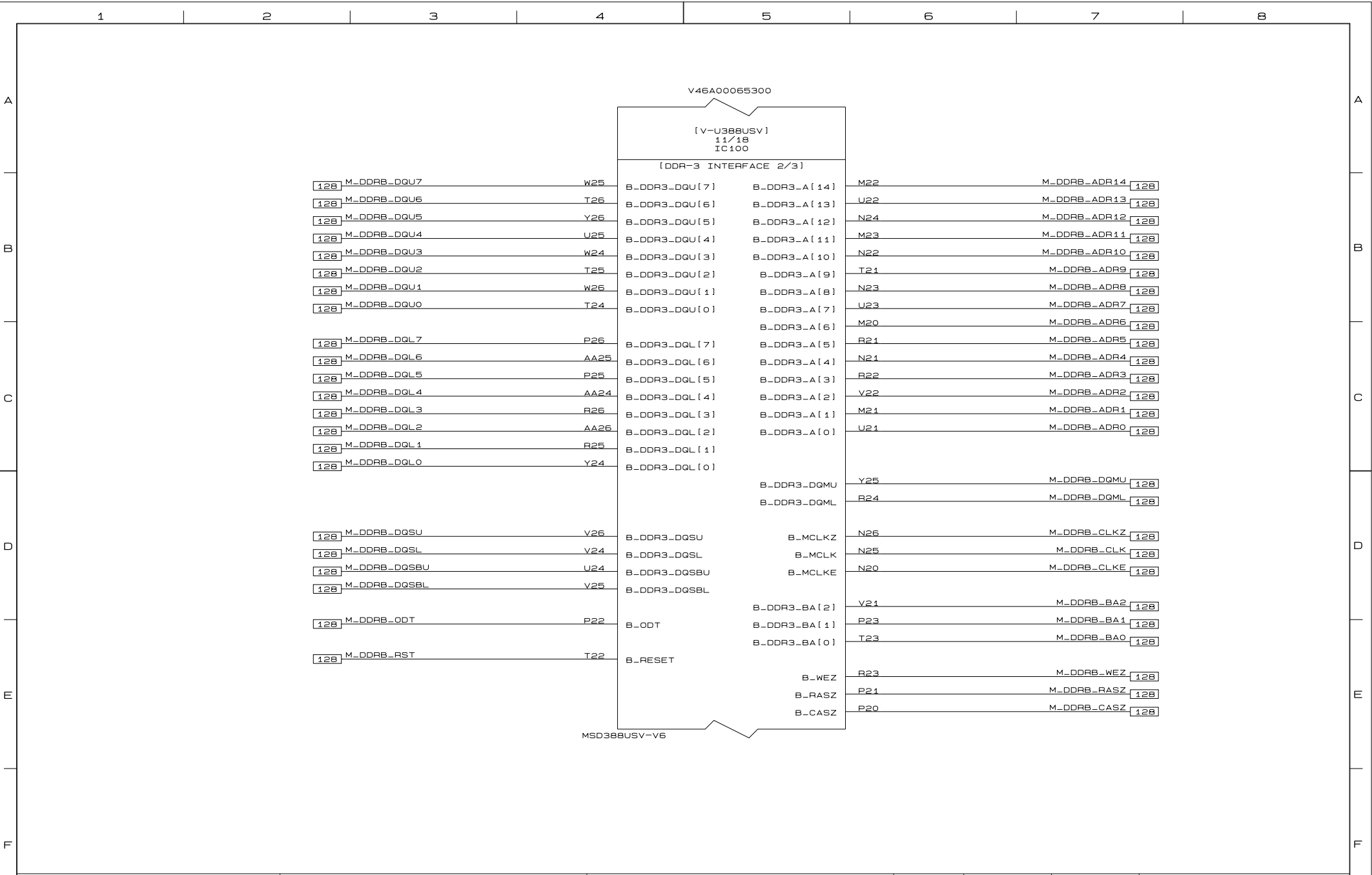
PAGE NO.
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REV. MARK
MP

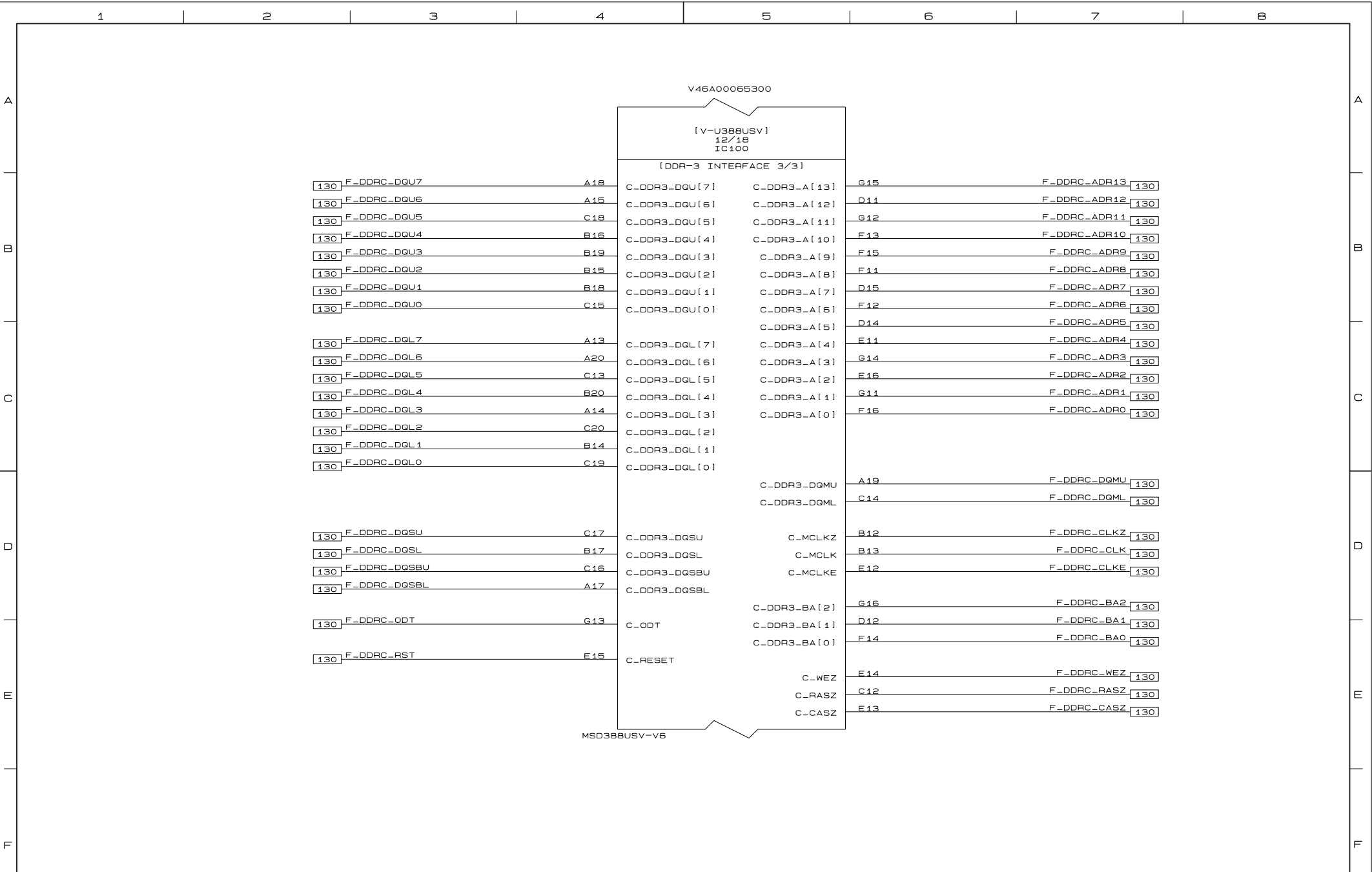
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PE1000ZA



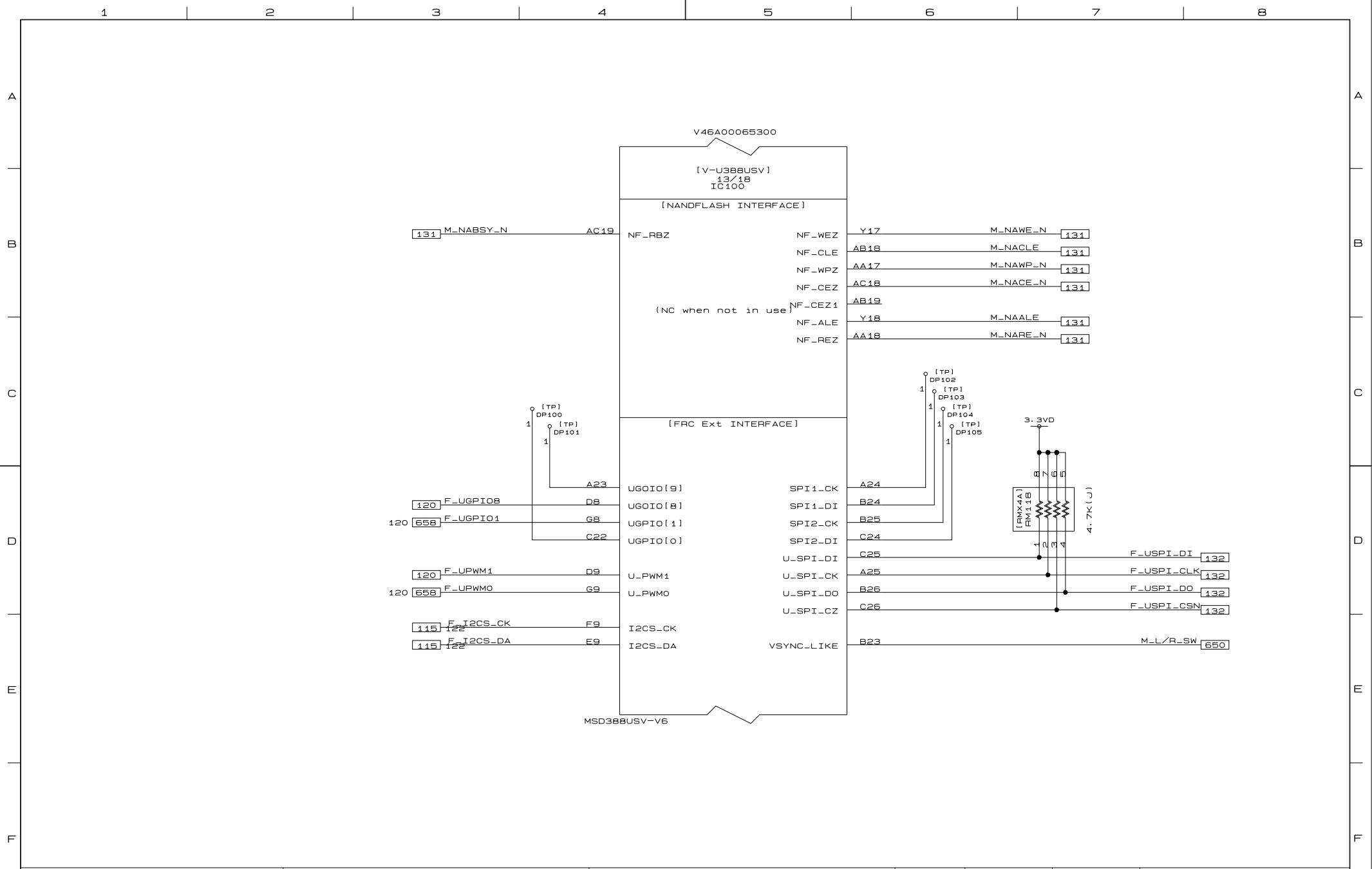
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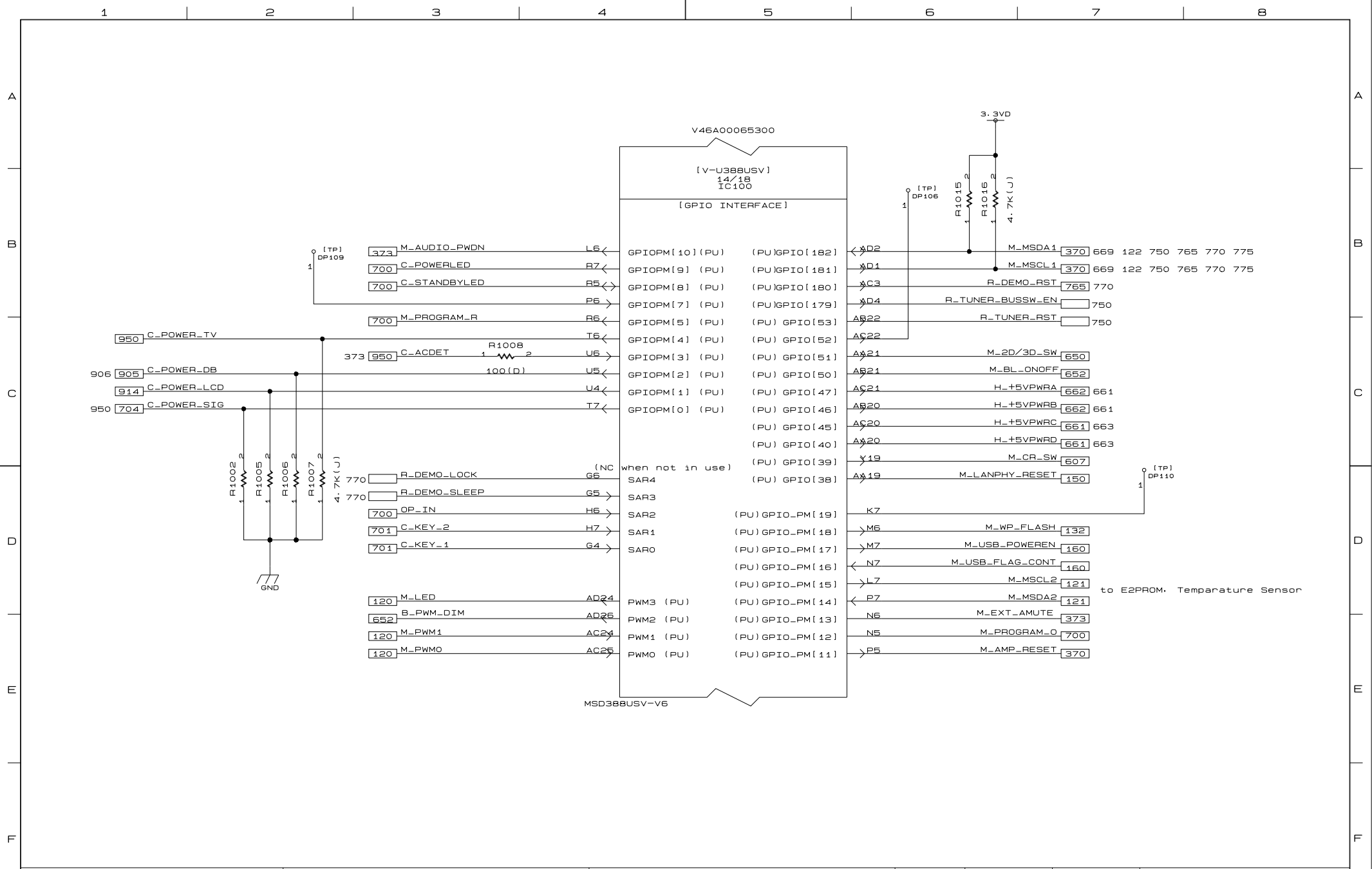
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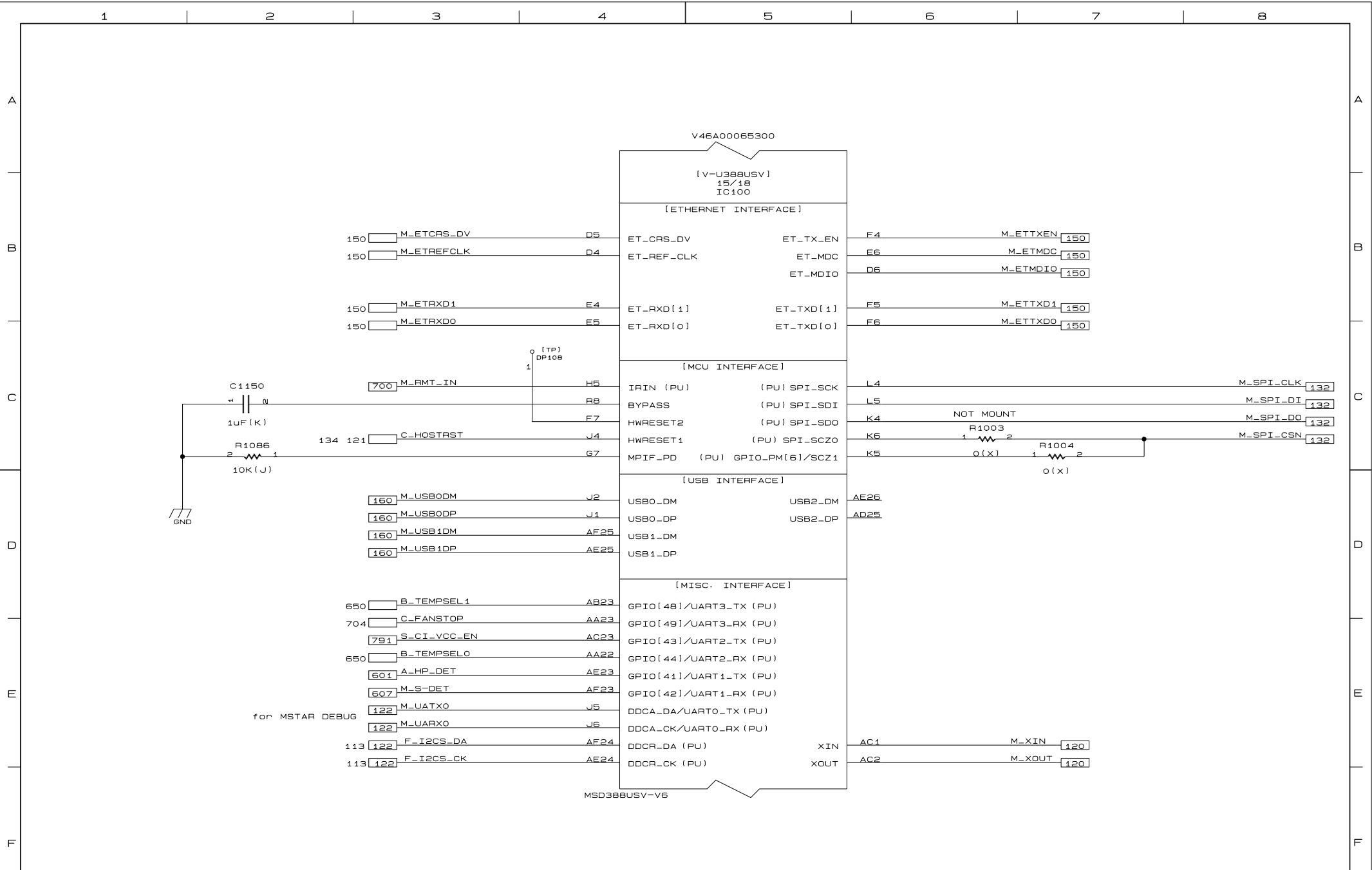
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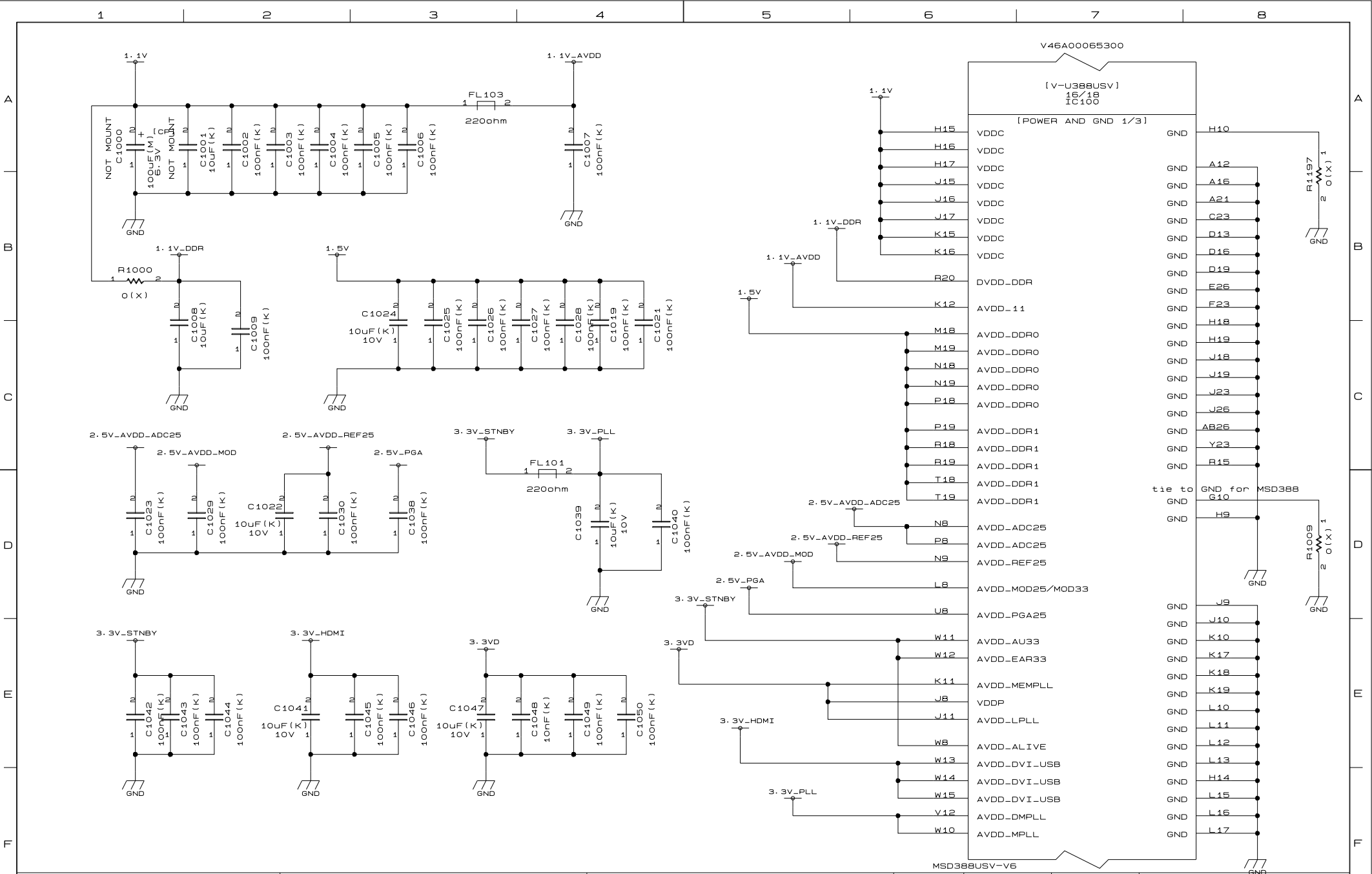
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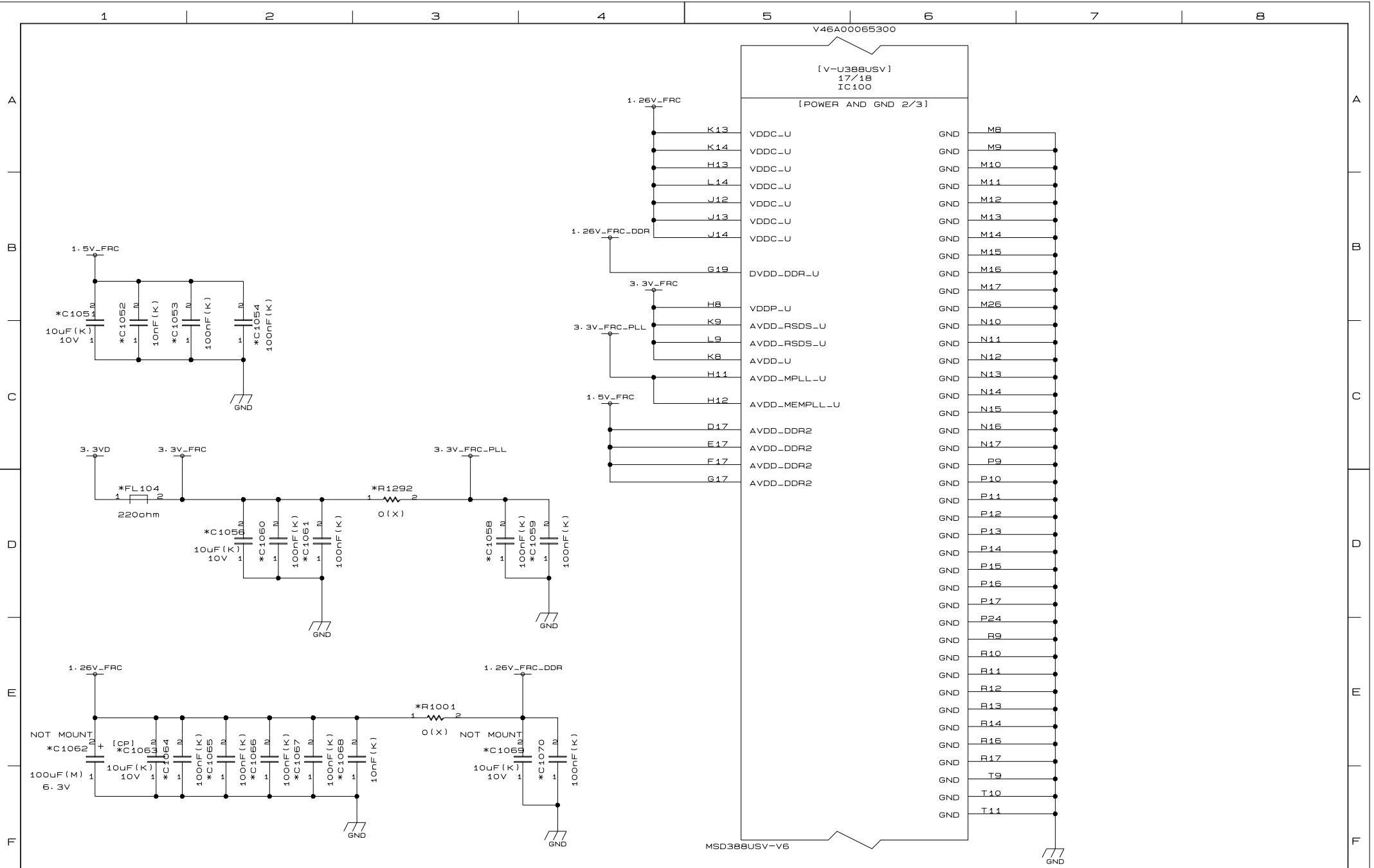
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION Mstar (USB, MISC)	SH. NO. 115	PAGE NO. 16	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION MStar (Power 1/3)	SH. NO. 116	PAGE NO. 17	REV. MARK MP	DRAWING. NO. PE1000ZA
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OSANAI

TITLE
MAIN UNIT

FUNCTION
MStar (Power 2/3)

SH. NO.
117

PAGE NO.
18

REV. MARK
MP

DRAWING NO.
PE1000ZA

1 2 3 4 5 6 7 8

A

B

C

D

E

F

A

B

C

D

E

F

V46A00065300

[V-U388BUSV]
18/18
IC100

[POWER AND GND 3/3]

- GND T12
- GND T13
- GND T14
- GND T15
- GND T16
- GND T17
- GND U9
- GND U10
- GND U11
- GND U12
- GND U13
- GND U14
- GND U15
- GND U16
- GND U17
- GND U18
- GND U19
- GND U20
- GND U26
- GND V8
- GND V9
- GND V10
- GND V11
- GND V13
- GND V14
- GND V15
- GND V16
- GND V17
- GND V18
- GND V19
- GND V20
- GND V23
- GND W9
- GND W16
- GND W17
- GND W18
- GND W19
- GND W20
- GND Y20
- GND Y21
- GND Y22

- B11 N. C.
- C11 N. C.
- D7 N. C.
- E7 N. C.
- E8 N. C.
- F8 N. C.
- K2 N. C.
- K3 N. C.
- AA5 N. C.
- AB5 N. C.
- AC5 N. C.

660 106 [] H-ARC1

msd381-388基板共通化対応

MSD388BUSV-V6



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OSANAI

TITLE
MAIN UNIT

FUNCTION
MStar (Power 3/3)

SH. NO.
118

PAGE NO.
19

REV. MARK
MP

DRAWING. NO.
PE1000ZA

1 2 3 4 5 6 7 8

A

B

C

D

E

F

A

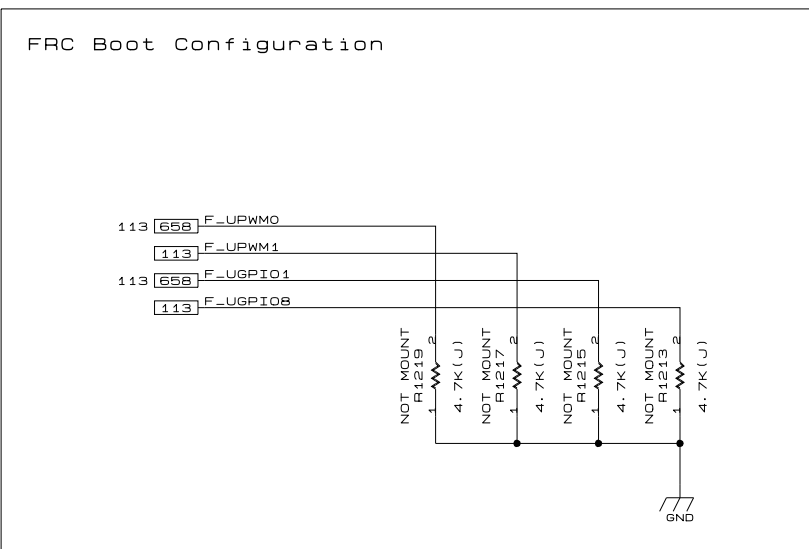
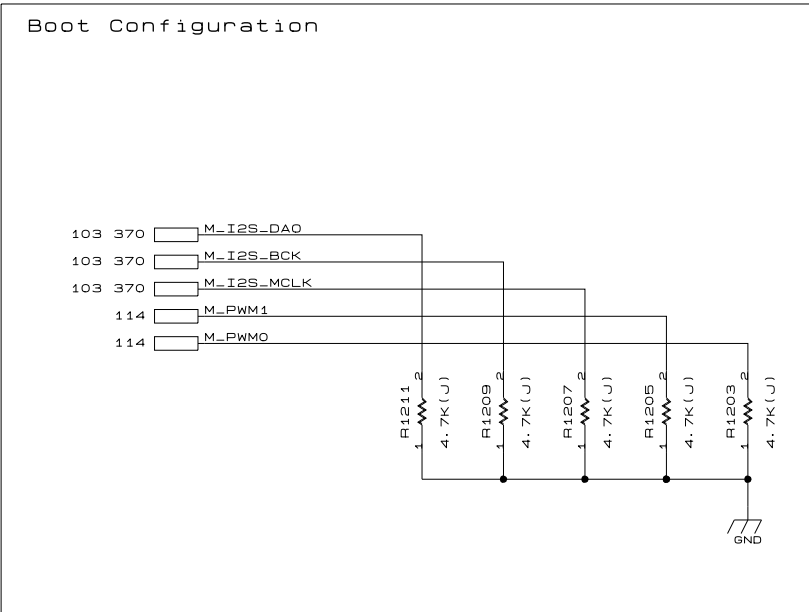
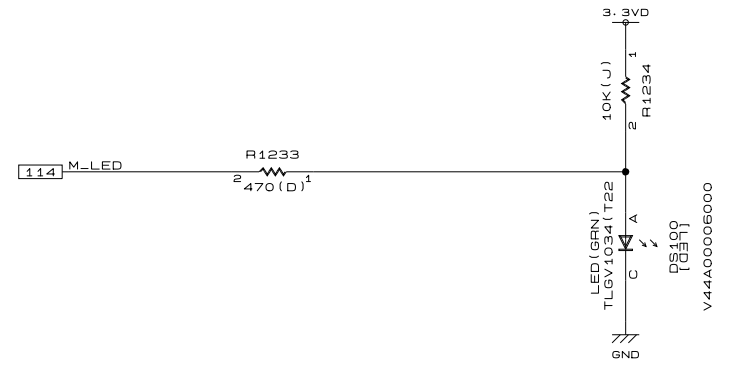
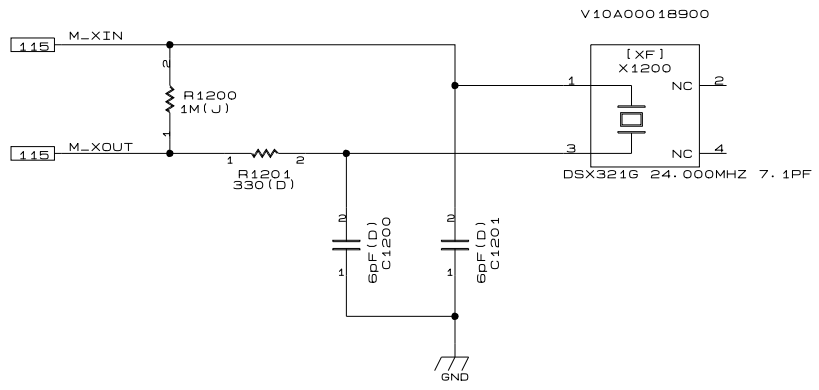
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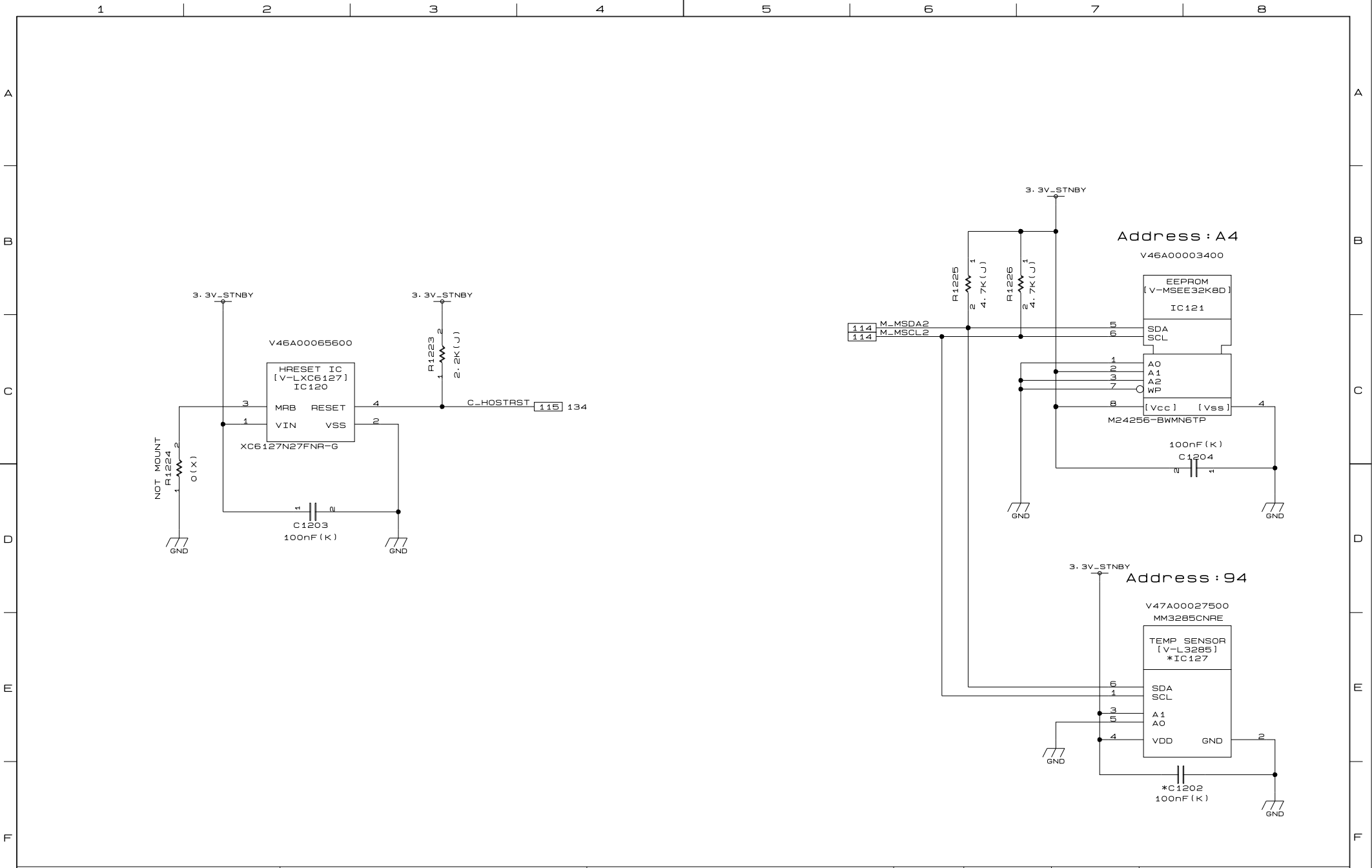
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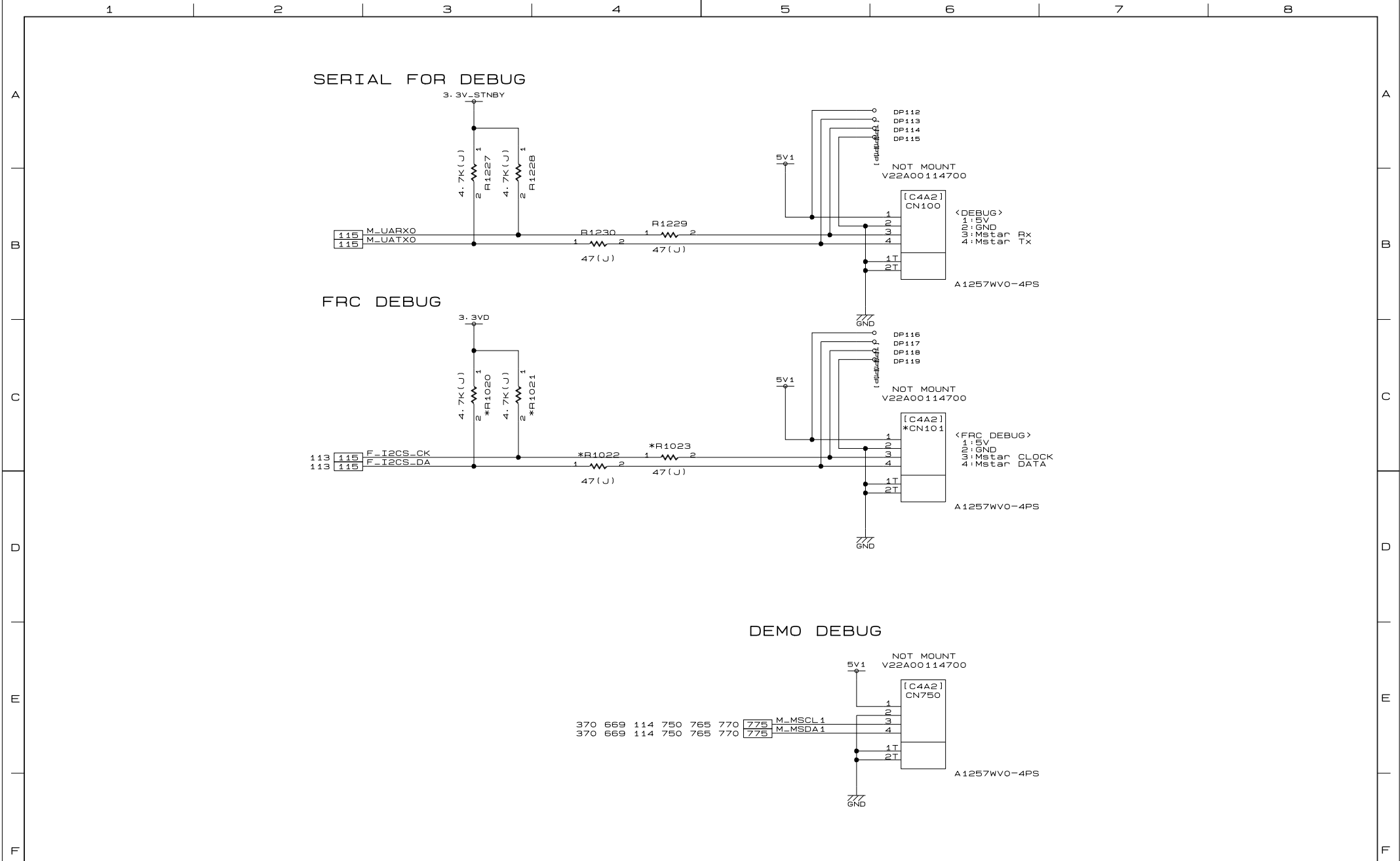
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION PERI (XTAL, STRAPS)	SH. NO. 120	PAGE NO. 20	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION RESET, I2C	SH. NO. 121	PAGE NO. 21	REV. MARK MP	DRAWING. NO. PE1000ZA
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OSANAI

TITLE
MAIN UNIT

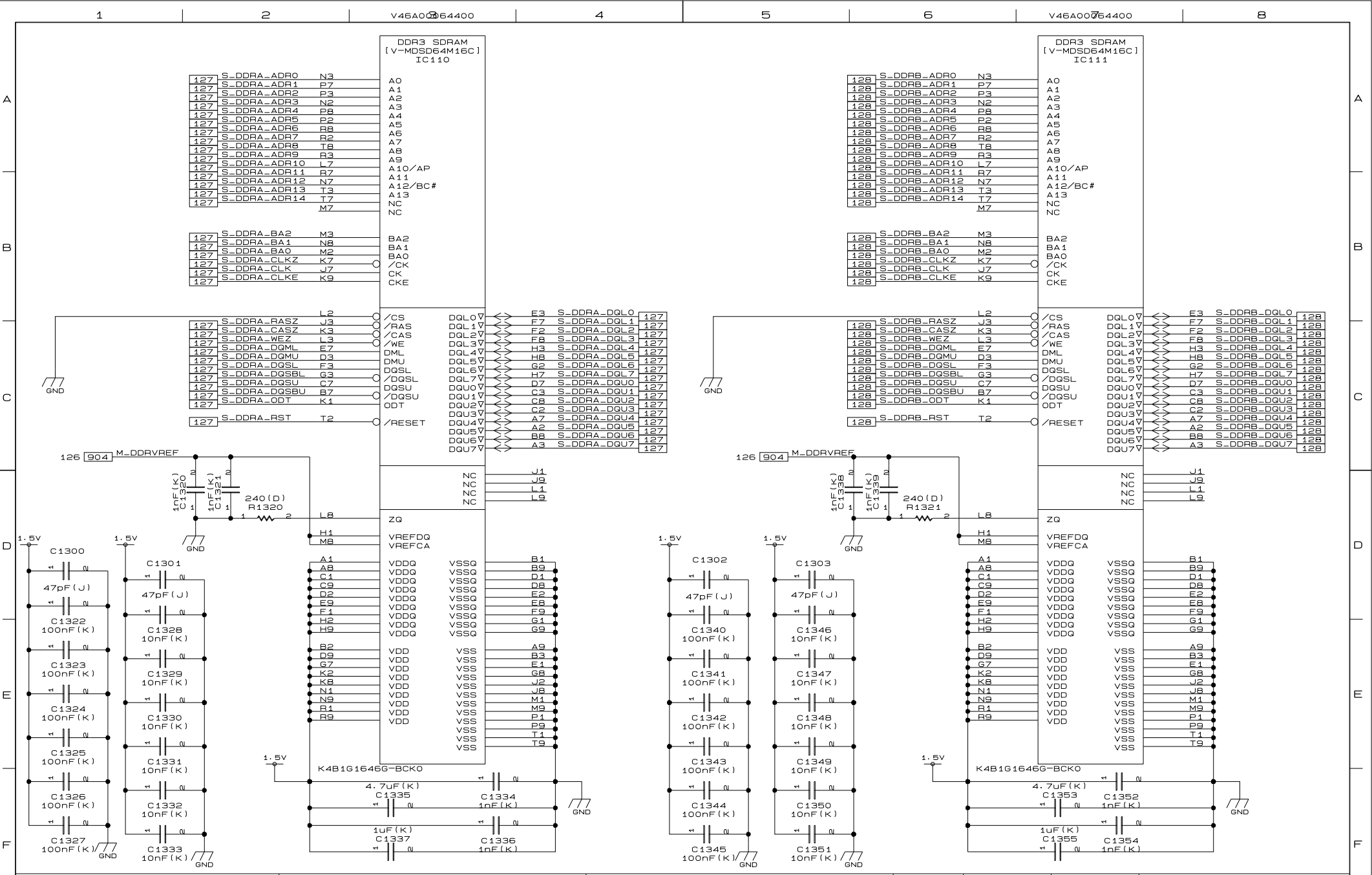
FUNCTION
PERI (SERIAL)

SH. NO.
122

PAGE NO.
22

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY 2011/07/11

TITLE MAIN UNIT

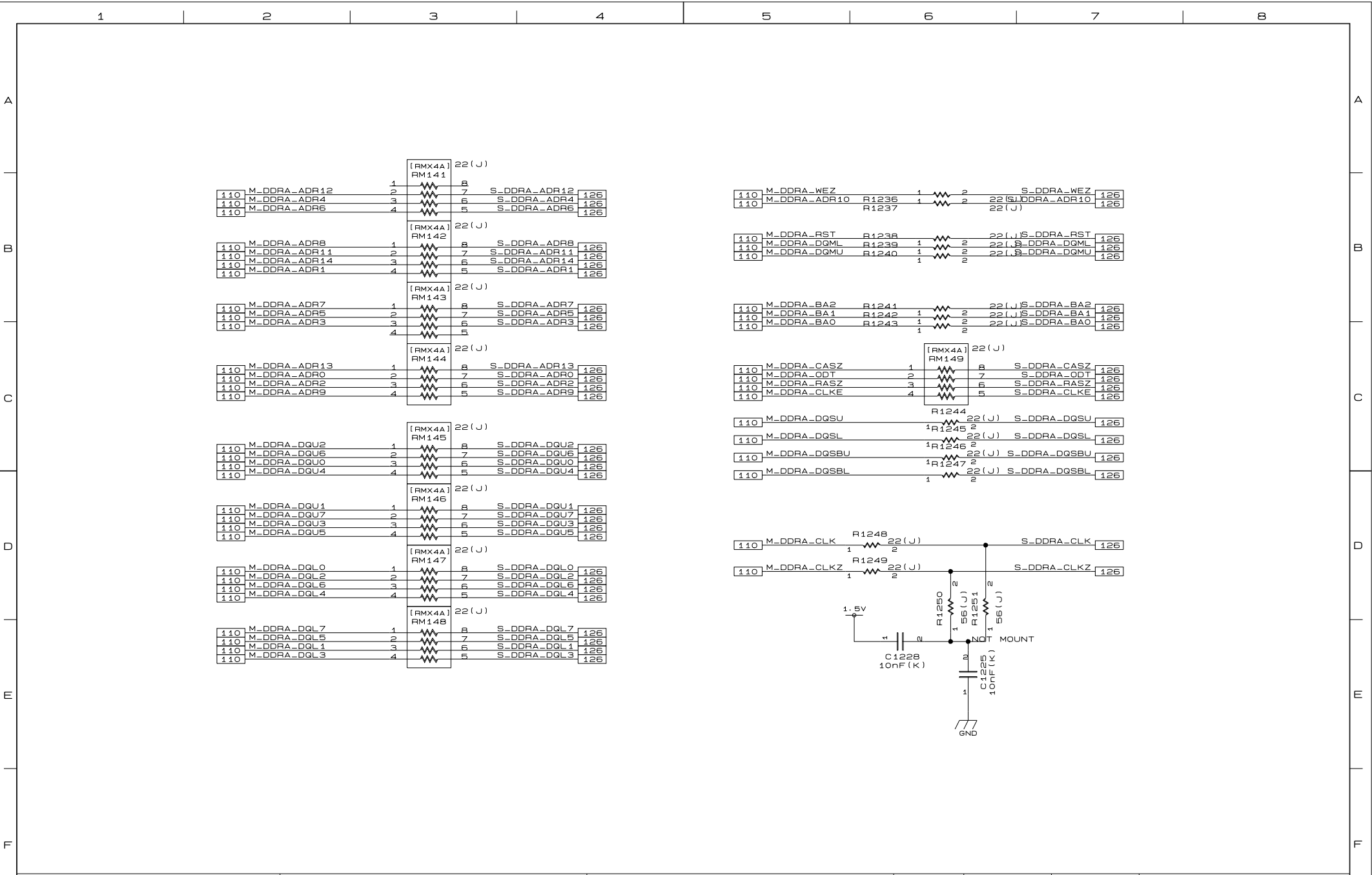
FUNCTION DDR3 SDRAM

SH. NO. 126

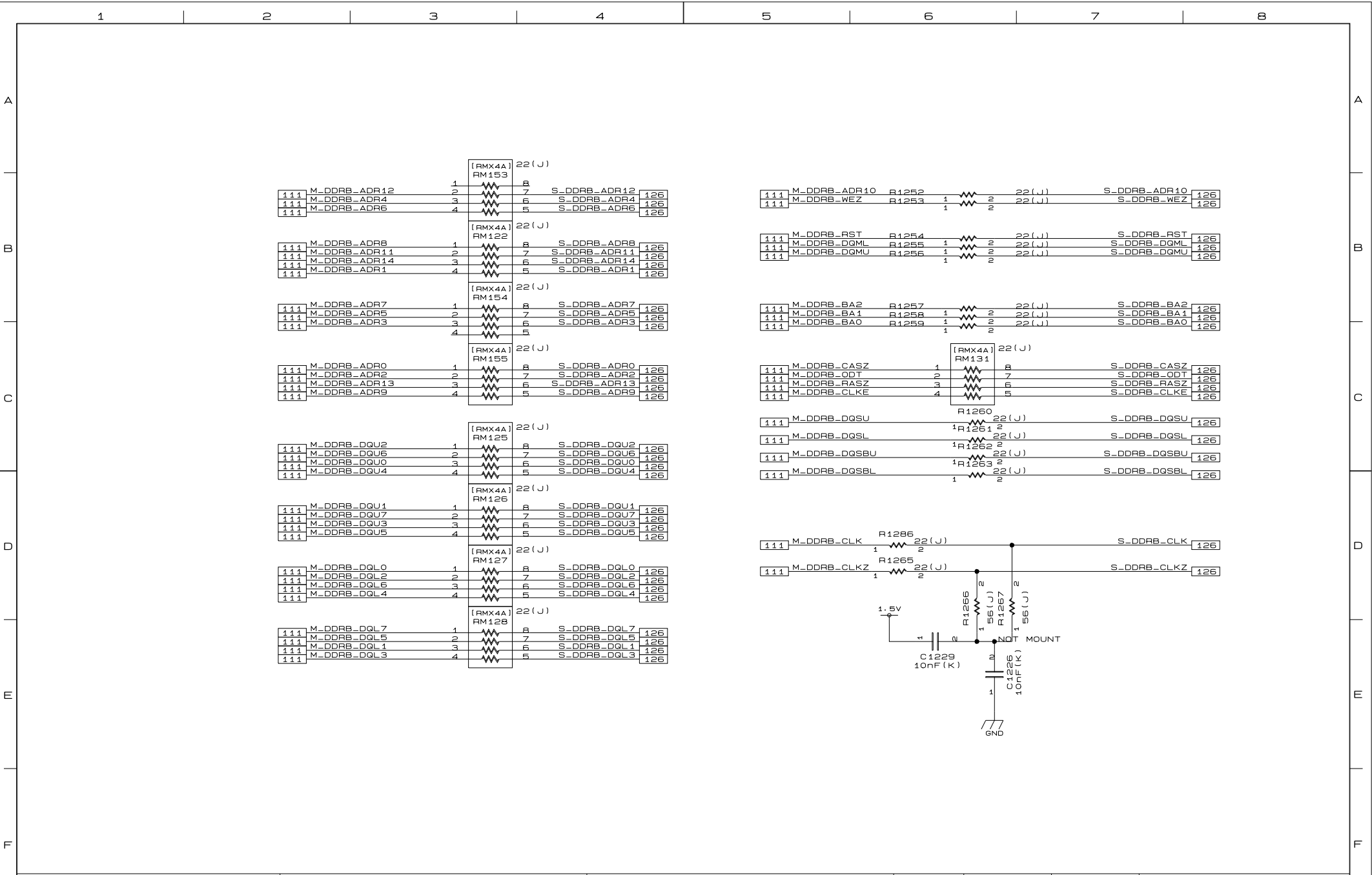
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REV. MARK MP

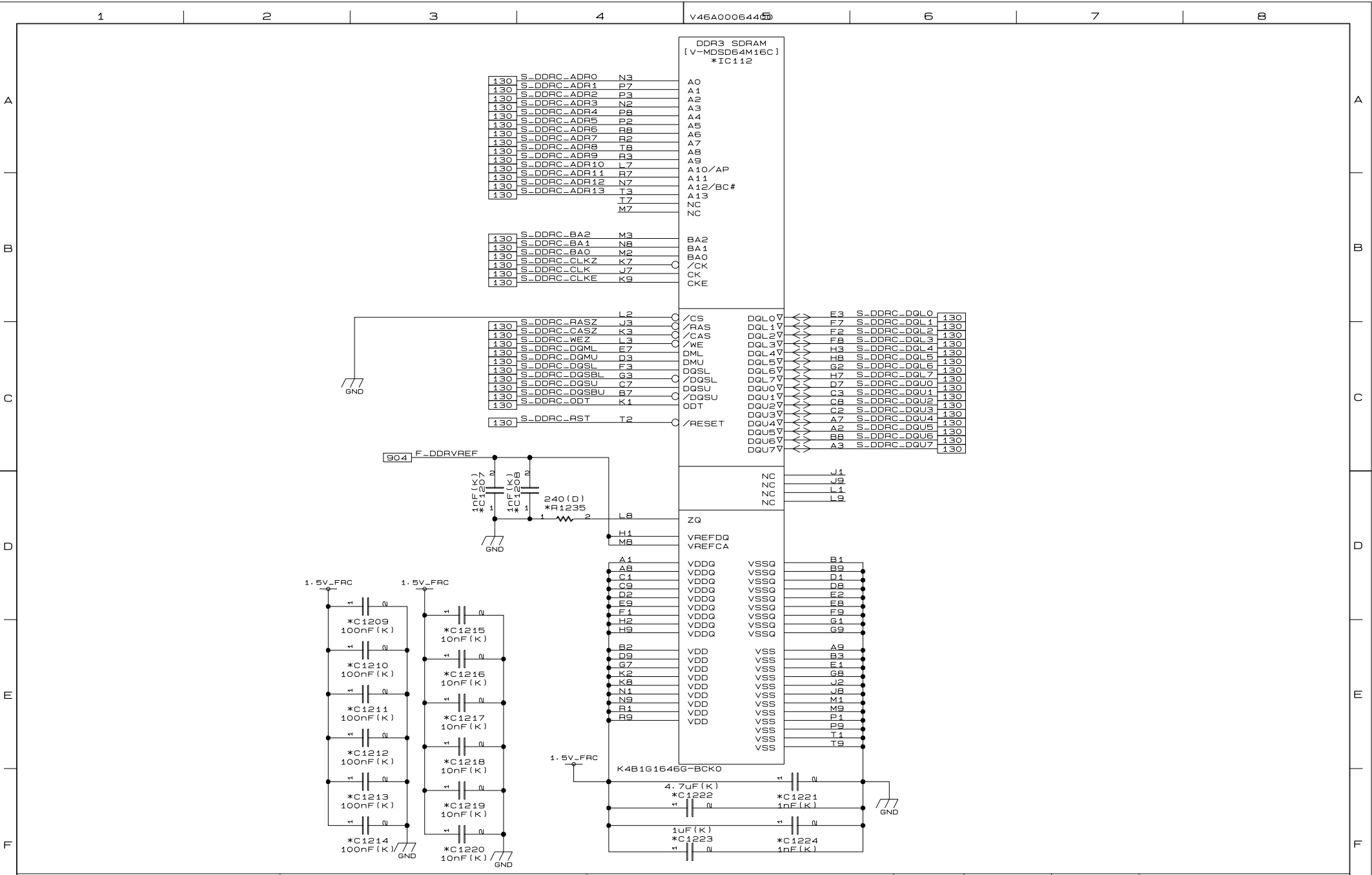
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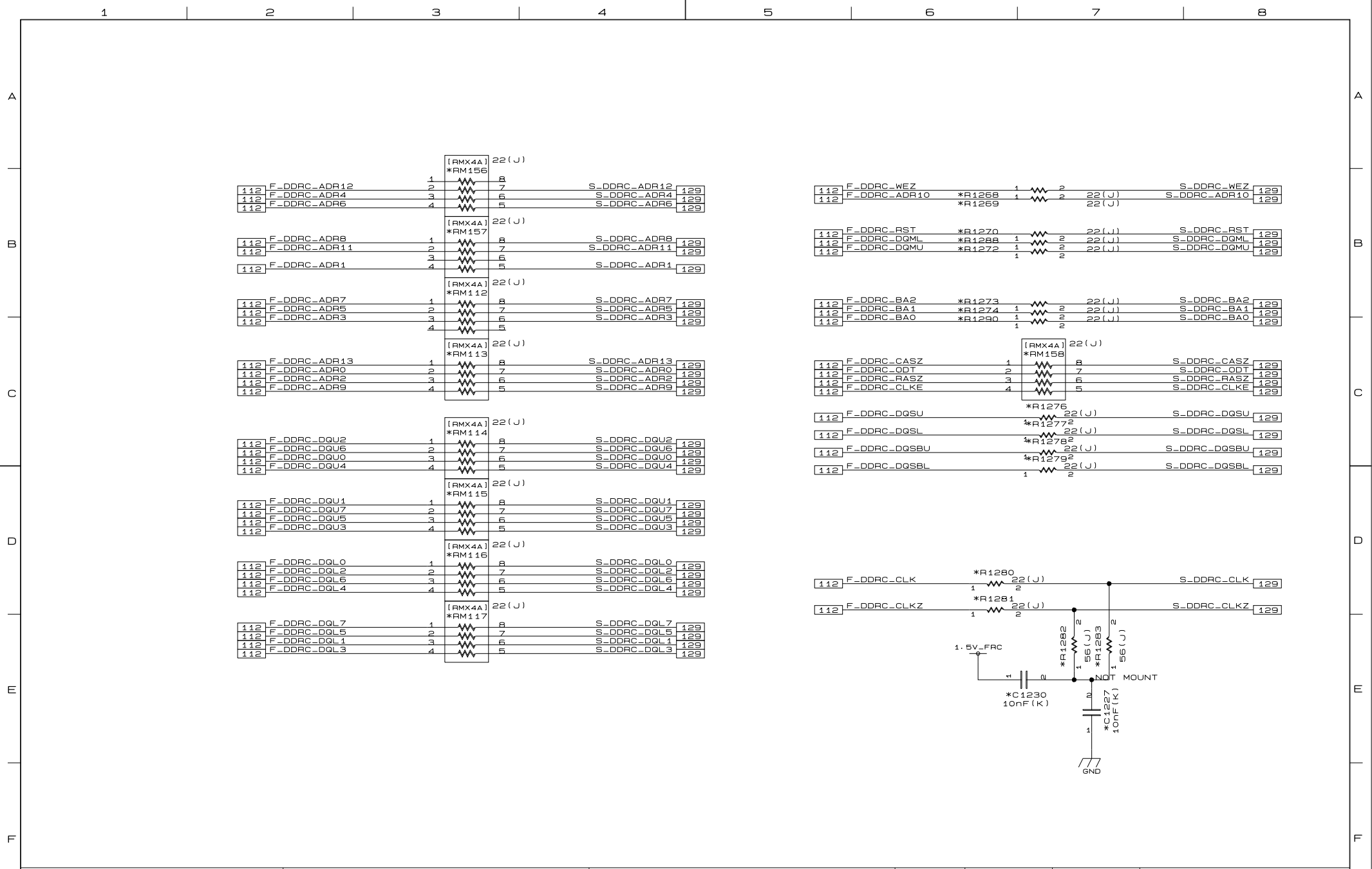
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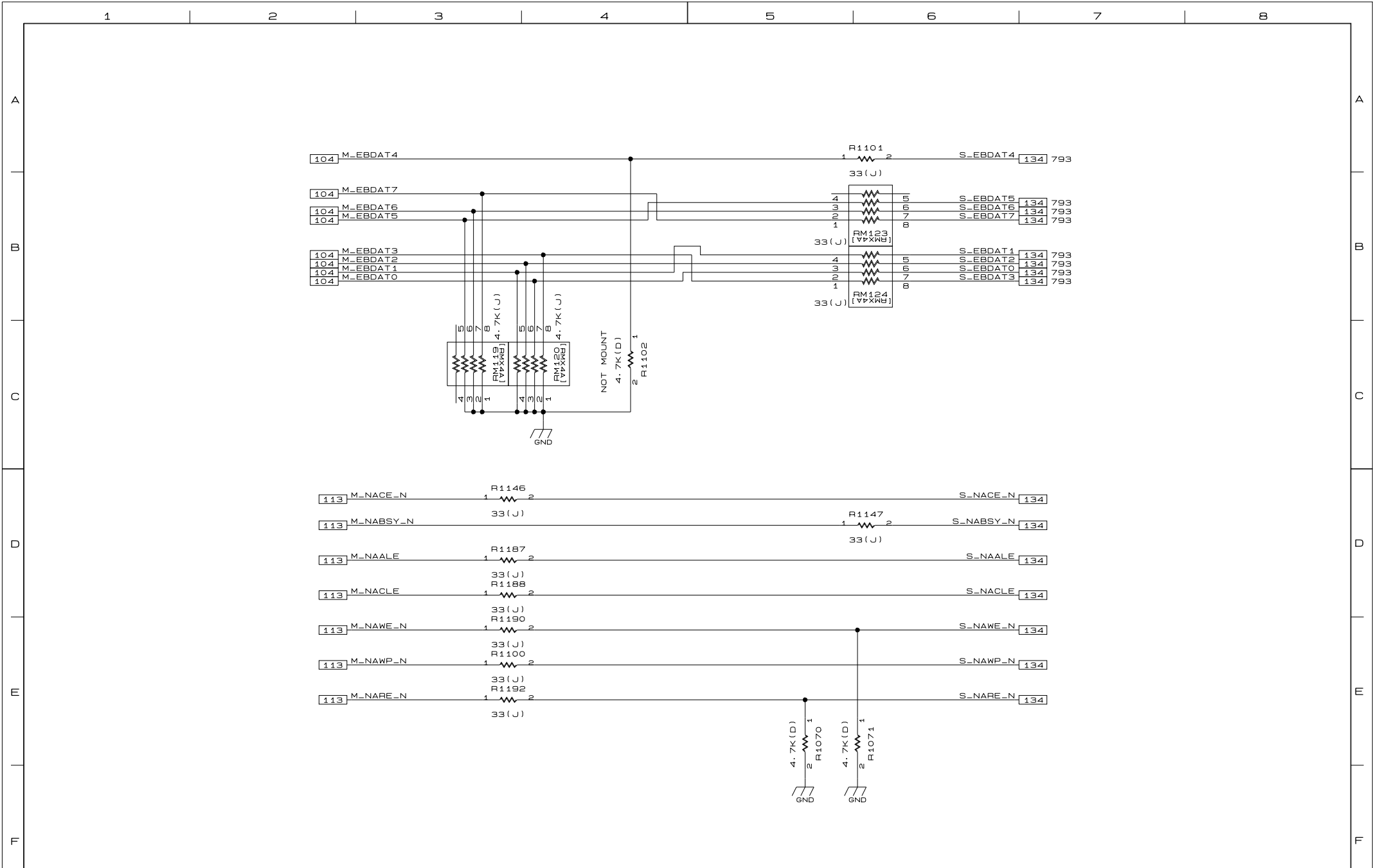
DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION DDR3 Damping Resistors	SH. NO. 128	PAGE NO. 25	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY OSANAI	2011/07/11	TITLE MAIN UNIT	FUNCTION DDR3 SDRAM	SH. NO. 129	PAGE NO. 26	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION DDR3 Series Resistors	SH. NO. 130	PAGE NO. 27	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY 2011/07/11
OSANAI

TITLE
MAIN UNIT

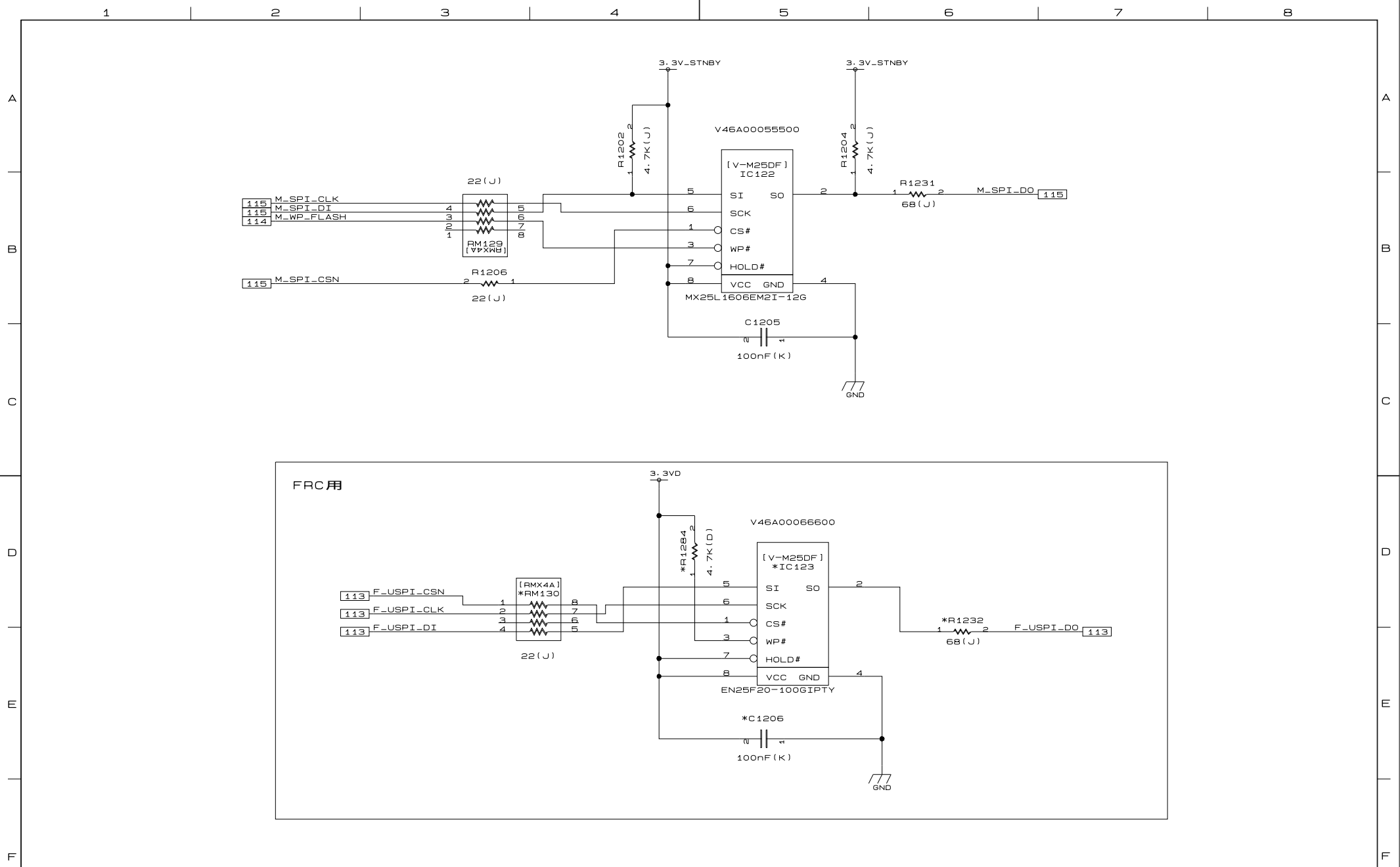
FUNCTION
EBUS RESISTOR

SH. NO.
131

PAGE NO.
28

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

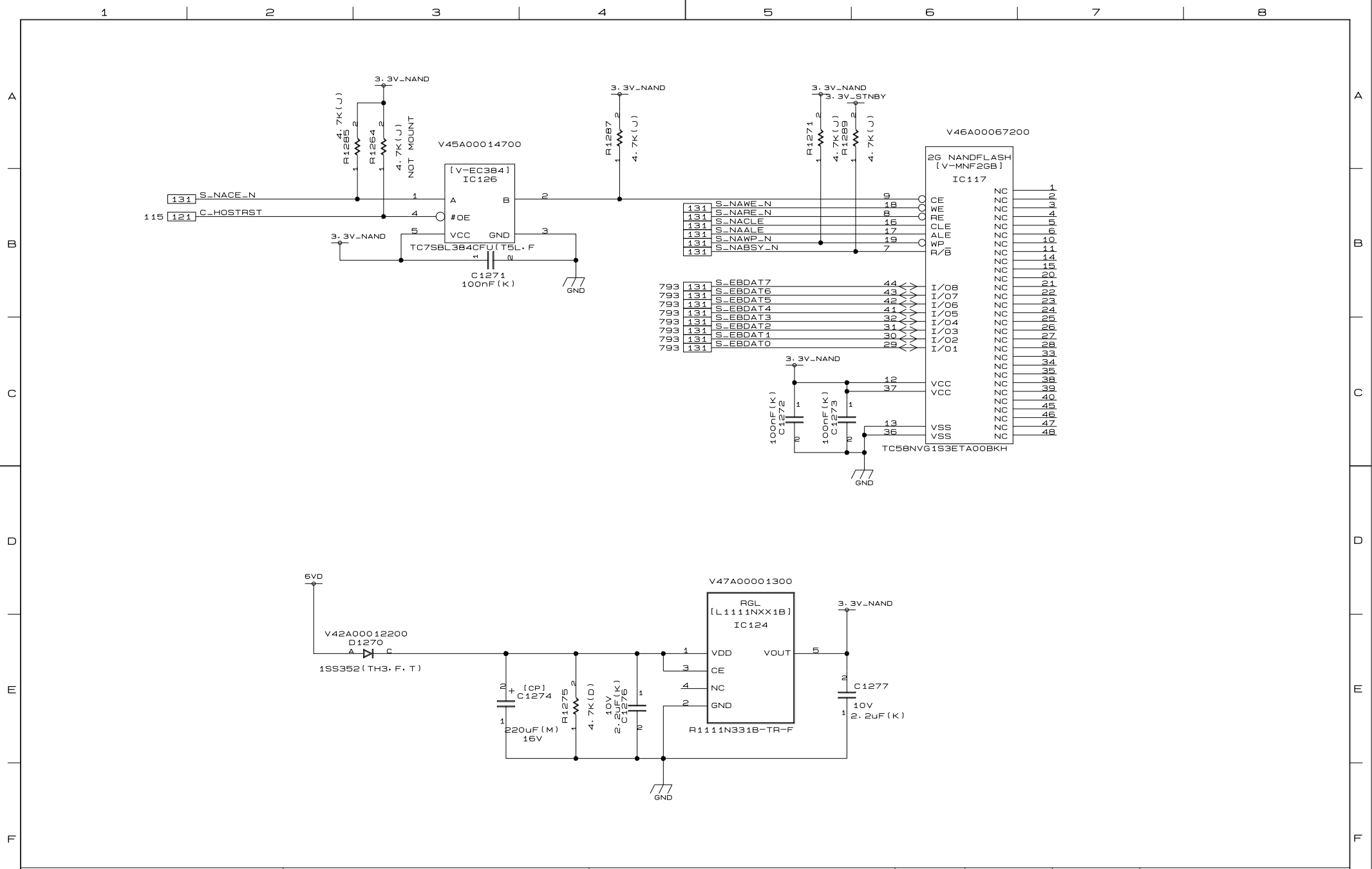
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SPI FLASH

SH. NO.
132

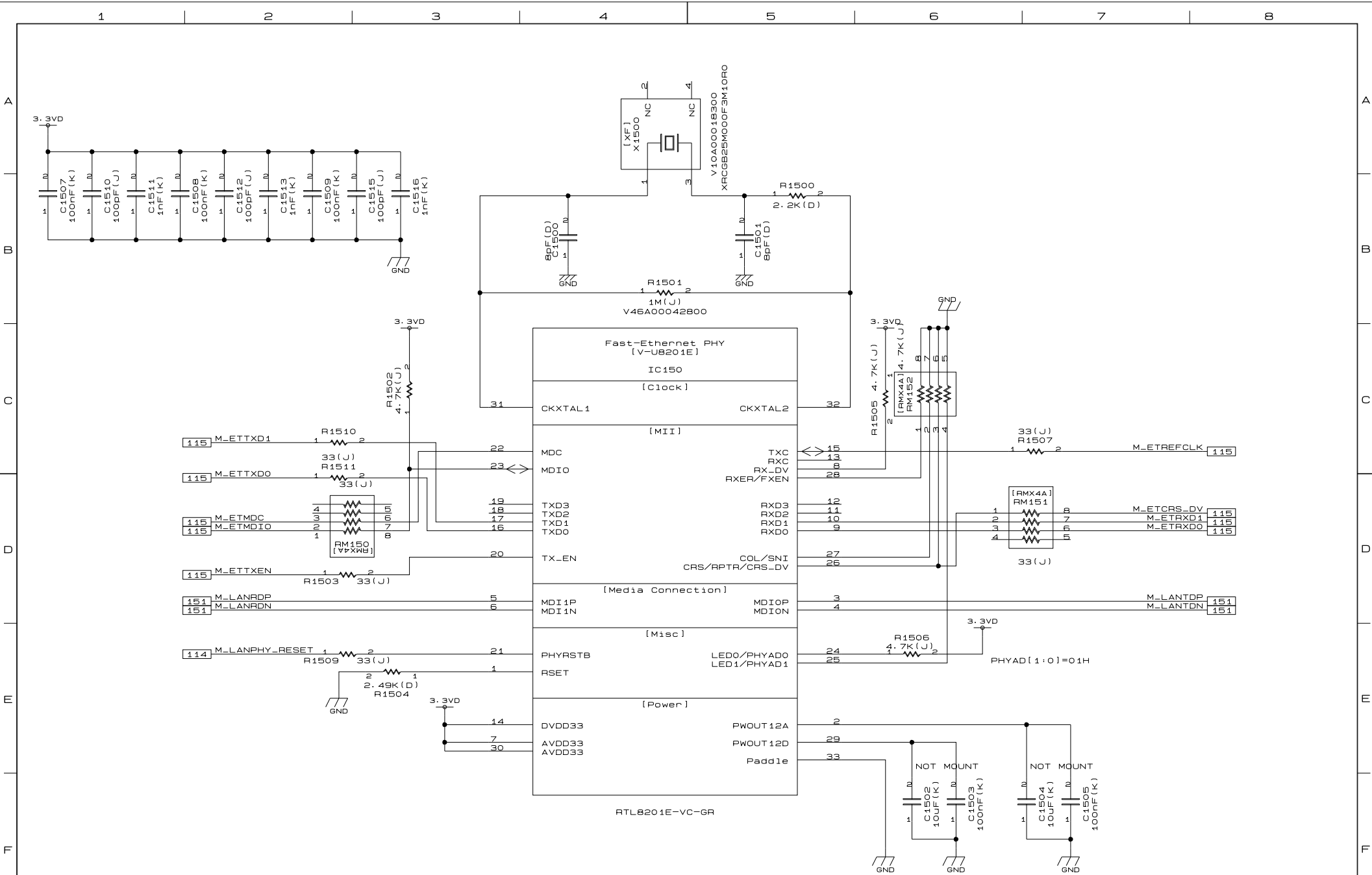
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REV. MARK
MP

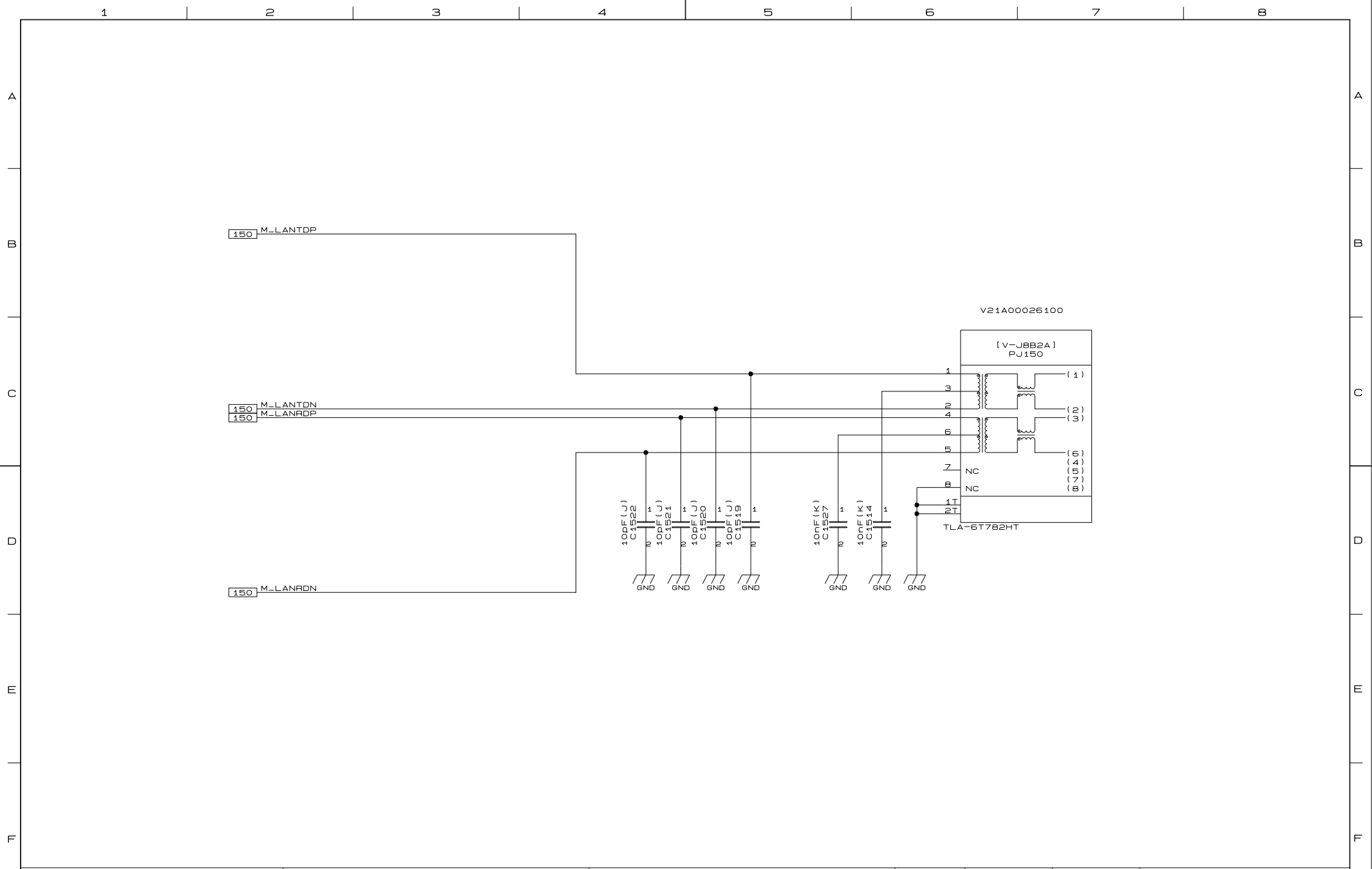
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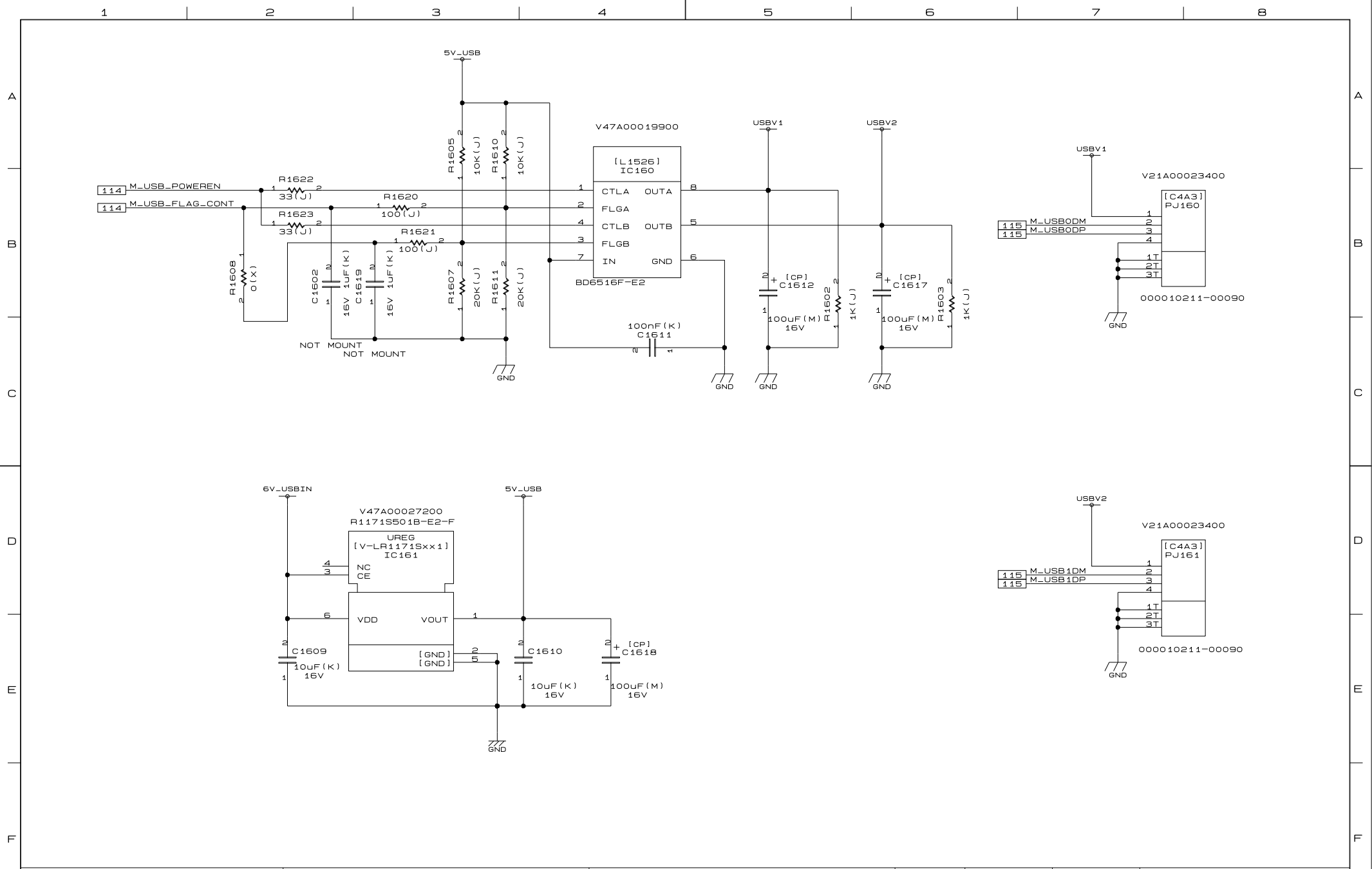
DESIGNED BY OSANAI	2011/07/11	TITLE MAIN UNIT	FUNCTION NAND FLASH	SH. NO. 134	PAGE NO. 30	REV. MARK MP	DRAWING. NO. PE1000ZA
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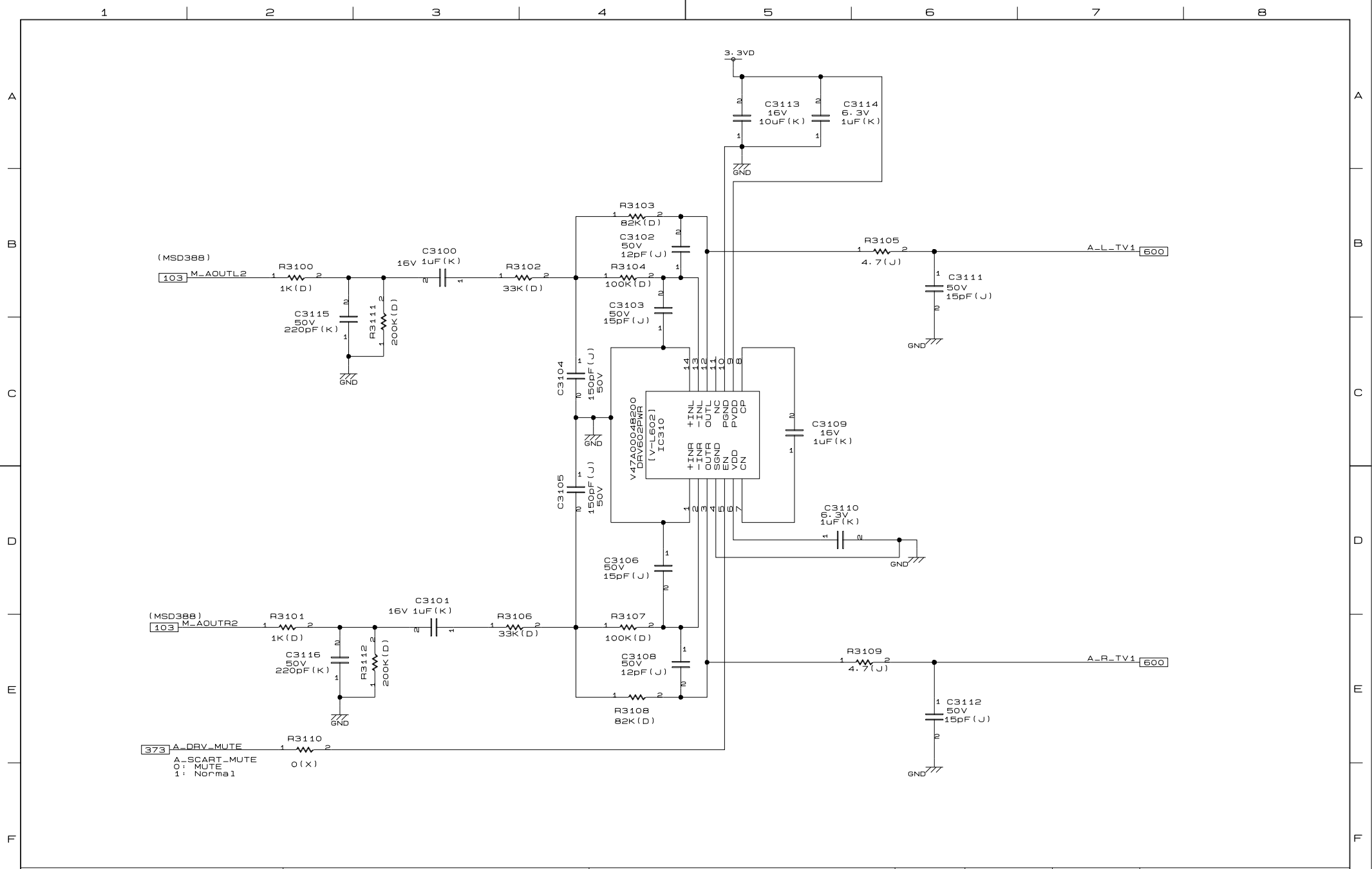
DESIGNED BY KOJIMA	TITLE MAIN UNIT	FUNCTION ETHER PHY	SH. NO. 150	PAGE NO. 31	REV. MARK MP	DRAWING NO. PE1000ZA
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DESIGNED BY KOJIMA	TITLE MAIN UNIT	FUNCTION RJ45 CONNECTOR	SH. NO. 151	PAGE NO. 32	REV. MARK MP	DRAWING. NO. PE1000ZA
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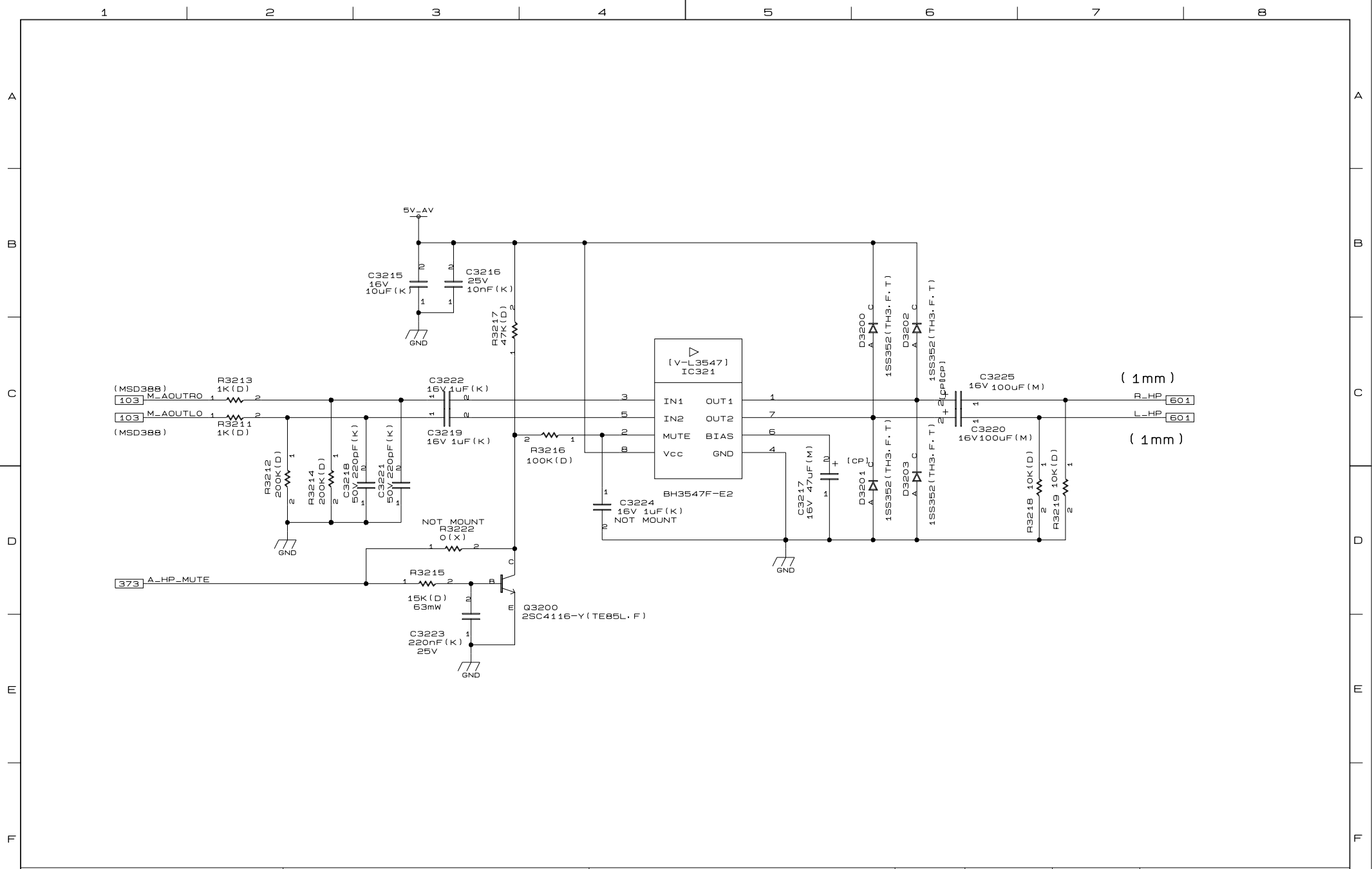


DESIGNED BY KOJIMA	TITLE MAIN UNIT	FUNCTION USB CONNECTOR(side) 160	SH. NO. 33	PAGE NO. MP	REV. MARK PE1000ZA	DRAWING. NO.
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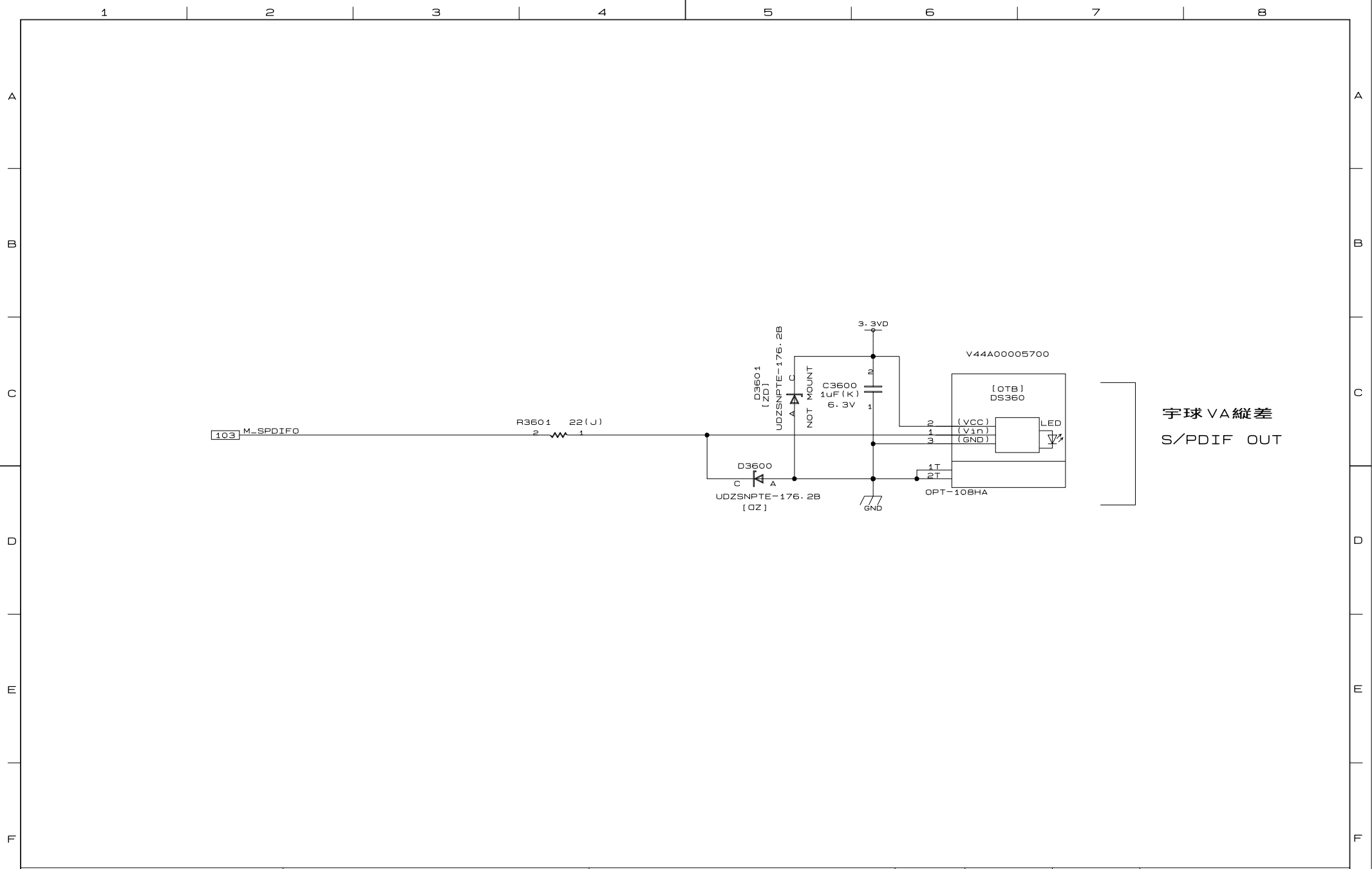


[373] A_DRV_MUTE
 A_SCART_MUTE
 0: MUTE
 1: Normal

DESIGNED BY ENDO	TITLE MAIN UNIT	FUNCTION AUDIO TV OUTPUT	SH. NO. 310	PAGE NO. 34	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY ENDO	TITLE MAIN UNIT	FUNCTION Headphone AMP	SH. NO. 321	PAGE NO. 35	REV. MARK MP	DRAWING. NO. PE1000ZA
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宇球VA縦差
S/PDIF OUT

DESIGNED BY
ENDO

TITLE
MAIN UNIT

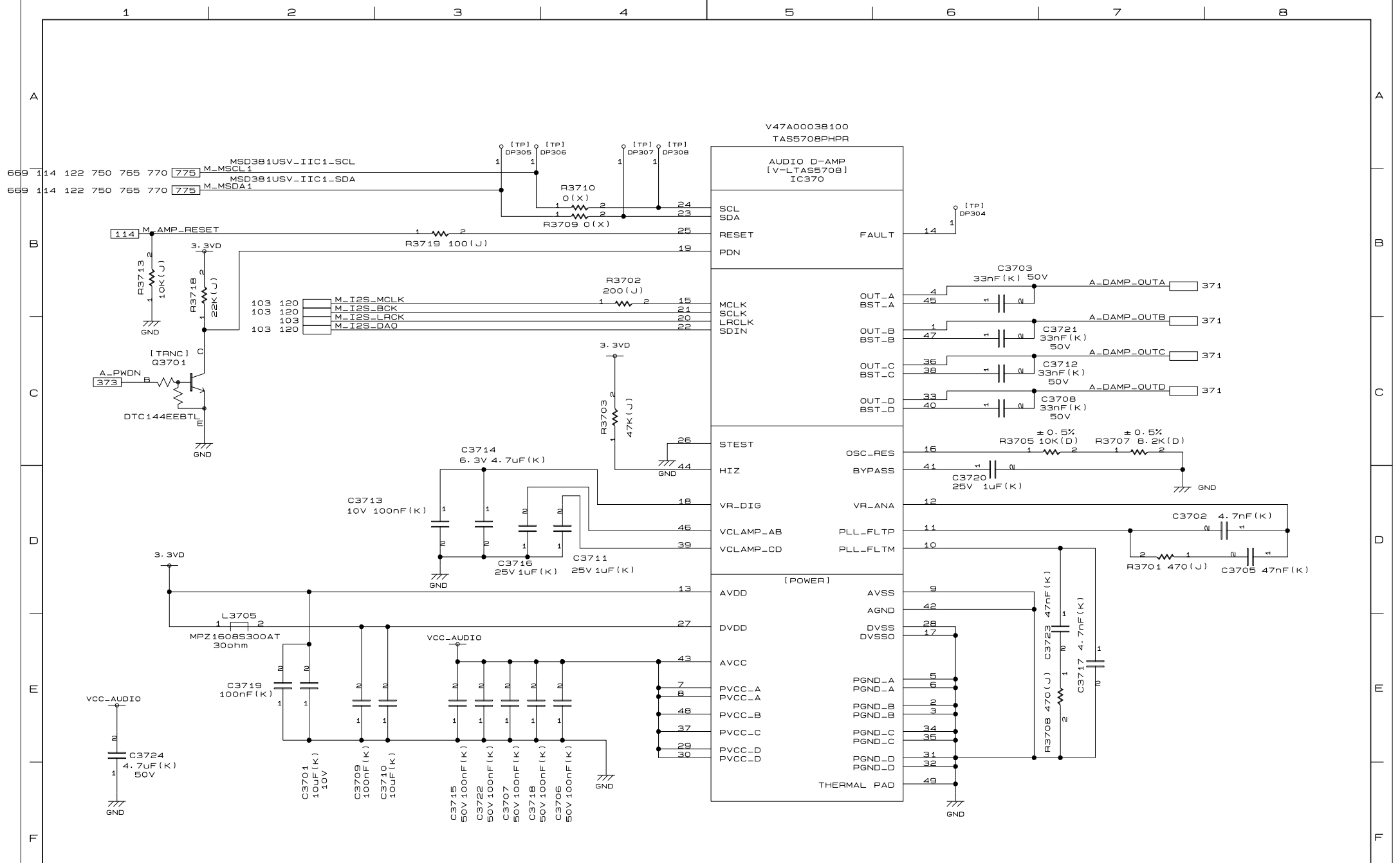
FUNCTION
OPTICAL OUTPUT

SH. NO.
360

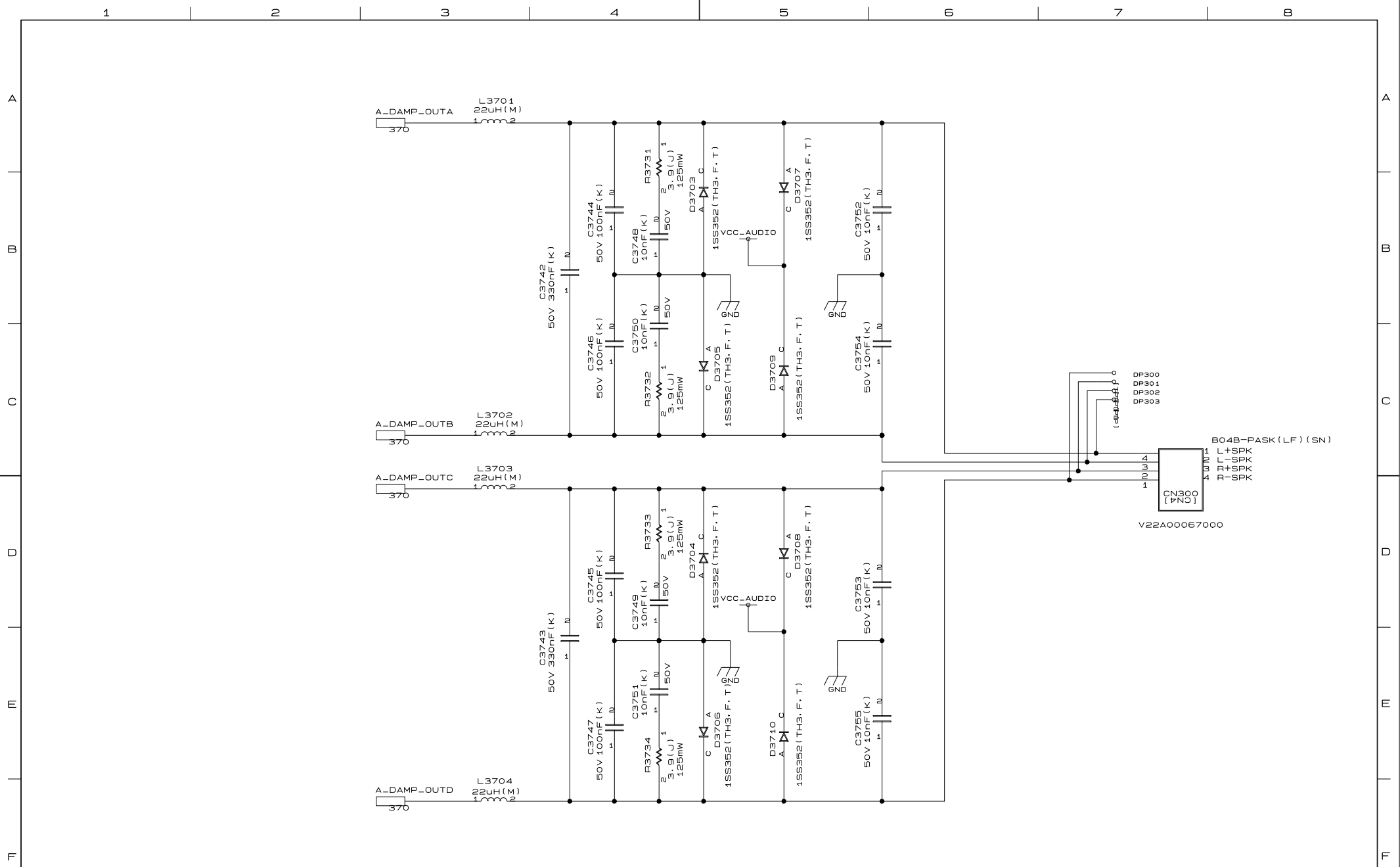
PAGE NO.
36

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY ENDO	TITLE MAIN UNIT	FUNCTION AUDIO D-AMP (MAIN)	SH. NO. 370	PAGE NO. 37	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY
ENDO

TITLE
MAIN UNIT

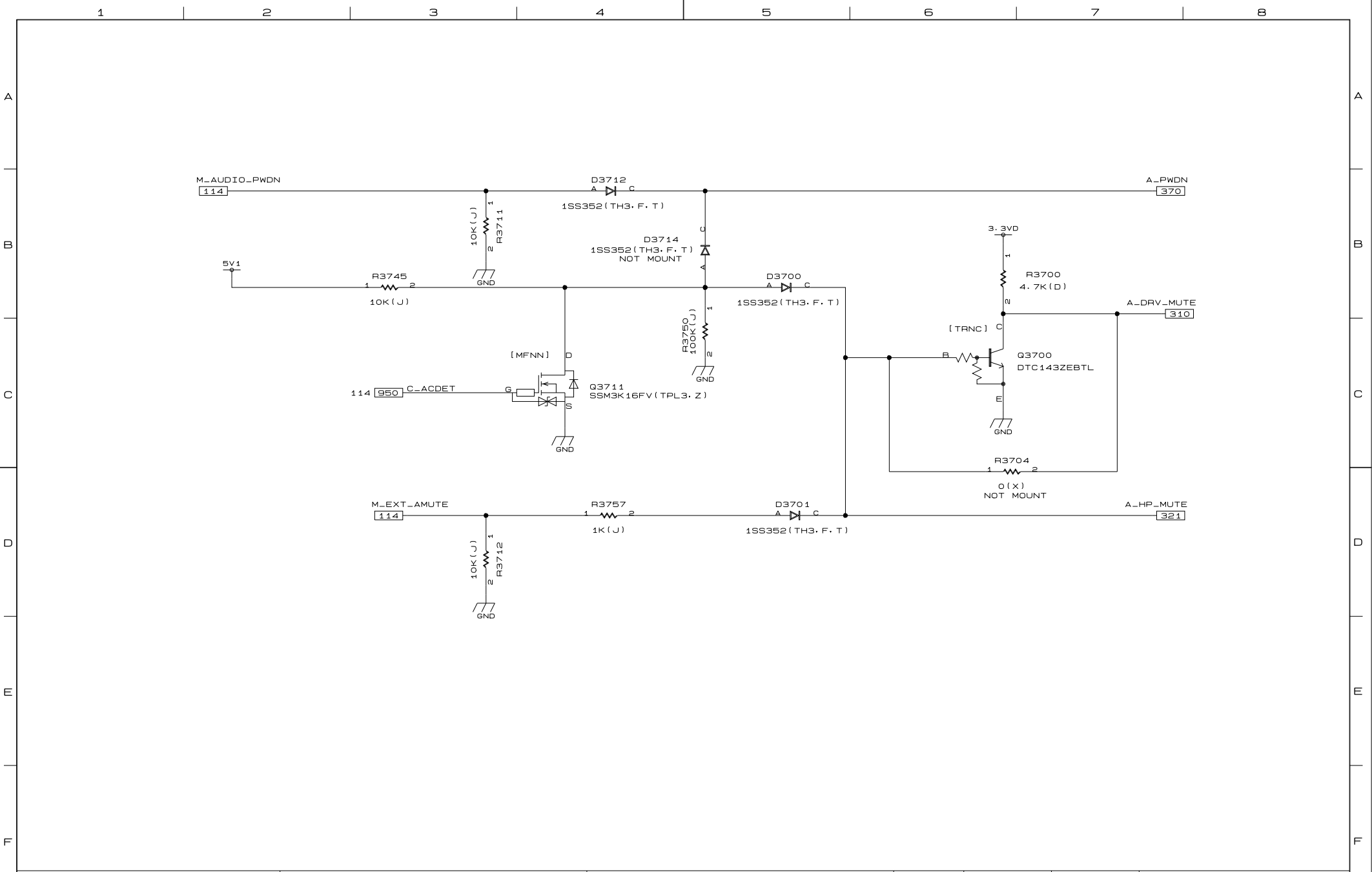
FUNCTION
AUDIO D-AMP OUT (MAIN)

SH. NO.
371

PAGE NO.
38

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY
ENDO

TITLE
MAIN UNIT

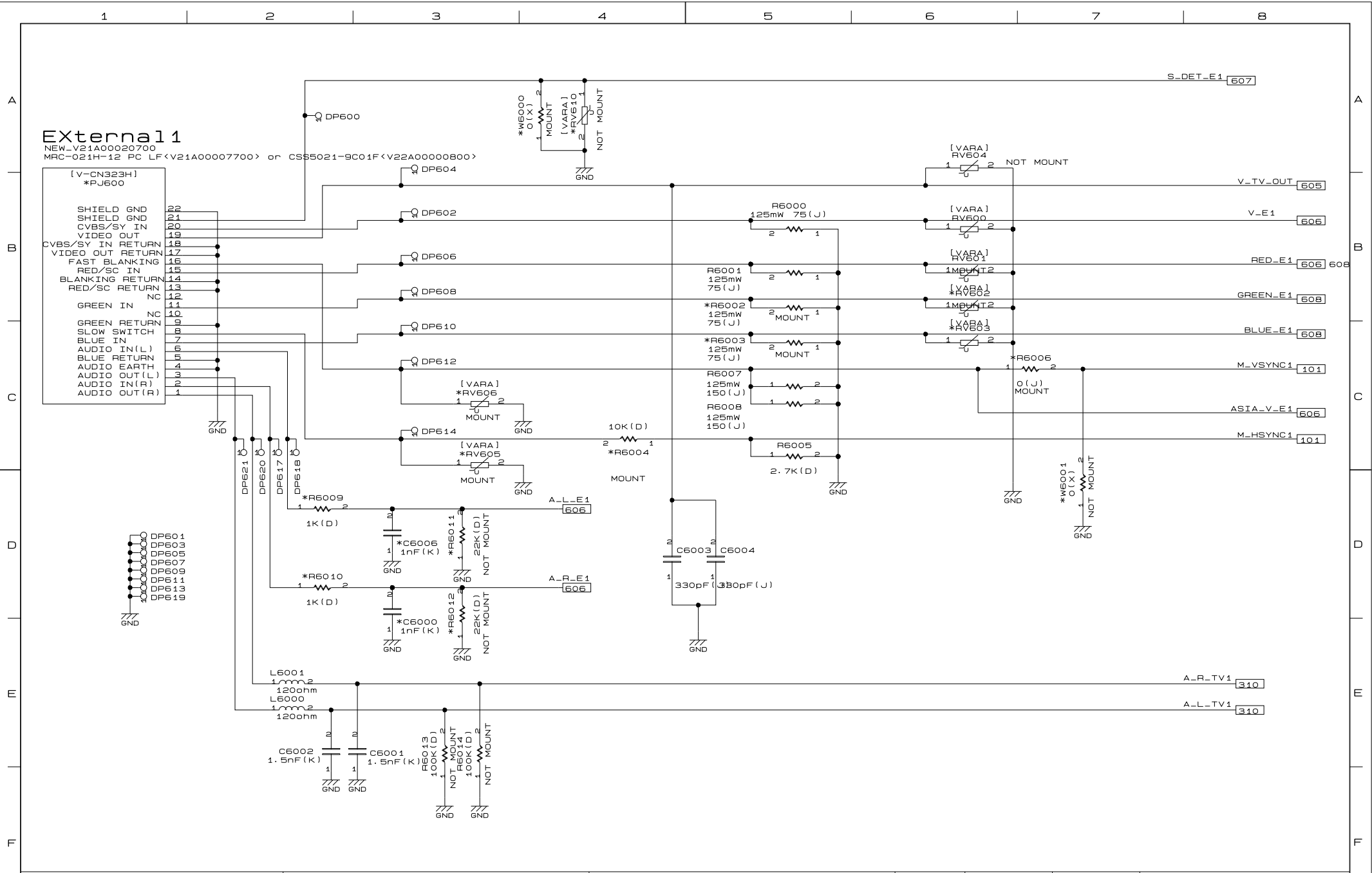
FUNCTION
AUDIO MUTE

SH. NO.
373

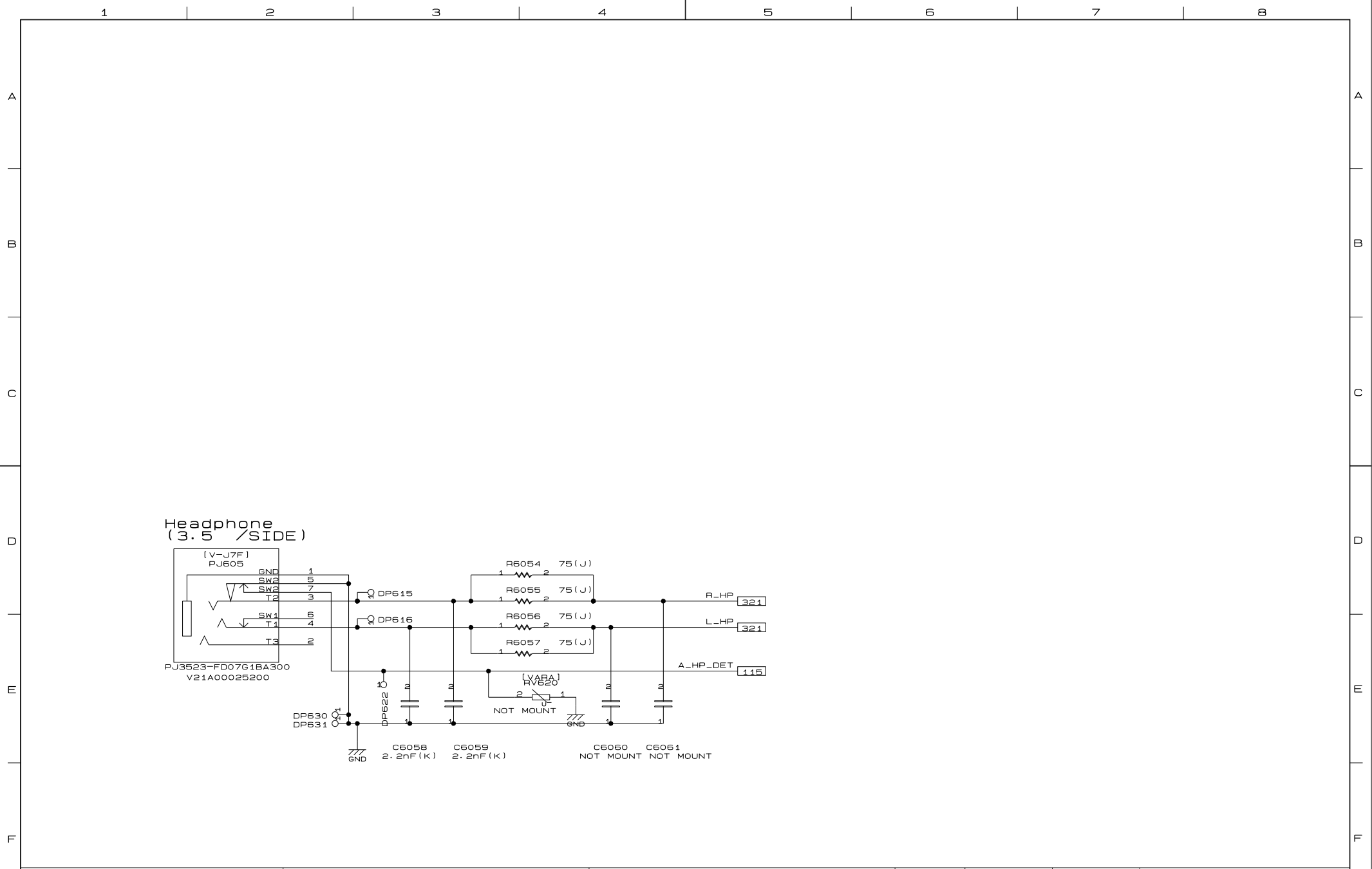
PAGE NO.
39

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY SHIBATA/Audio	TITLE MAIN UNIT	FUNCTION EURO SCART	SH. NO. 600	PAGE NO. 40	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY
SHIBATA/Audio

TITLE
MAIN UNIT

FUNCTION
FRONT AV

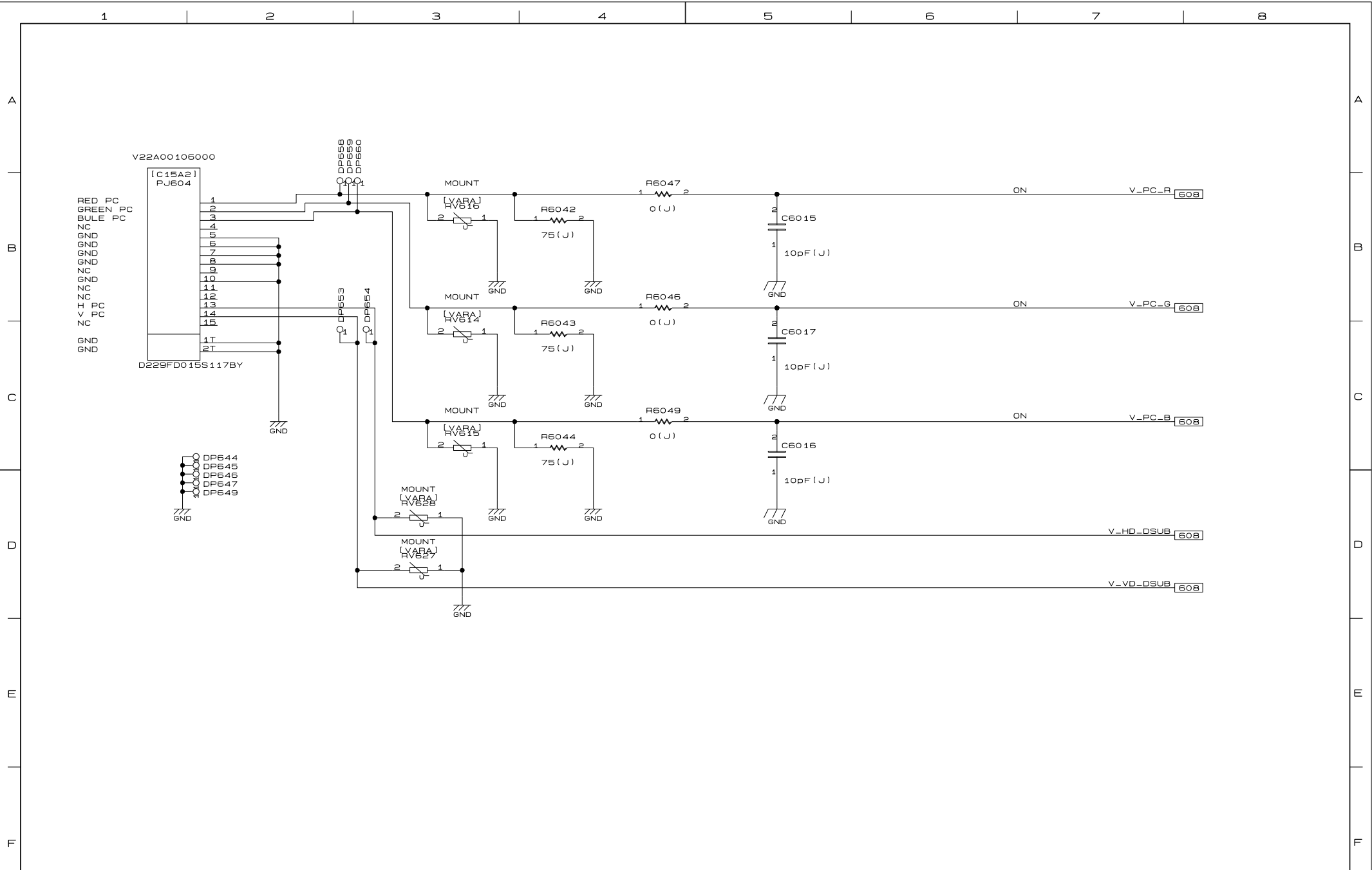
SH. NO.
601

PAGE NO.
41

REV. MARK
MP

DRAWING. NO.
PE1000ZA

2011.07.11 18:55



DESIGNED BY
SHIBATA

TITLE
MAIN UNIT

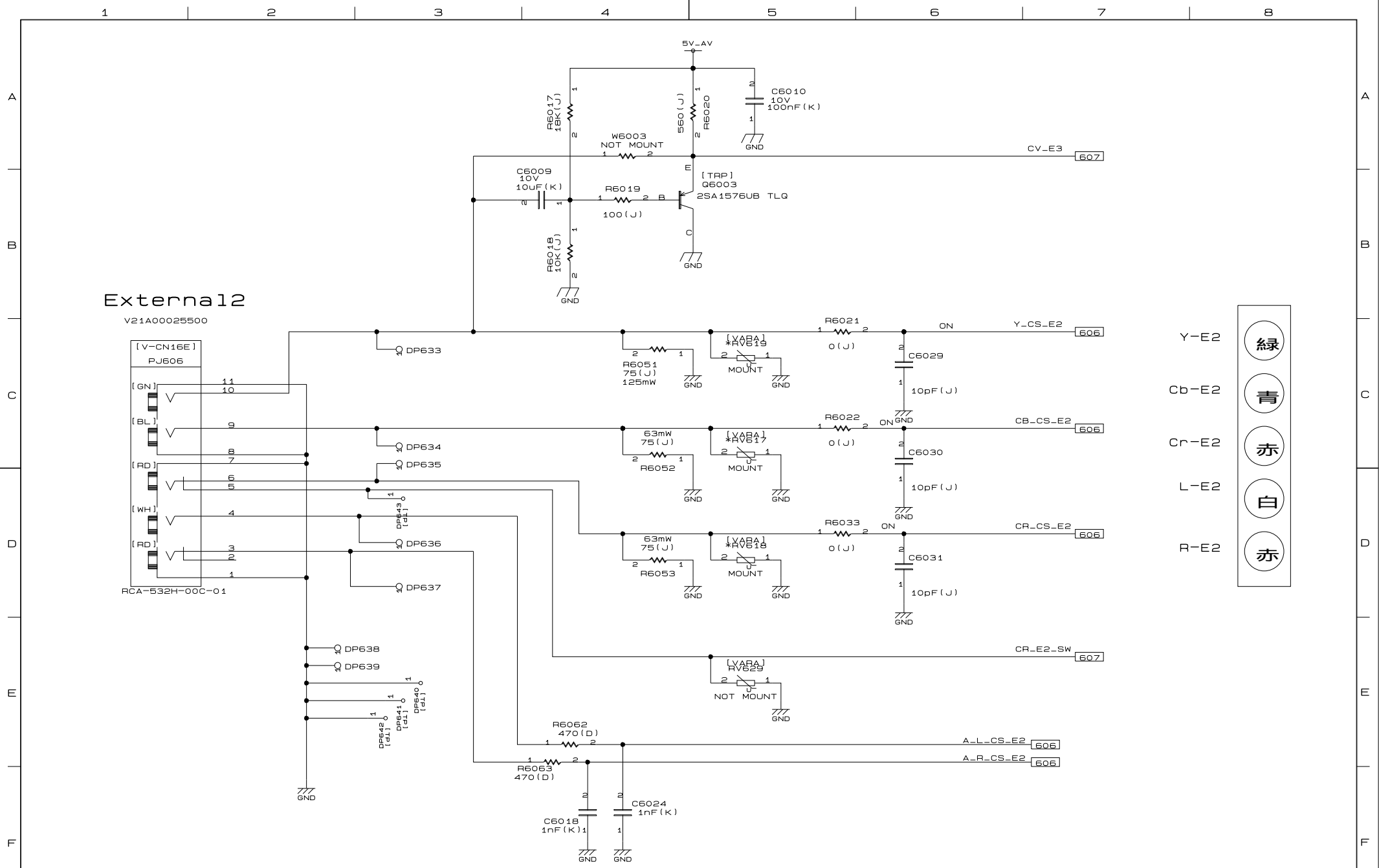
FUNCTION
DSUB PC IN

SH. NO.
602

PAGE NO.
42

REV. MARK
MP

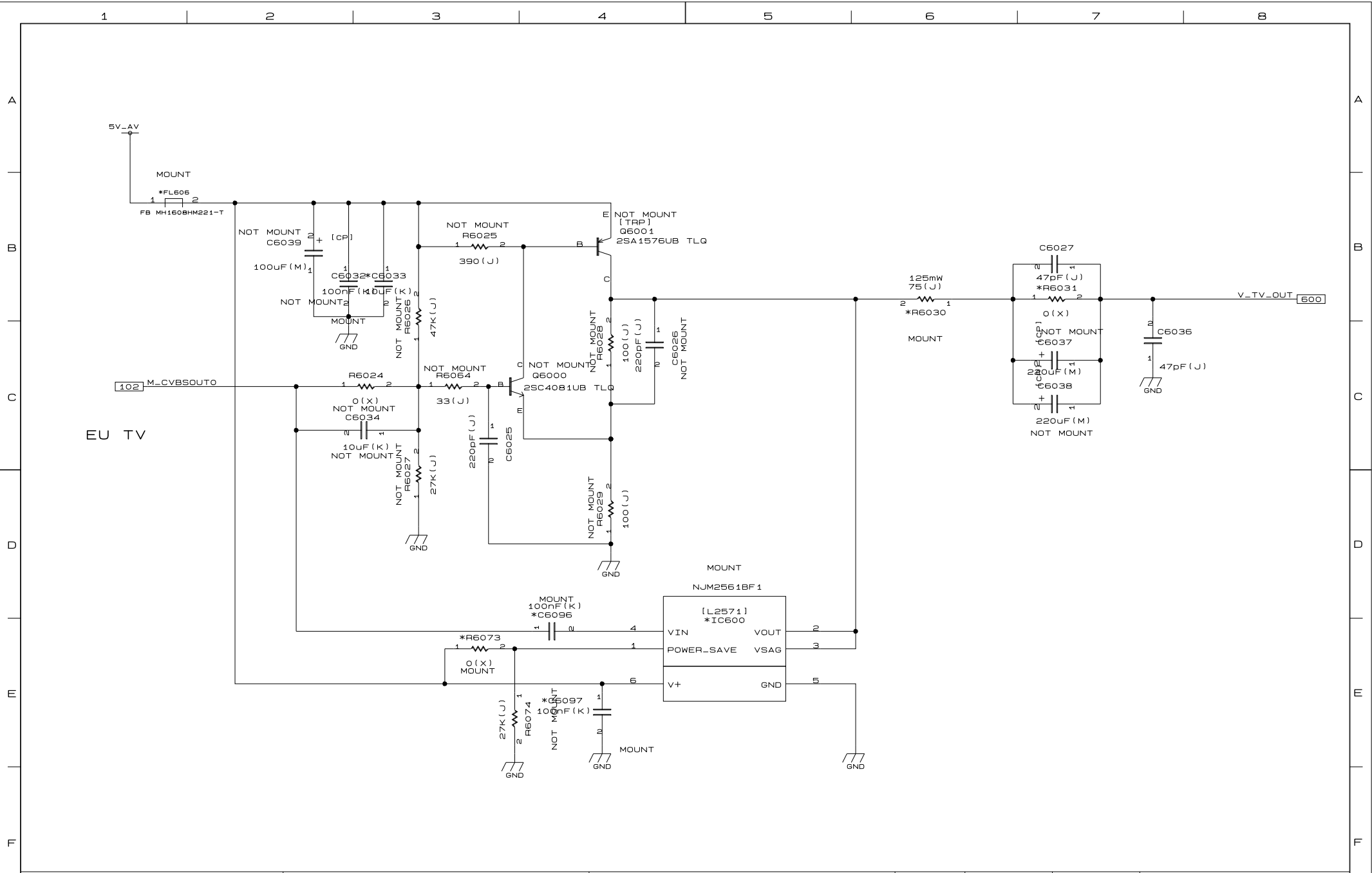
DRAWING. NO.
PE1000ZA



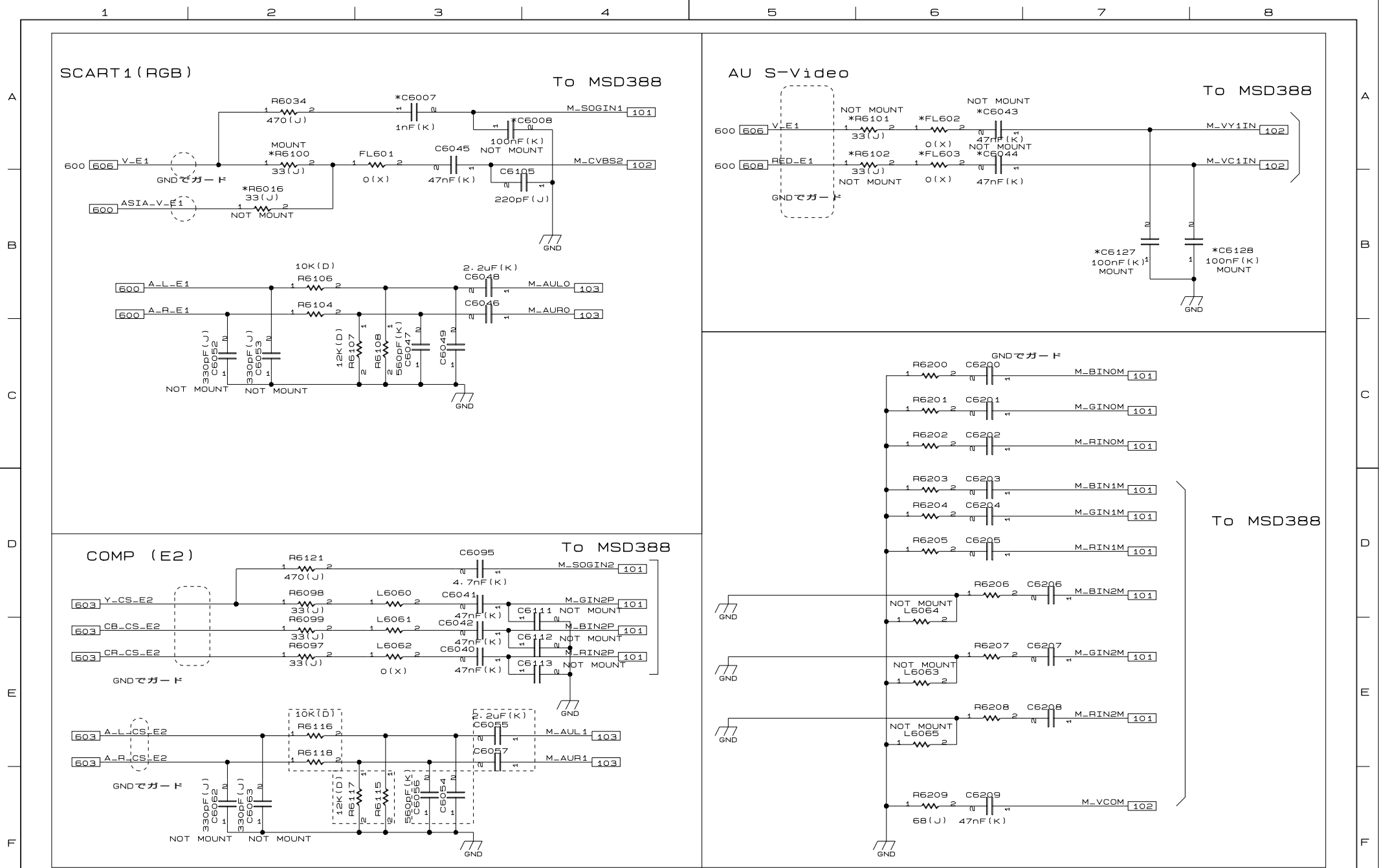
- Y-E2 (緑)
- Cb-E2 (青)
- Cr-E2 (赤)
- L-E2 (白)
- R-E2 (赤)

DESIGNED BY SHIBATA/Audio	TITLE MAIN UNIT	FUNCTION EURO COMPONENT IN	SH. NO. 603	PAGE NO. 43	REV. MARK MP	DRAWING. NO. PE1000ZA
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2011.07.11 18:55



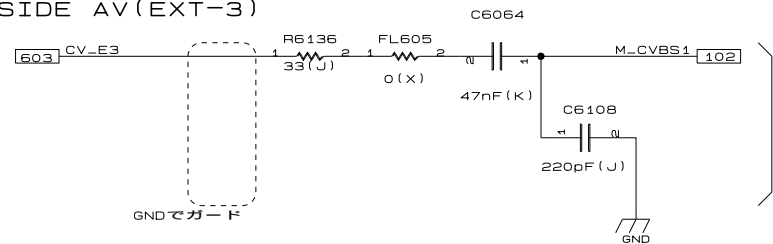
DESIGNED BY SHIBATA	TITLE MAIN UNIT	FUNCTION EURO TV OUT	SH. NO. 605	PAGE NO. 44	REV. MARK MP	DRAWING. NO. PE1000ZA
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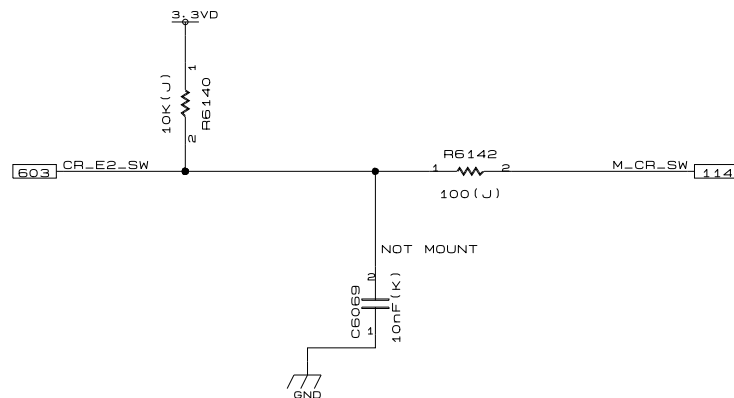
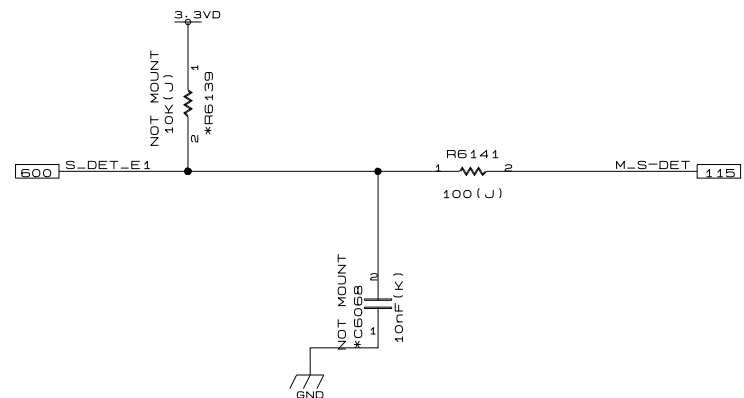
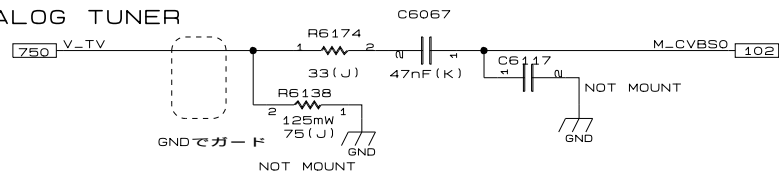
DESIGNED BY SHIBATA/Audio	TITLE MAIN UNIT	FUNCTION AV INPUT 1	SH. NO. 606	PAGE NO. 45	REV. MARK MP	DRAWING. NO. PE1000ZA
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2011.07.11 18:55

INPUT
SIDE AV(EXT-3)



ANALOG TUNER



DESIGNED BY

SHIBATA/Audio

TITLE

MAIN UNIT

FUNCTION

AV INPUT2

SH. NO.

607

PAGE NO.

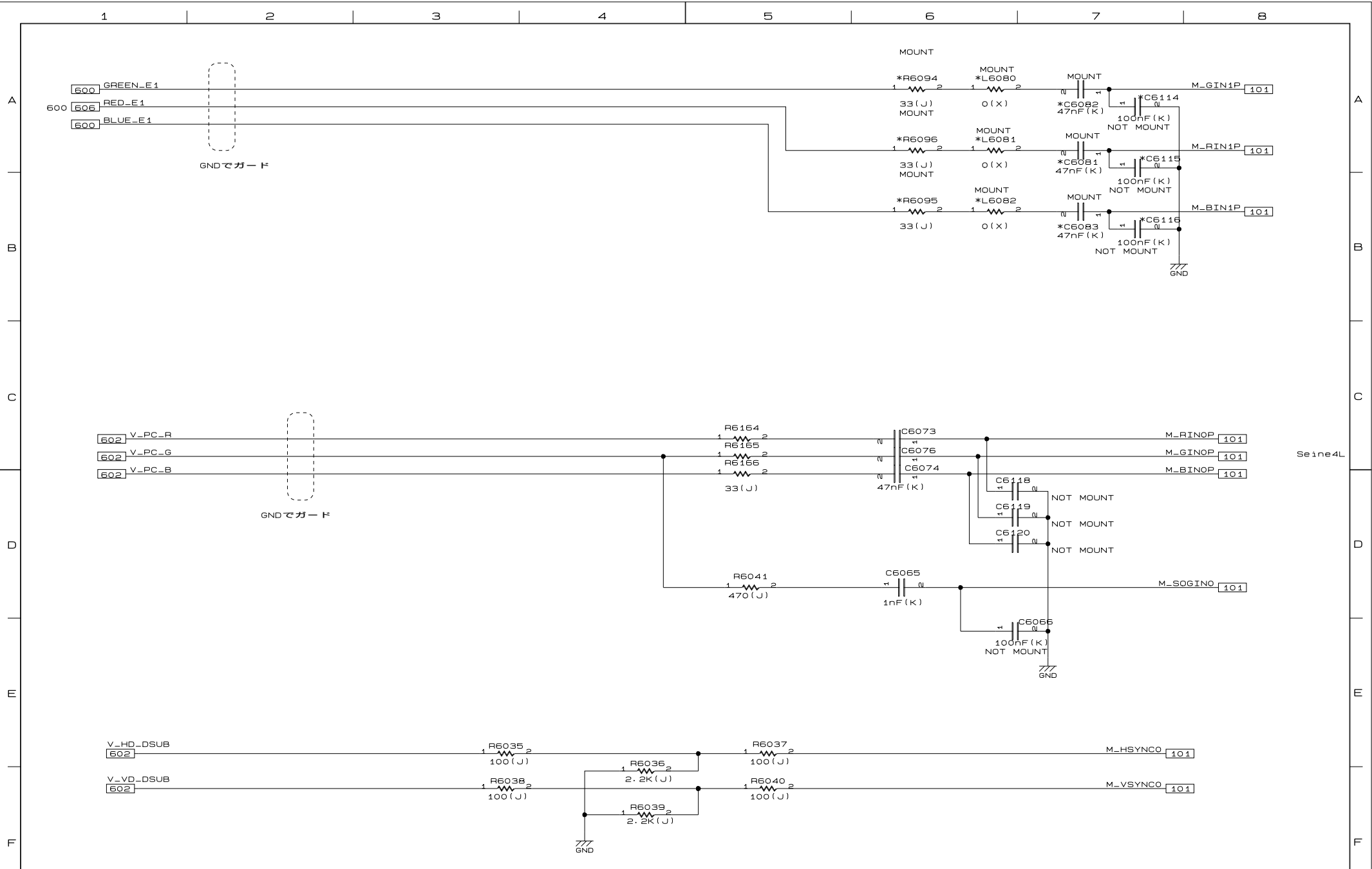
46

REV. MARK

MP

DRAWING. NO.

PE1000ZA



DESIGNED BY
SHIBATA/Endo

TITLE
MAIN UNIT

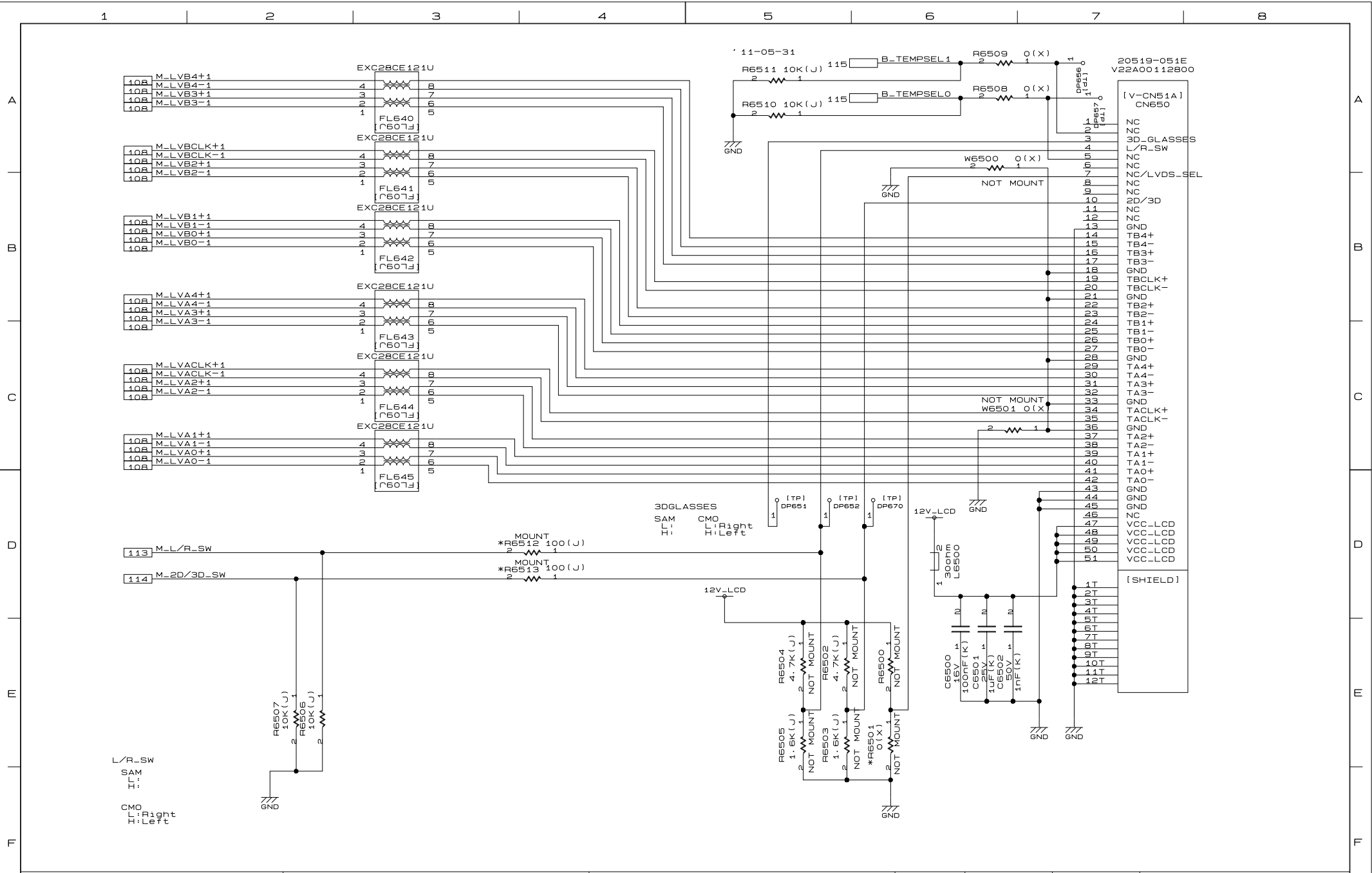
FUNCTION
AV INPUT3

SH. NO.
608

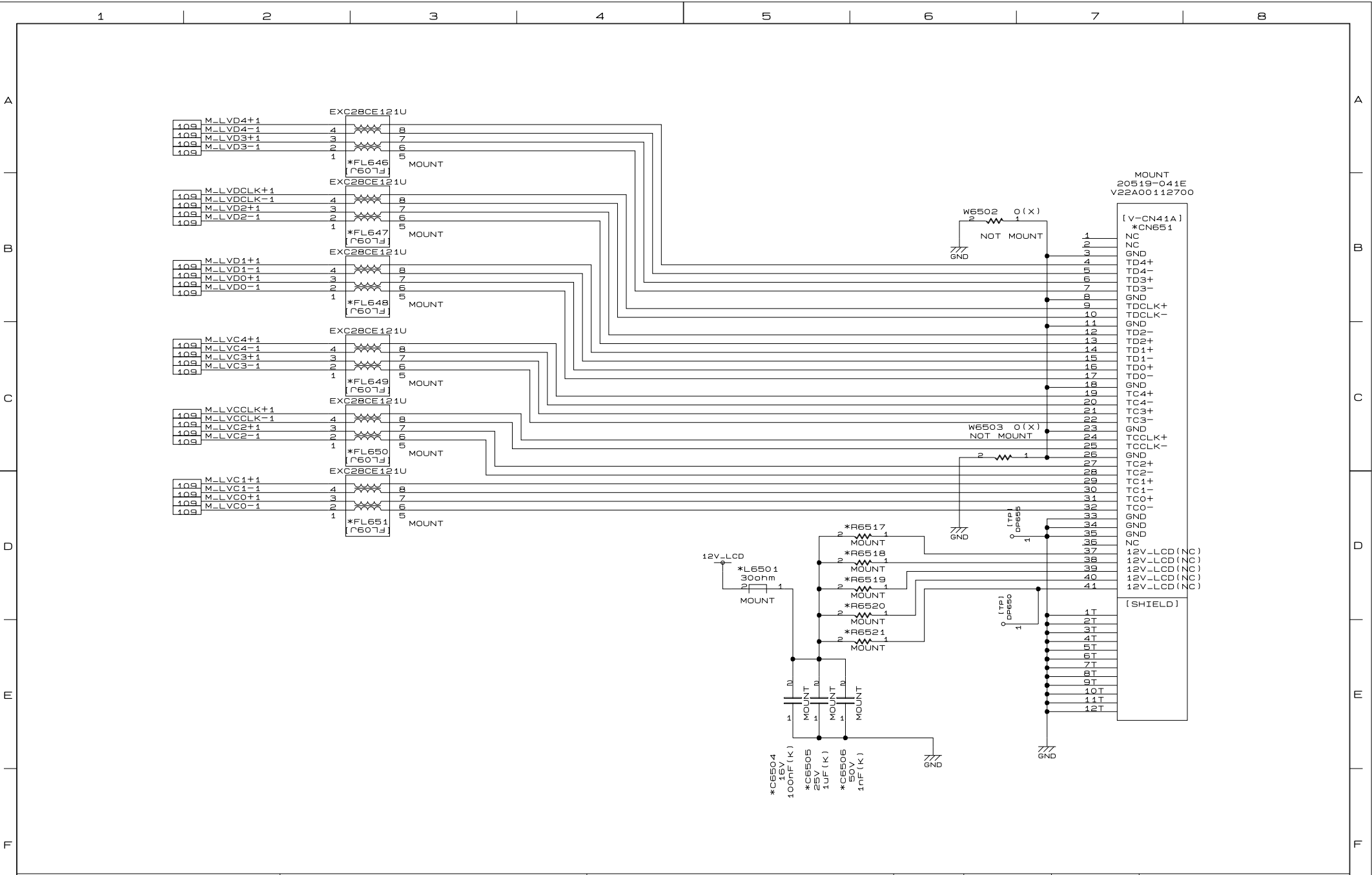
PAGE NO.
47

REV. MARK
MP

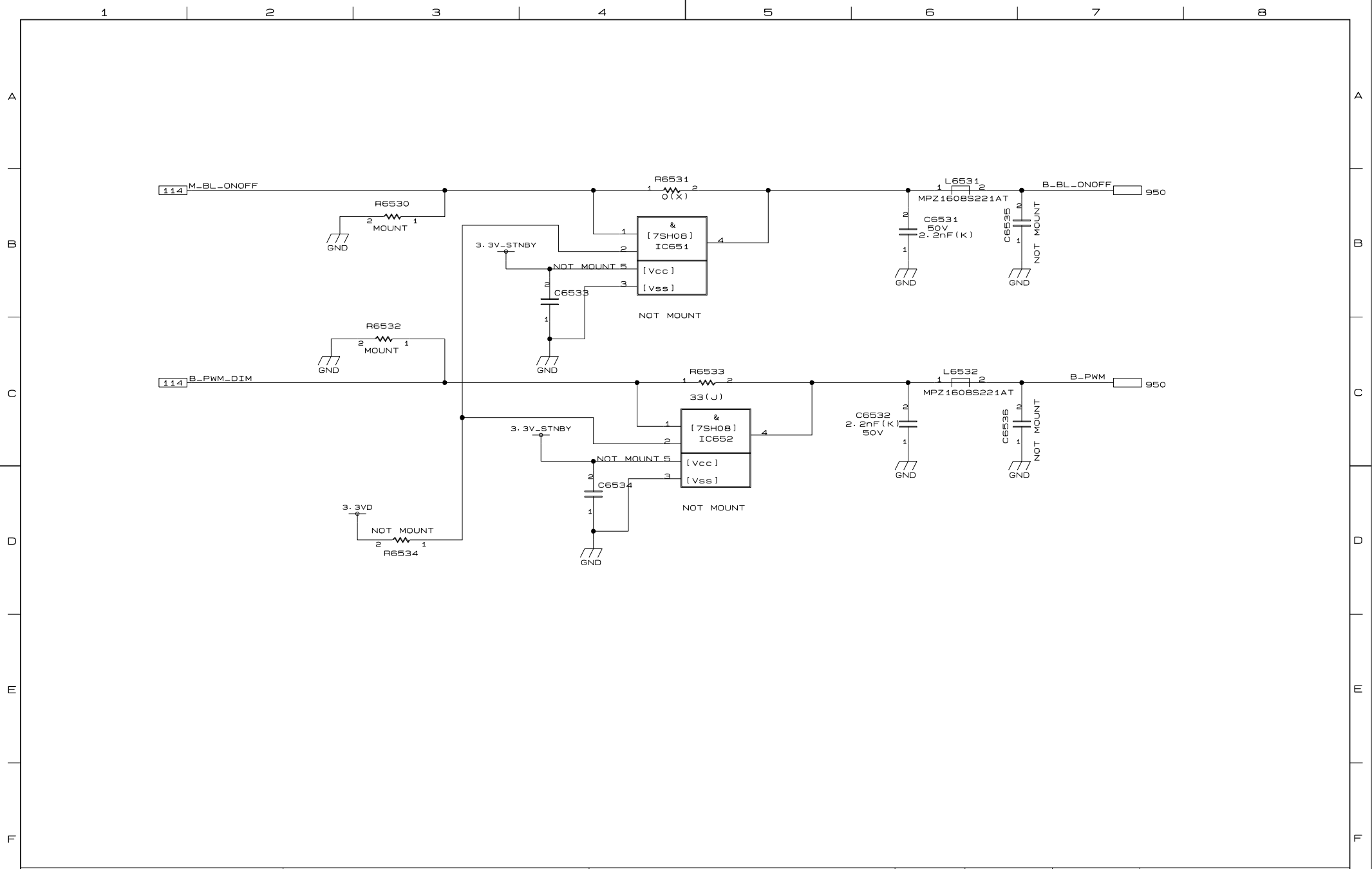
DRAWING. NO.
PE1000ZA



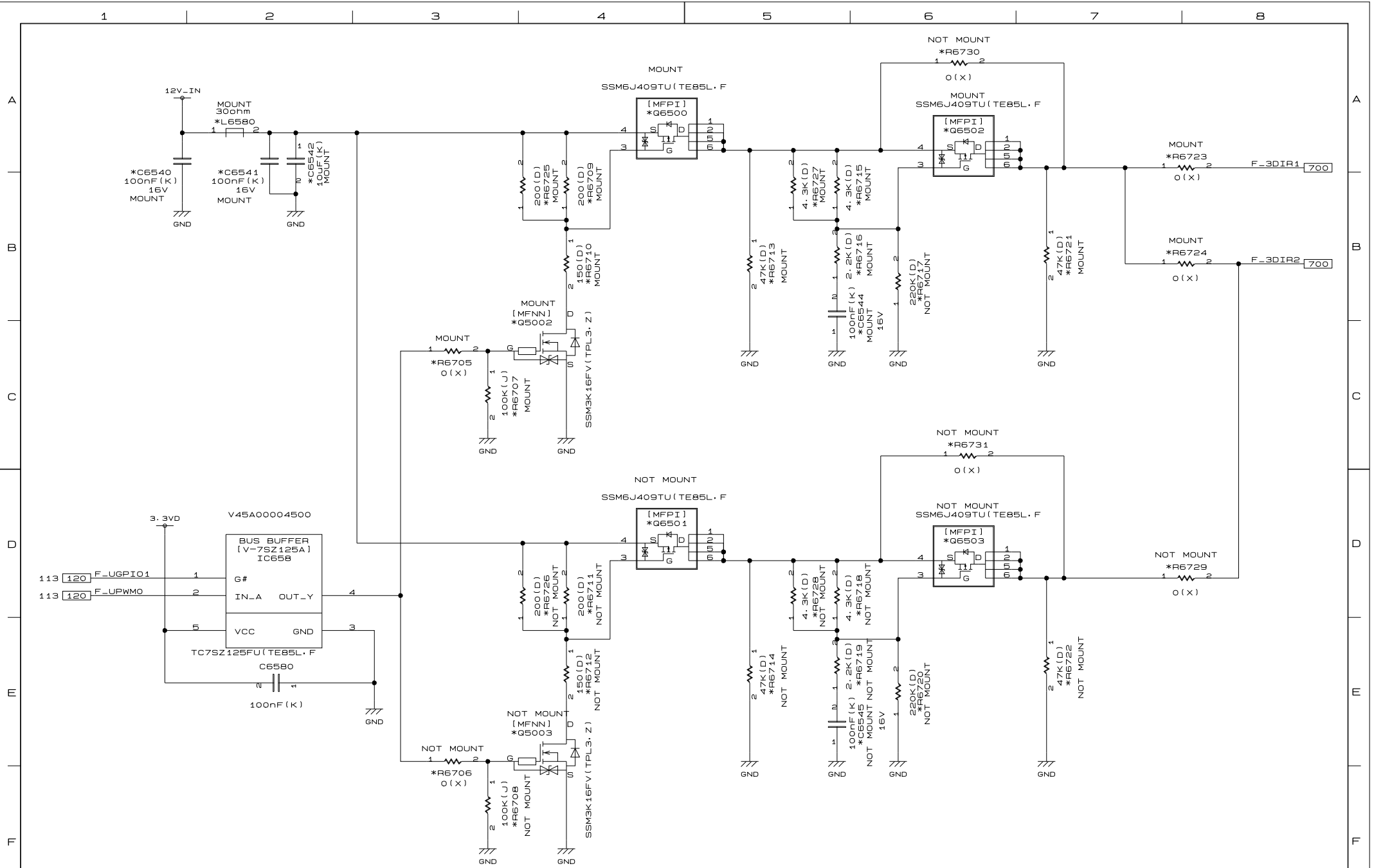
DESIGNED BY S. KOMIYA	TITLE MAIN UNIT	FUNCTION LVDS CONNECTOR	SH. NO. 650	PAGE NO. 48	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY S. KOMIYA	TITLE MAIN UNIT	FUNCTION LVDS CONNECTOR	SH. NO. 651	PAGE NO. 49	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY S. KOMIYA	TITLE MAIN UNIT	FUNCTION BL_CONTROL & DIMMING	SH. NO. 652	PAGE NO. 50	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY
M. SATO

TITLE
MAIN UNIT

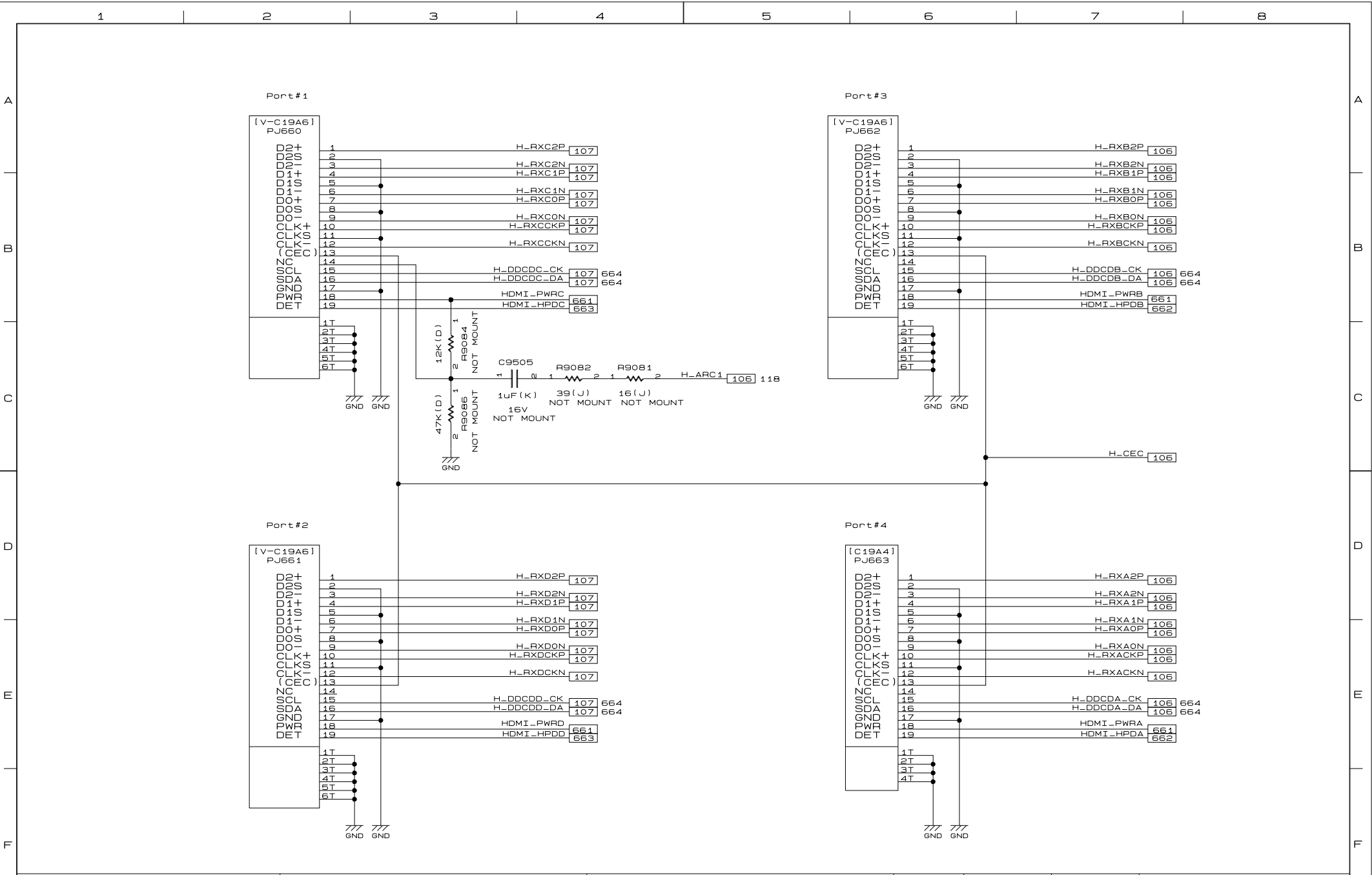
FUNCTION
@Func*i*

SH. NO.
658

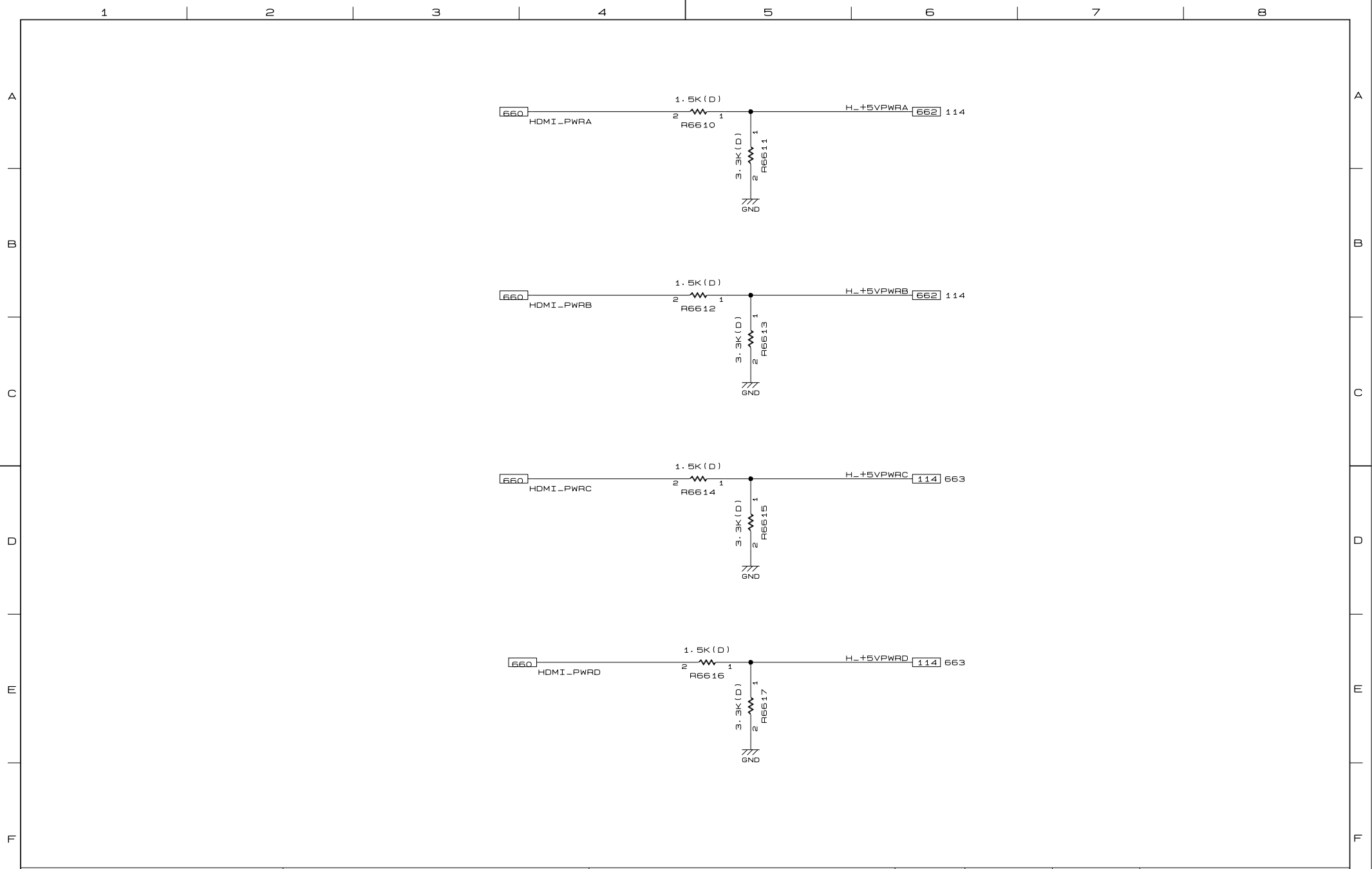
PAGE NO.
51

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY Noguchi/Kuwata	TITLE MAIN UNIT	FUNCTION HDMI CONNECTOR	SH. NO. 660	PAGE NO. 52	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY

Kuwata

TITLE

MAIN UNIT

FUNCTION

HDMI 5VPWR

SH. NO.

661

PAGE NO.

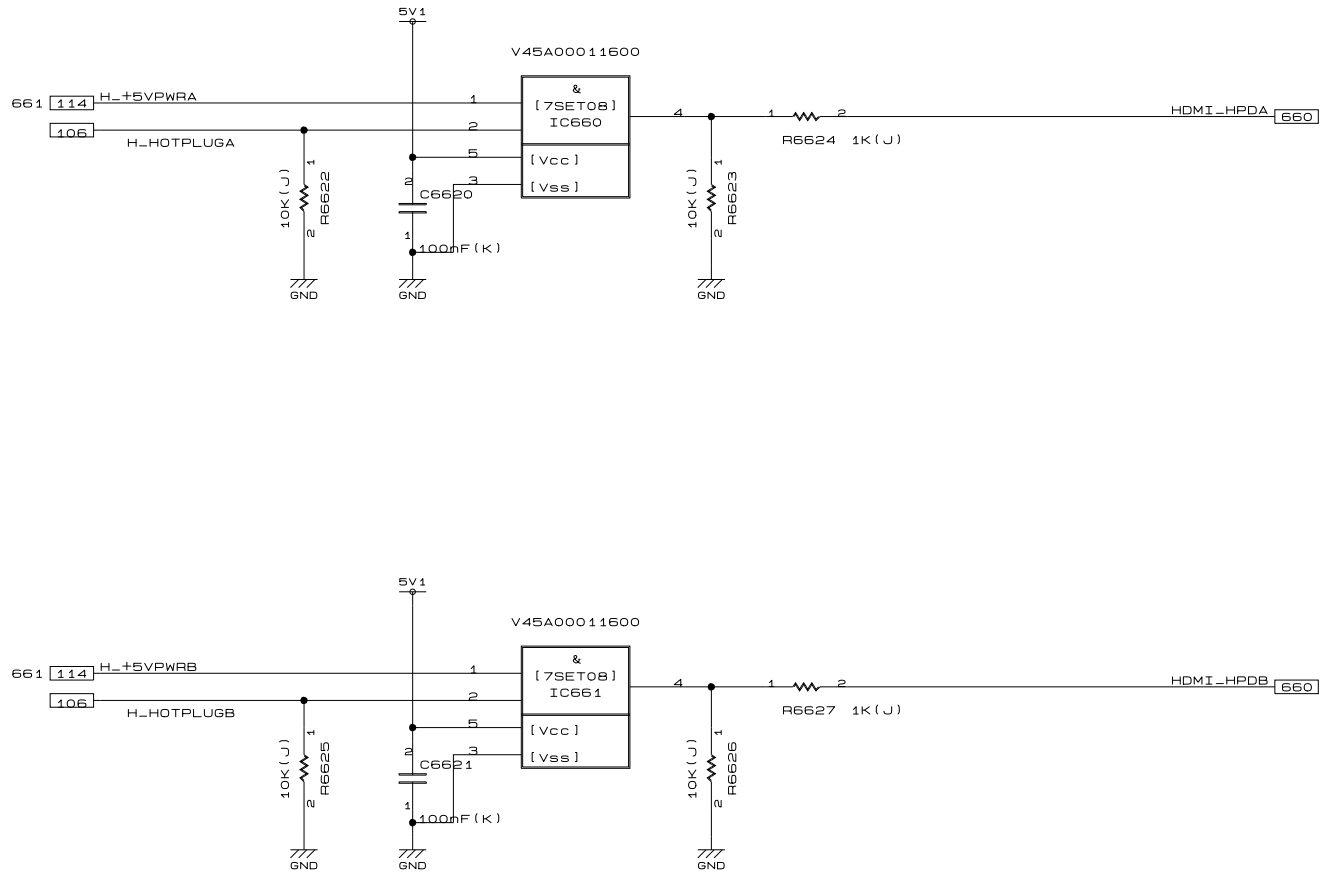
53

REV. MARK

MP

DRAWING. NO.

PE1000ZA



DESIGNED BY

Kuwata

TITLE

MAIN UNIT

FUNCTION

HDMI HPD (1)

SH. NO.

662

PAGE NO.

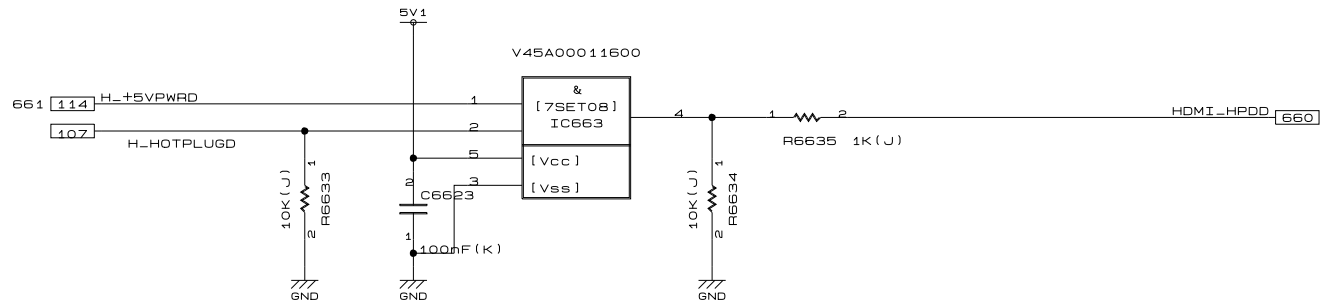
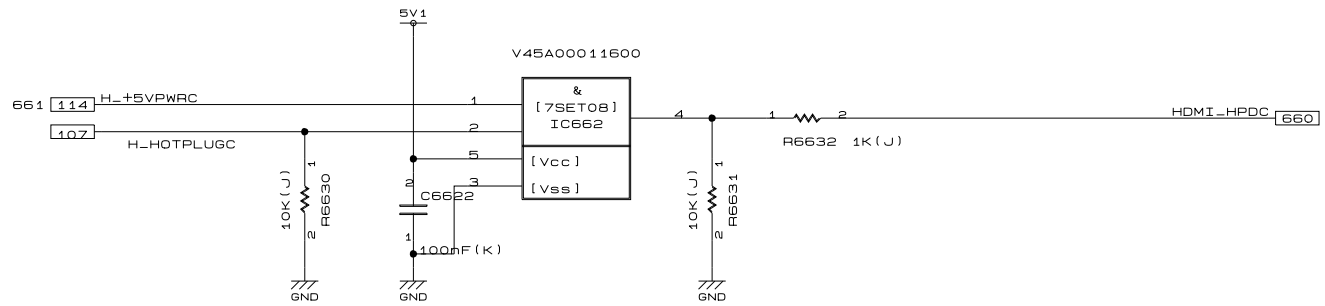
54

REV. MARK

MP

DRAWING. NO.

PE1000ZA



DESIGNED BY

Kuwata

TITLE

MAIN UNIT

FUNCTION

HDMI HPD(2)

SH. NO.

663

PAGE NO.

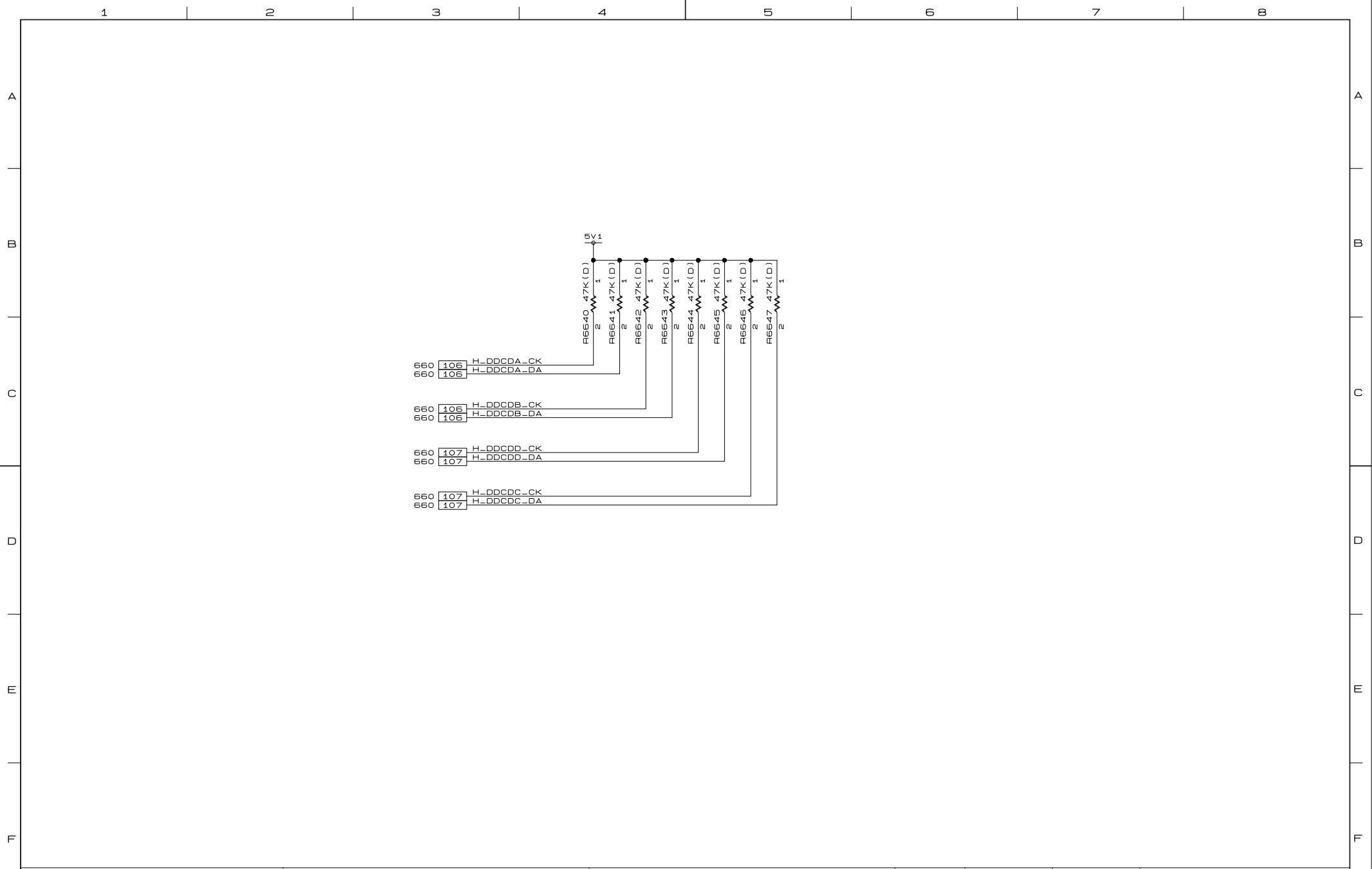
55

REV. MARK

MP

DRAWING. NO.

PE1000ZA



DESIGNED BY
Kuwata

TITLE
MAIN UNIT

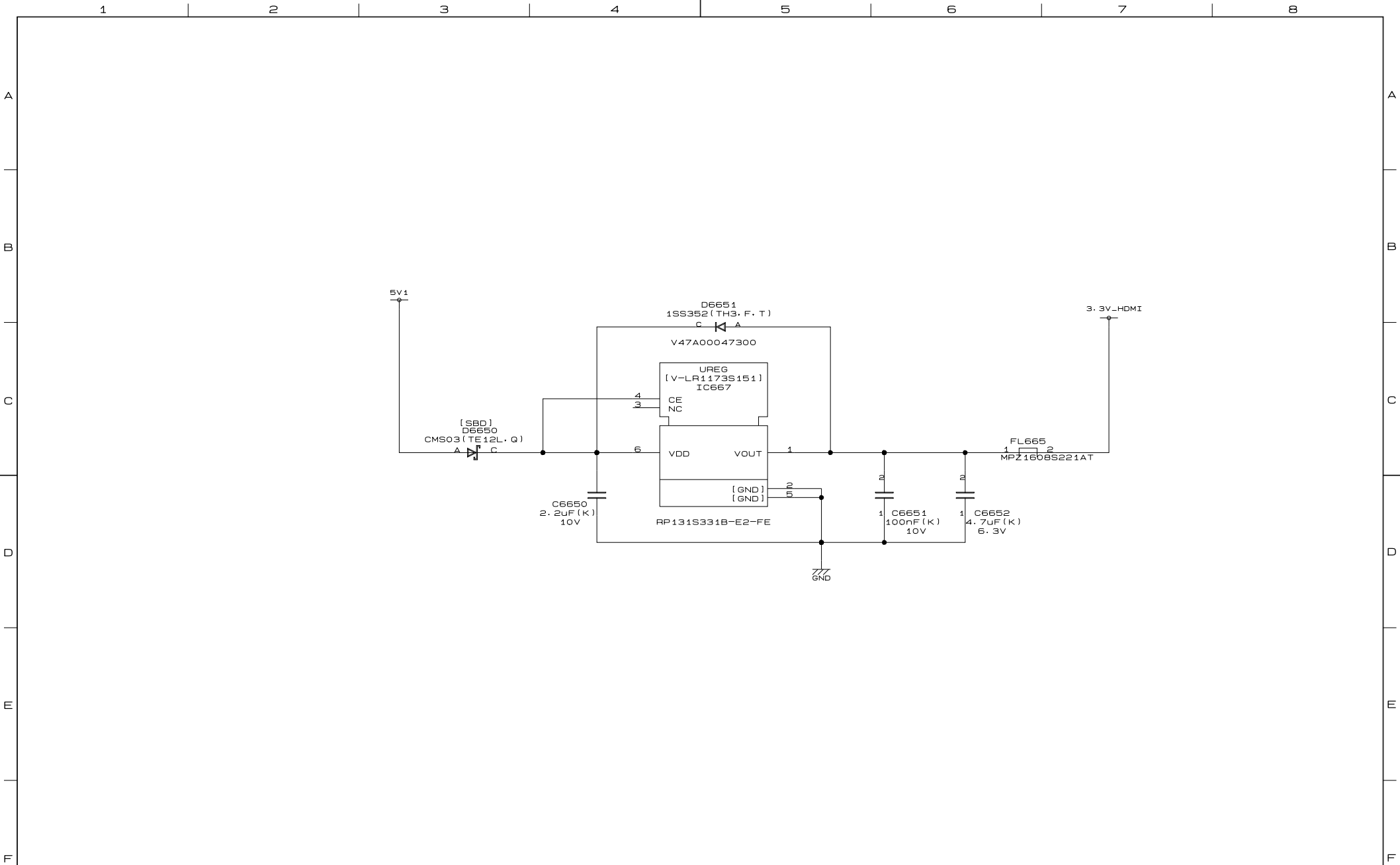
FUNCTION
HDMI DDC

SH. NO.
664

PAGE NO.
56

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY

Kuwata

TITLE

POWER TV2 [MAIN]

FUNCTION

3.3V_HDMI

SH. NO.

665

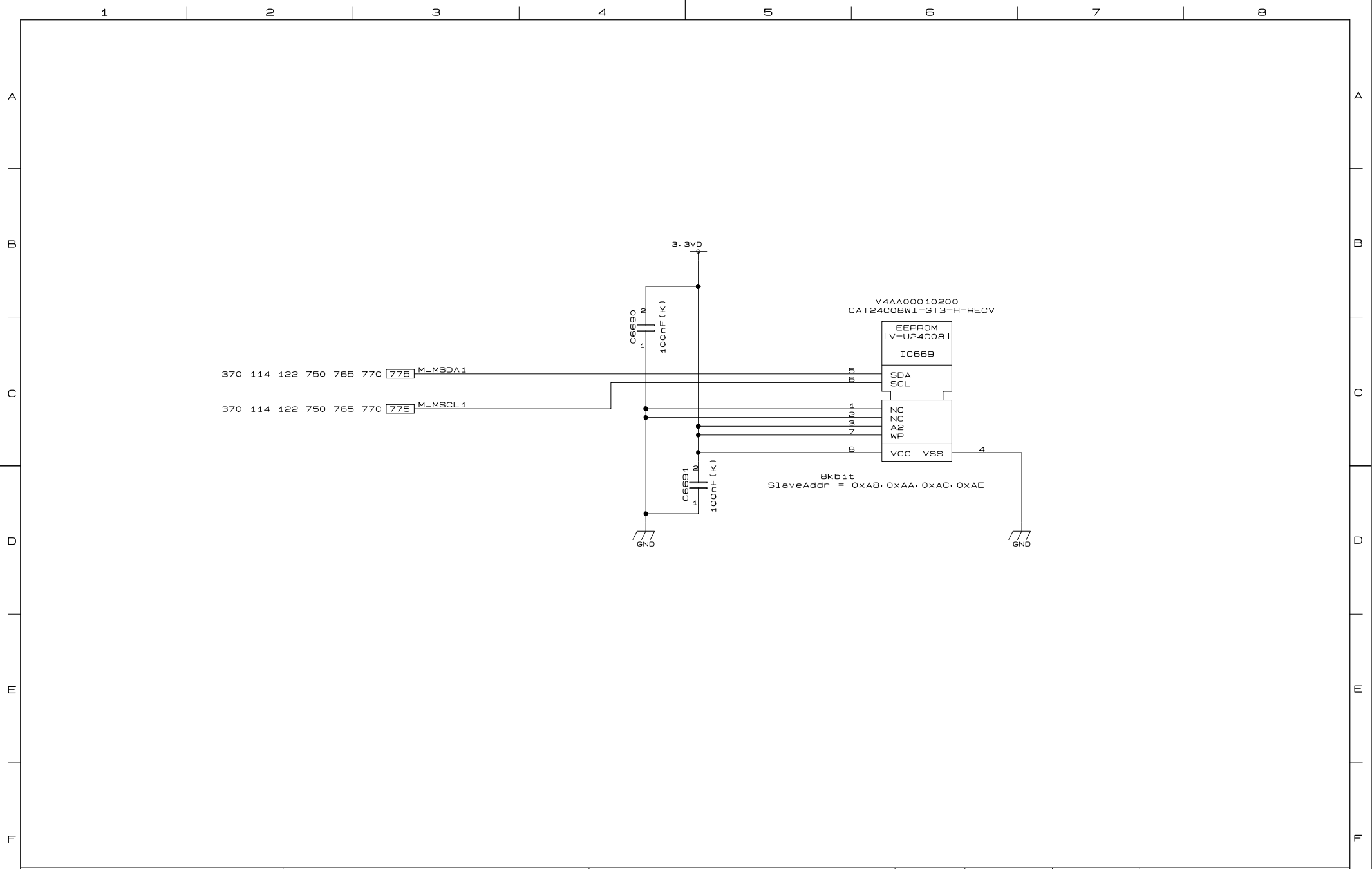
PAGE NO.

57

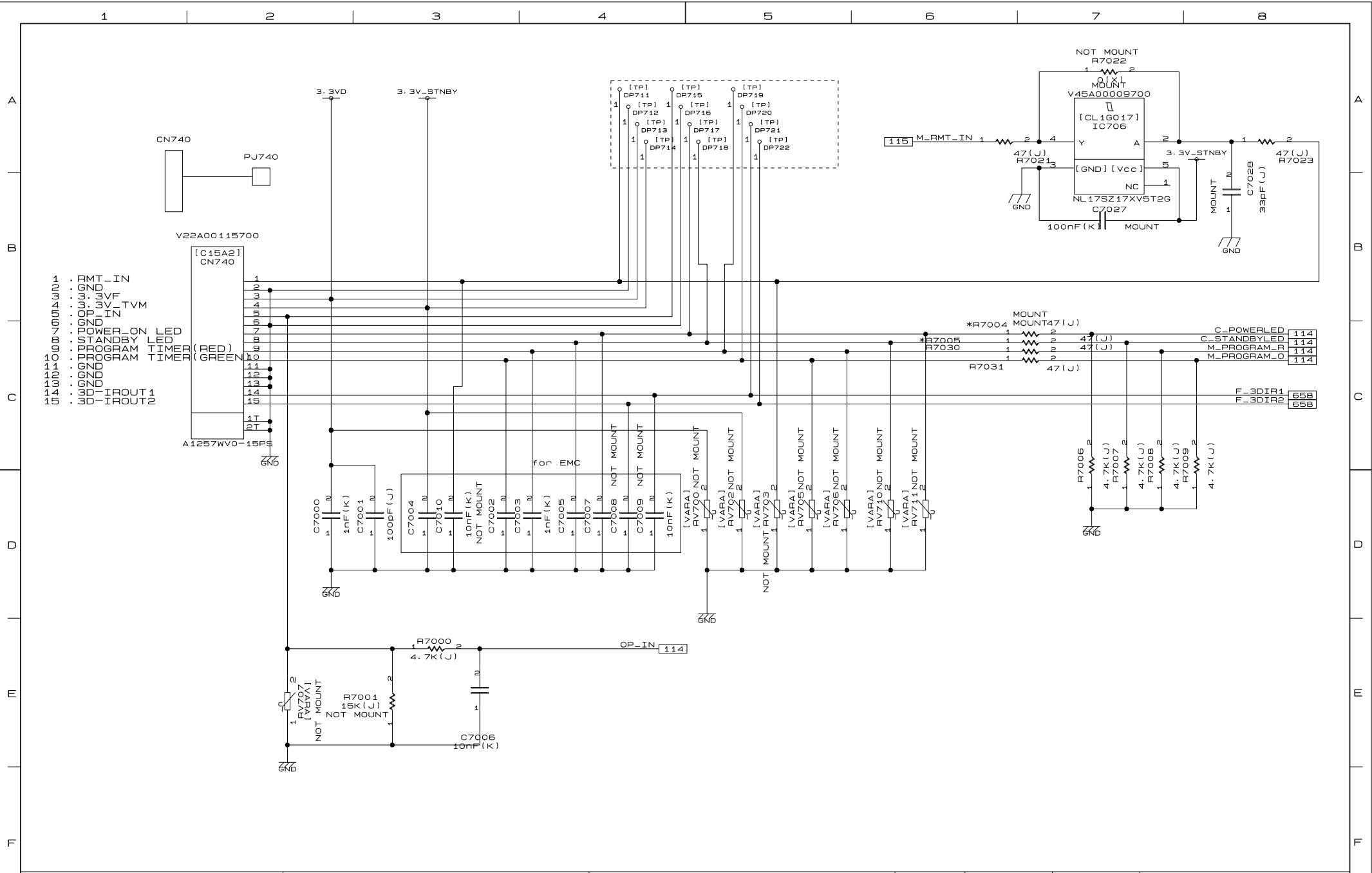
REV. MARK

MP

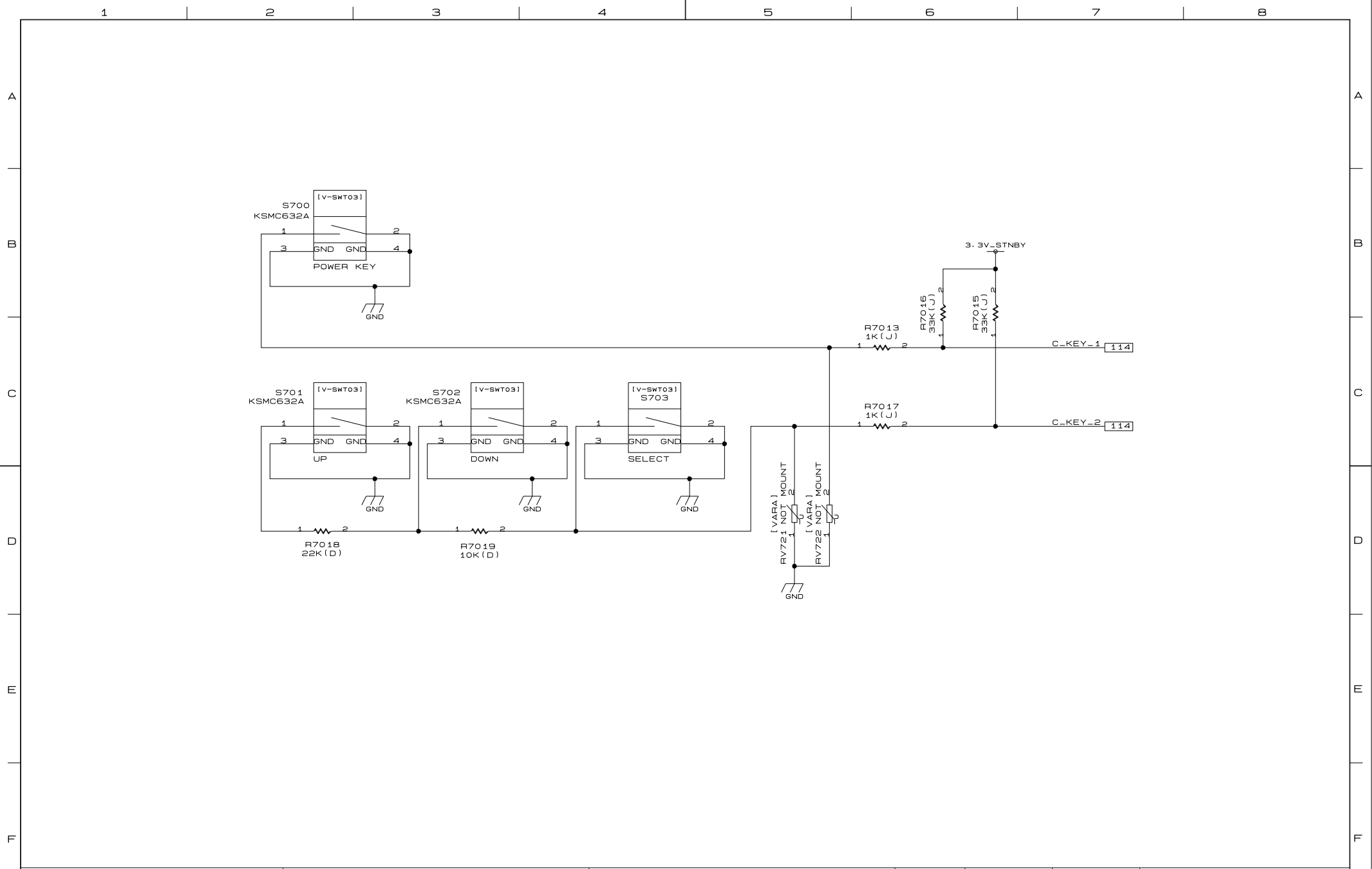
DRAWING. NO.



DESIGNED BY Noguchi/Kuwata	TITLE MAIN UNIT	FUNCTION HDCP KEY ROM	SH. NO. 669	PAGE NO. 58	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY IMURA	TITLE MAIN UNIT	FUNCTION LED/IR IF	SH. NO. 700	PAGE NO. 59	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY
IMURA

TITLE
MAIN UNIT

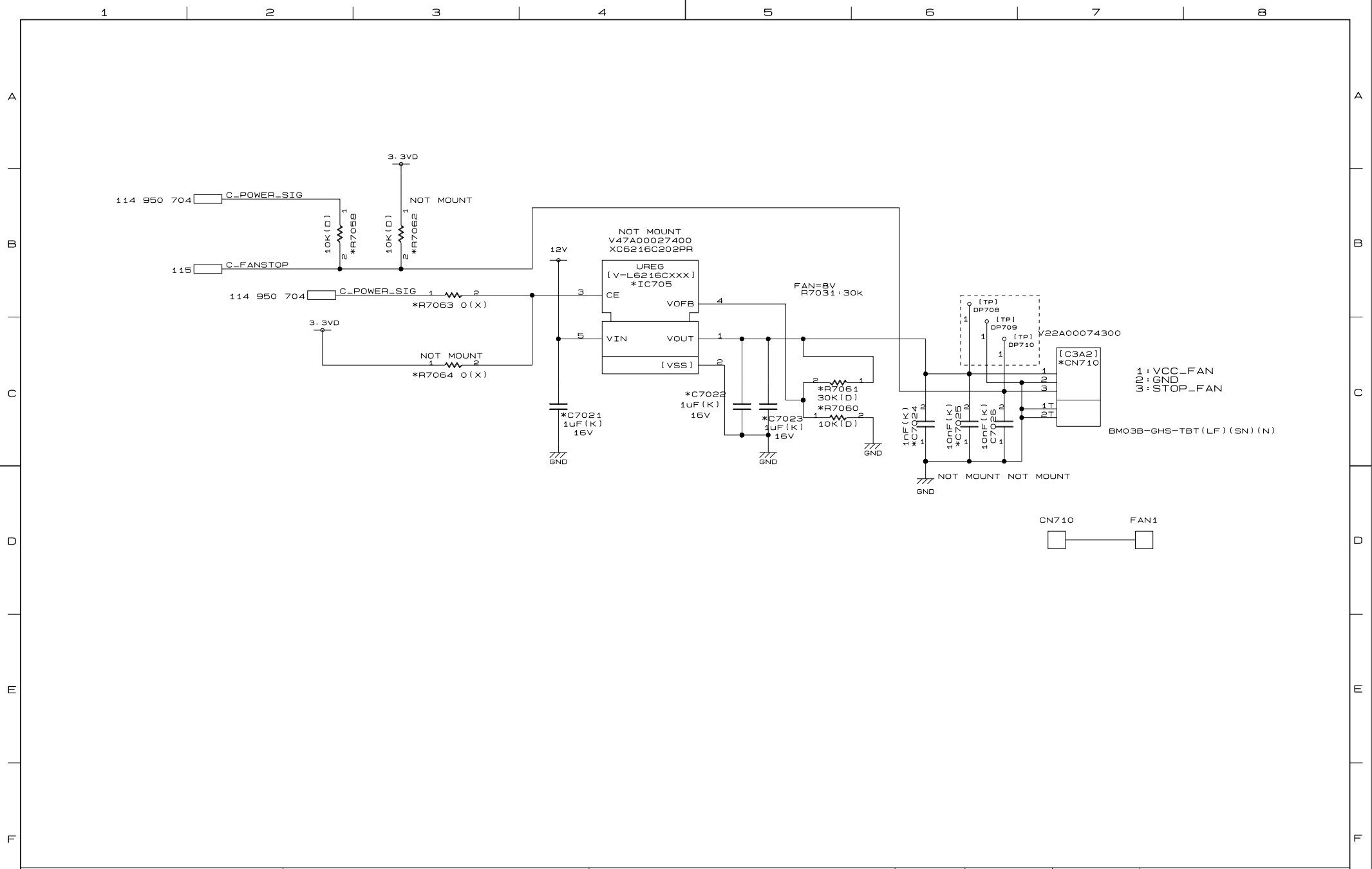
FUNCTION
5V LED/KEY

SH. NO.
701

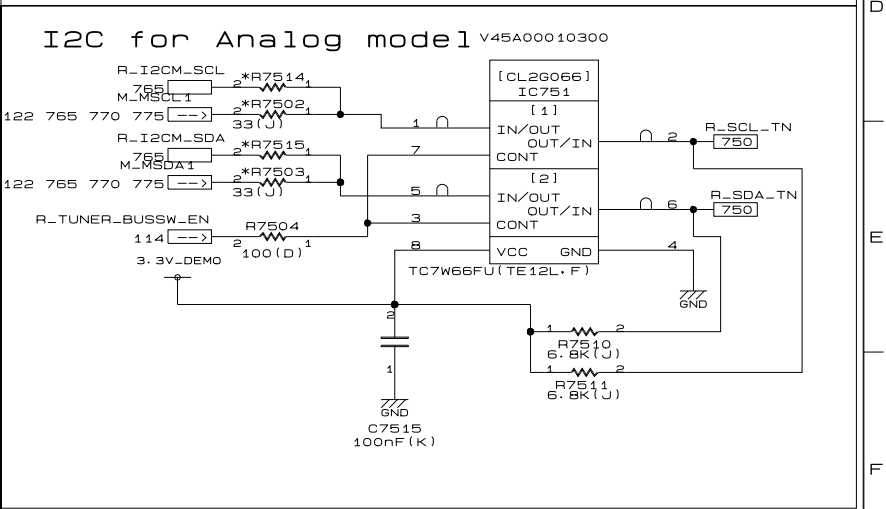
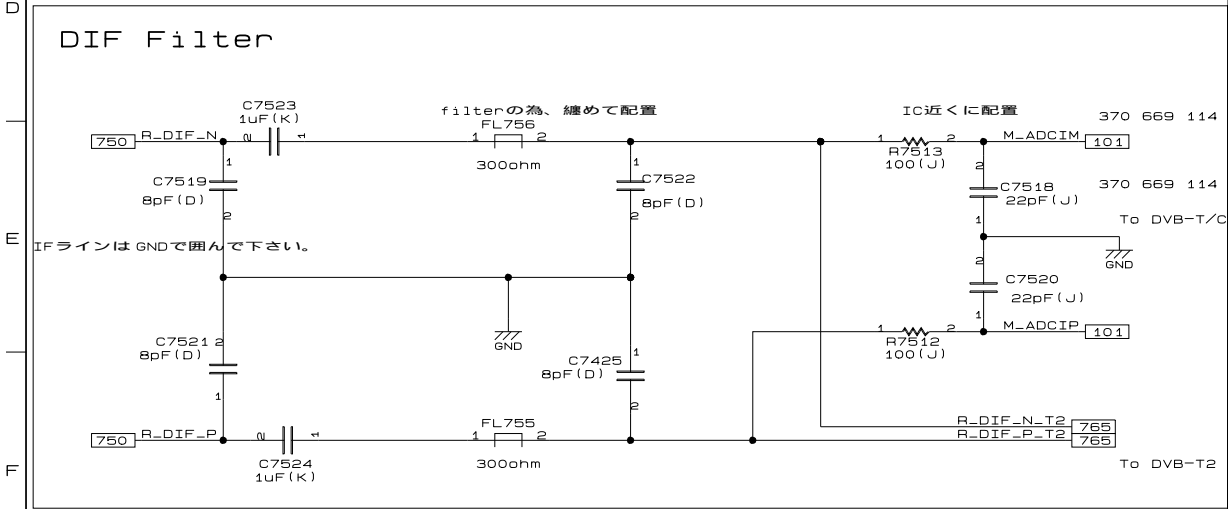
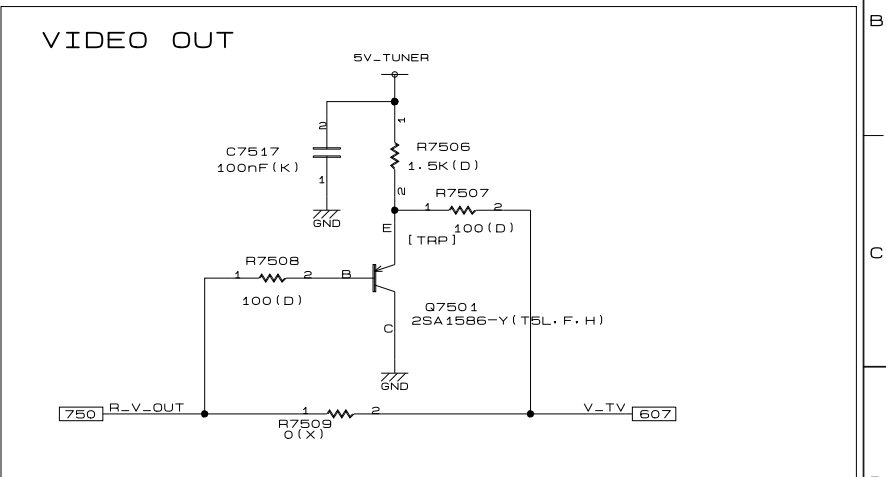
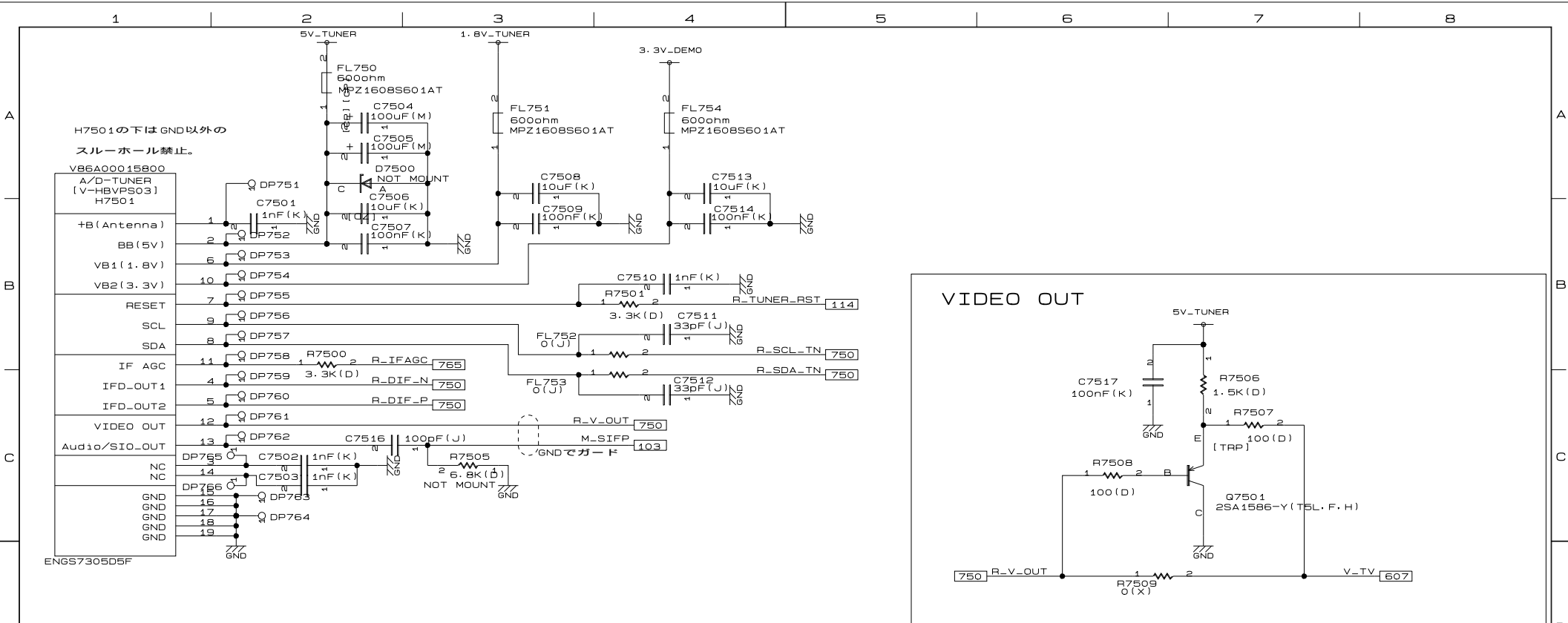
PAGE NO.
60

REV. MARK
MP

DRAWING. NO.
PE1000ZA

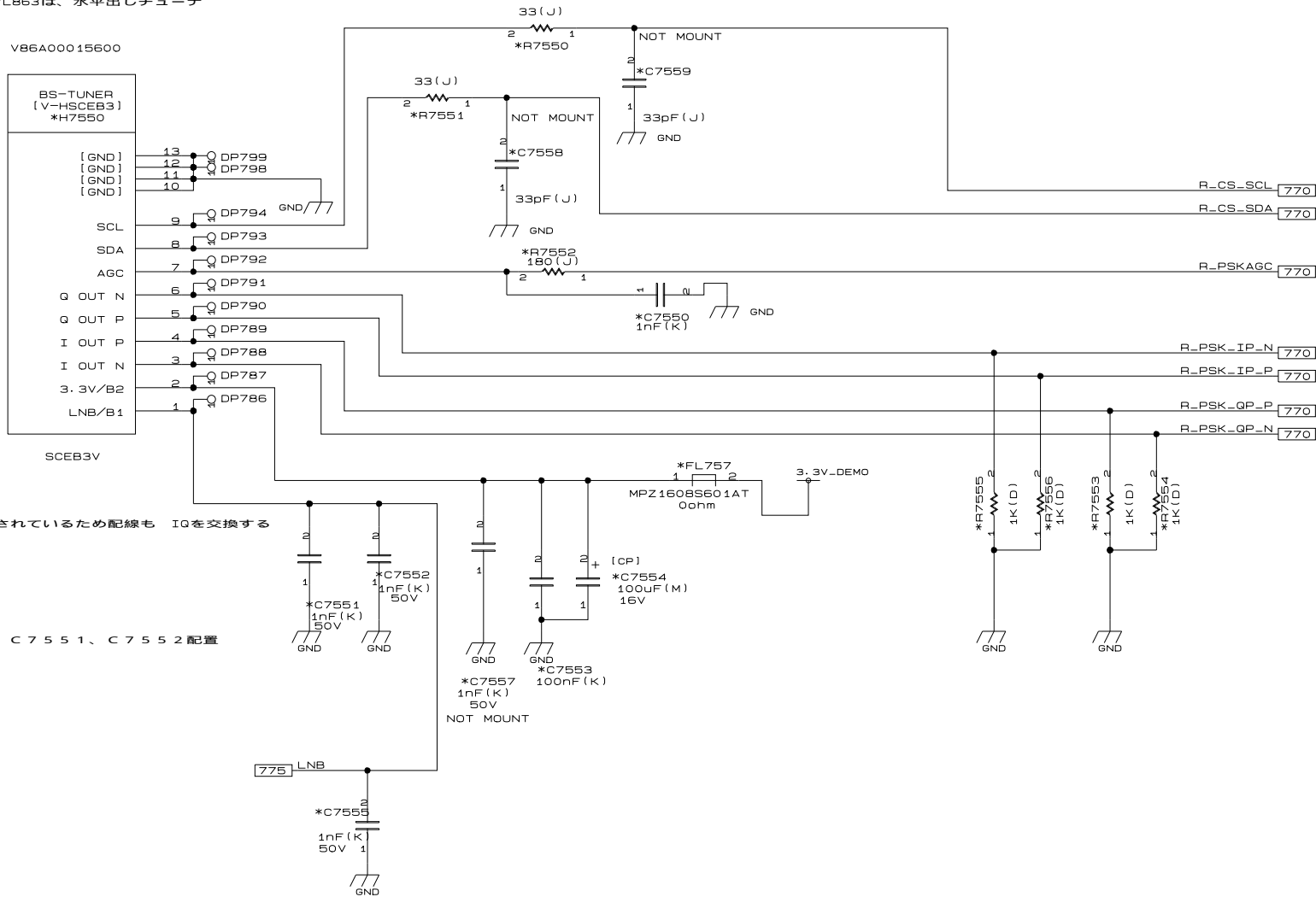


DESIGNED BY IMURA	TITLE MAIN UNIT	FUNCTION TVM FAN CONTROL	SH. NO. 704	PAGE NO. 61	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY R. IZUMI	TITLE MAIN UNIT	FUNCTION TUNER	SH. NO. 750	PAGE NO. 62	REV. MARK MP	DRAWING. NO. PE1000ZA
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SL863. 32-37UL863は、天面出しチューナ
VL863. WL863YL863は、水平出しチューナ



DESIGNED BY

R. IZUMI

TITLE

MAIN UNIT

FUNCTION

TUNER DVB-S/S2

SH. NO.

755

PAGE NO.

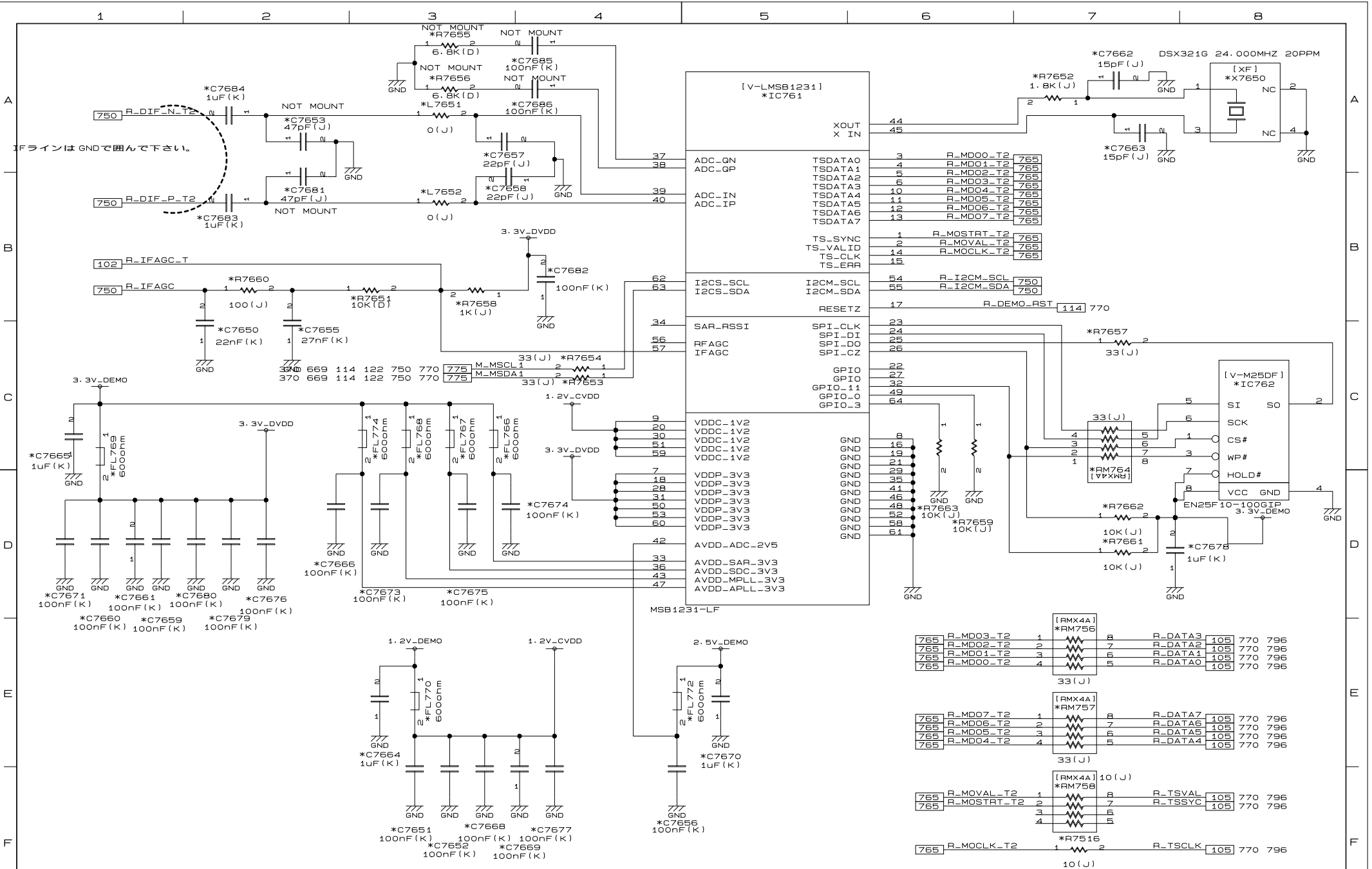
63

REV. MARK

MP

DRAWING. NO.

PE1000ZA



DESIGNED BY
R. IZUMI

TITLE
MAIN UNIT

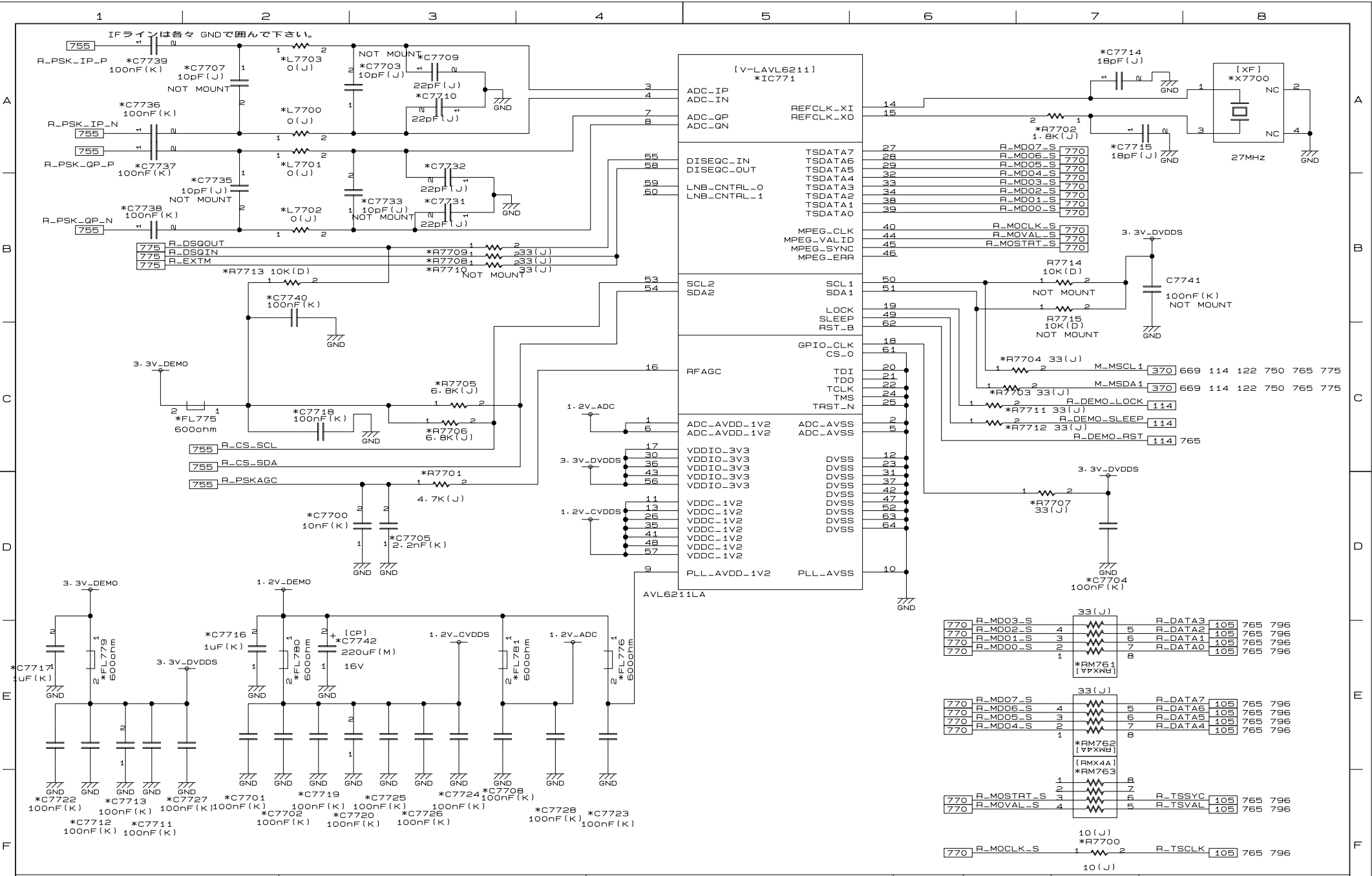
FUNCTION
DEMOD DVB-T2/T/C

SH. NO.
765

PAGE NO.
64

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY
R. IZUMI

TITLE
MAIN UNIT

FUNCTION
DEMOD DVB-S2/S

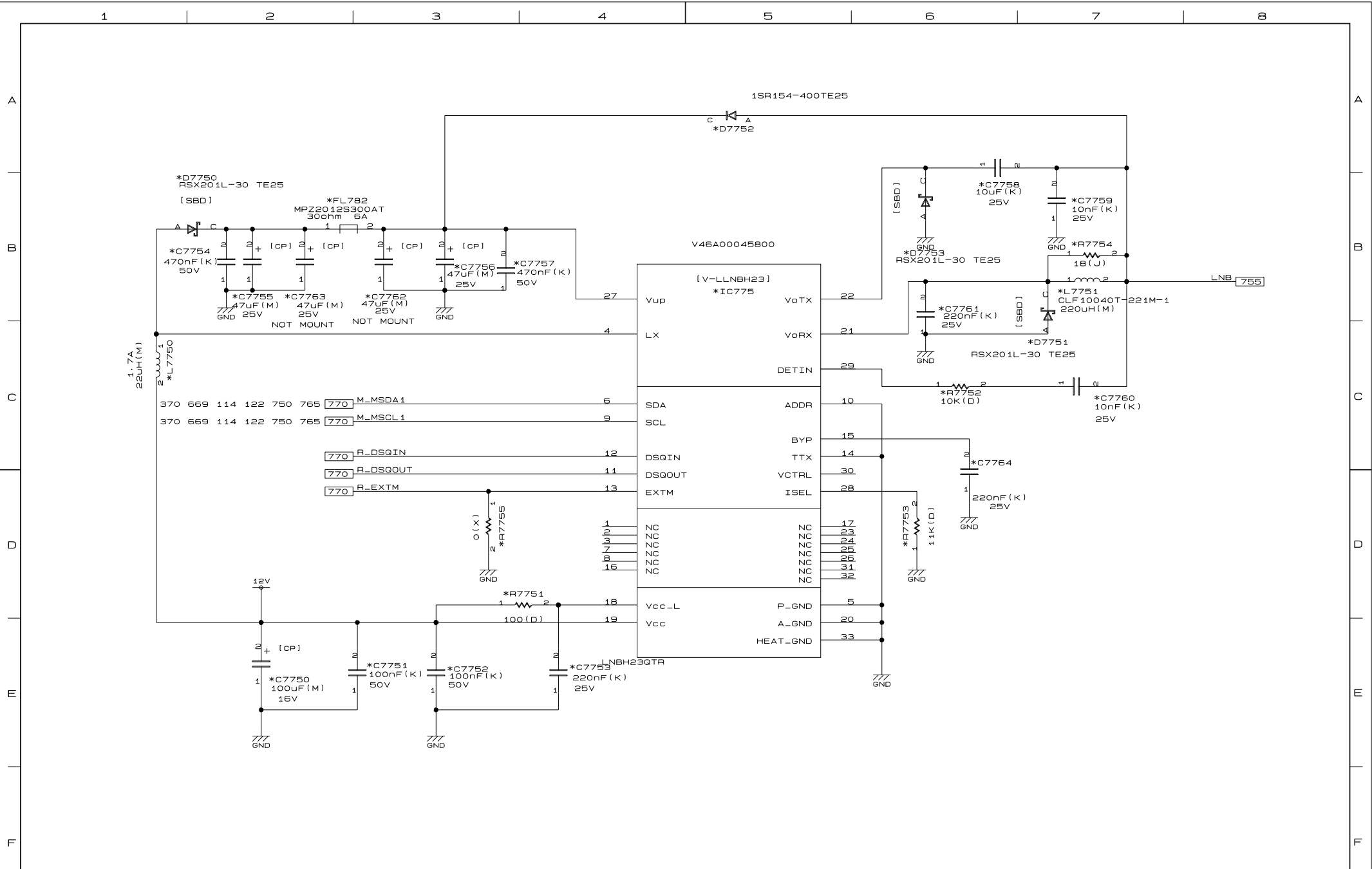
SH. NO.
770

PAGE NO.
65

REV. MARK
MP

DRAWING. NO.
PE1000ZA

2011.07.11 18:55



DESIGNED BY

R. IZUMI

TITLE

MAIN UNIT

FUNCTION

DisEqC, LNB POWER

SH. NO.

775

PAGE NO.

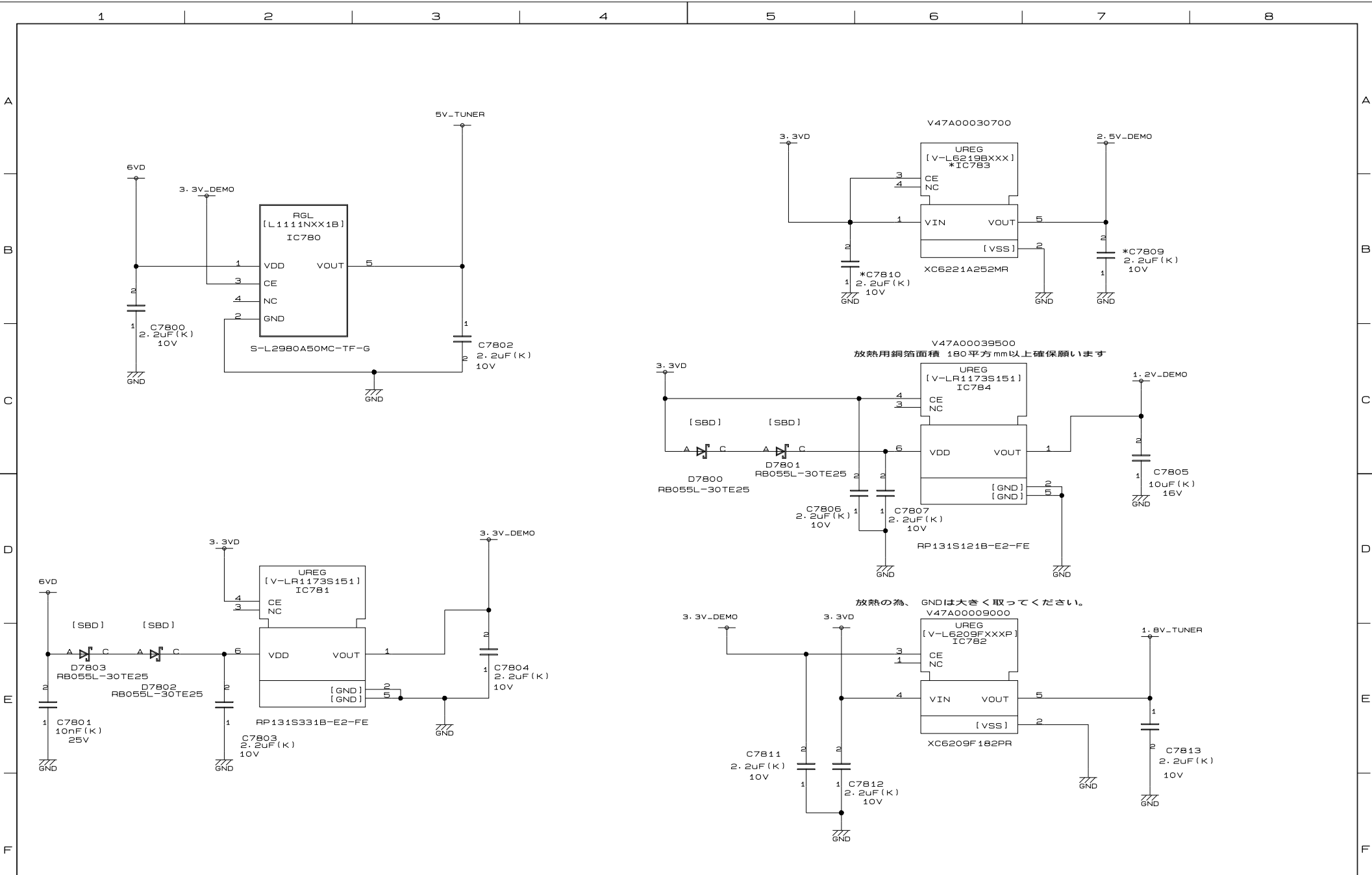
66

REV. MARK

MP

DRAWING. NO.

PE1000ZA



DESIGNED BY R. IZUMI	TITLE MAIN UNIT	FUNCTION TUNER POWER	SH. NO. 780	PAGE NO. 67	REV. MARK MP	DRAWING. NO. PE1000ZA
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1 2 3 4 5 6 7 8

A

B

C

D

E

F

A

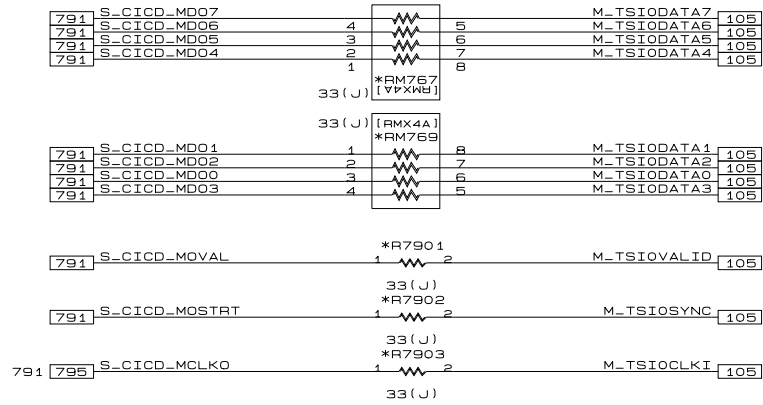
B

C

D

E

F

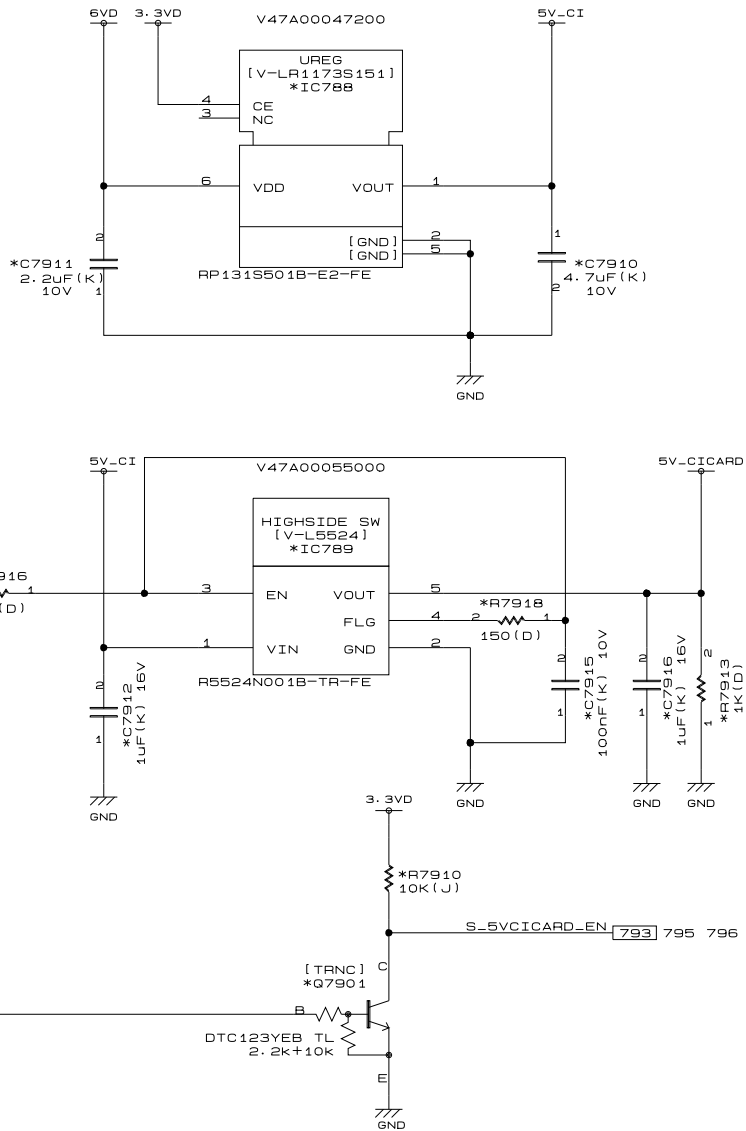


DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION CI TS RESISTOR	SH. NO. 790	PAGE NO. 68	REV. MARK MP	DRAWING. NO. PE1000ZA
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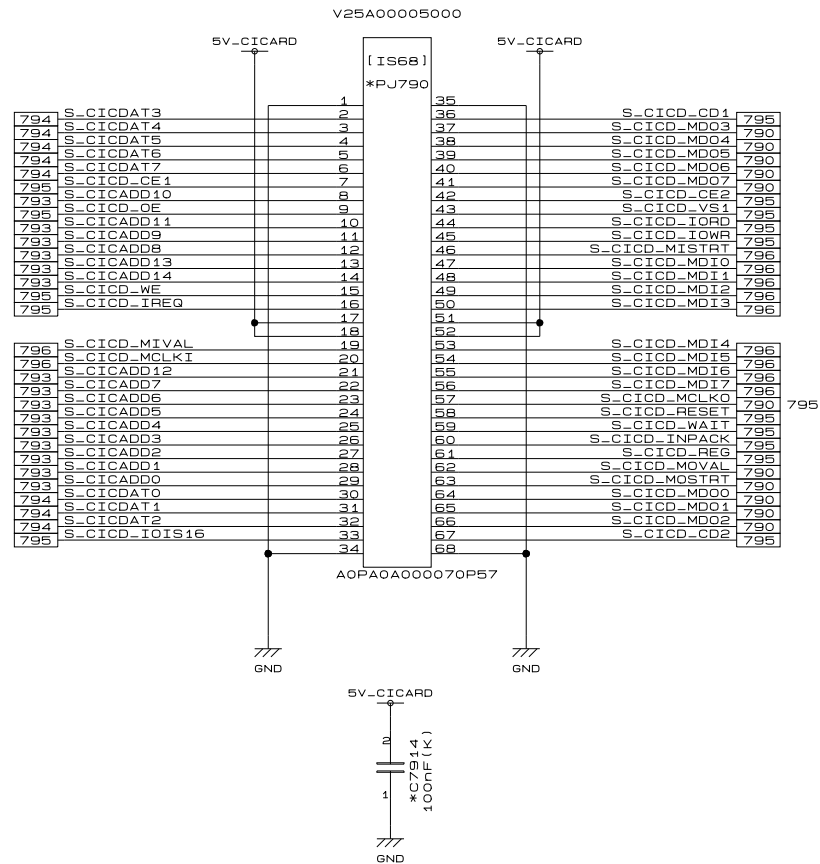
A
B
C
D
E
F

A
B
C
D
E
F

CI POWER



CI CONNECTOR



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

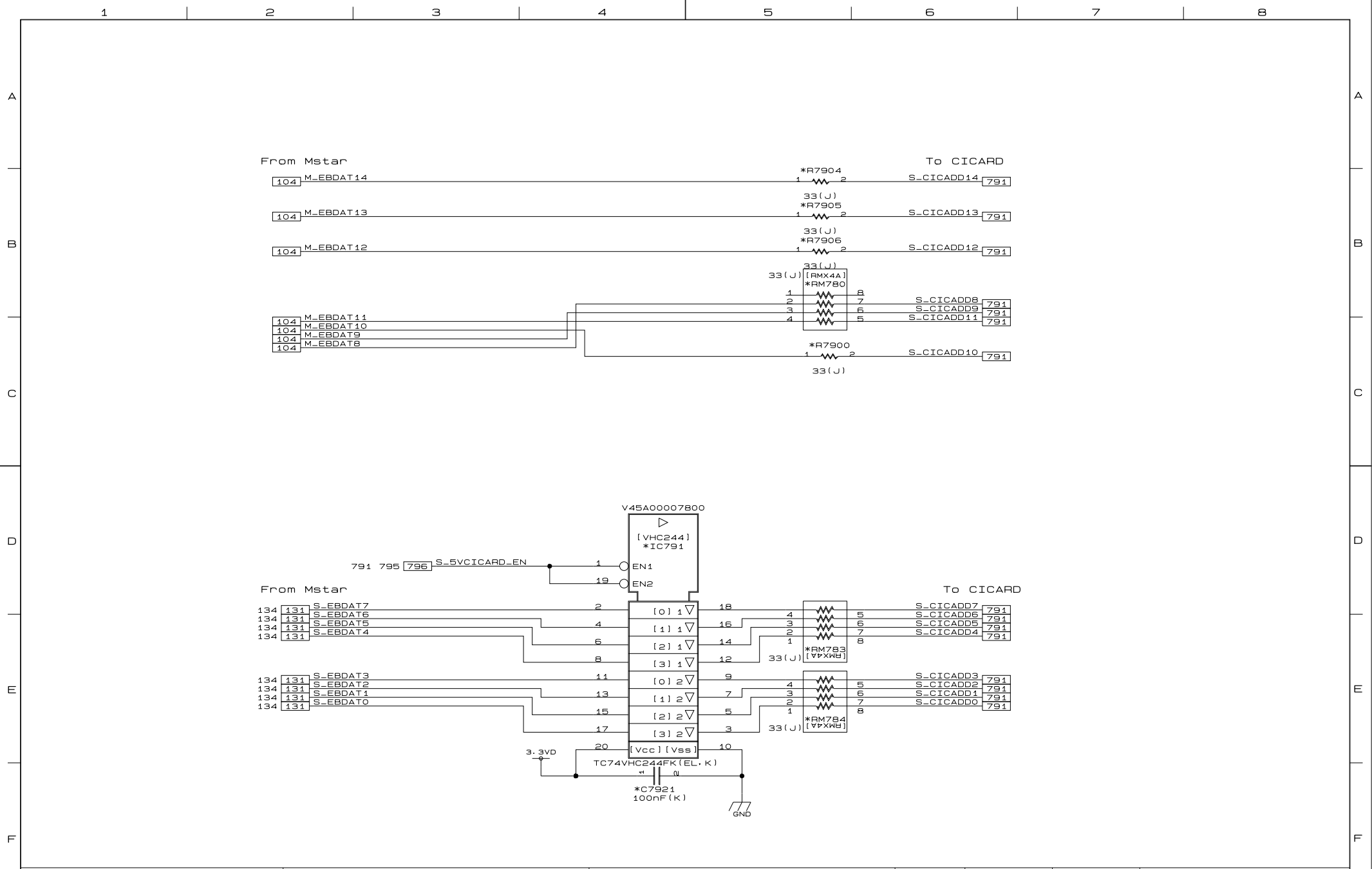
FUNCTION
CI POWER/CONNECTOR

SH. NO.
791

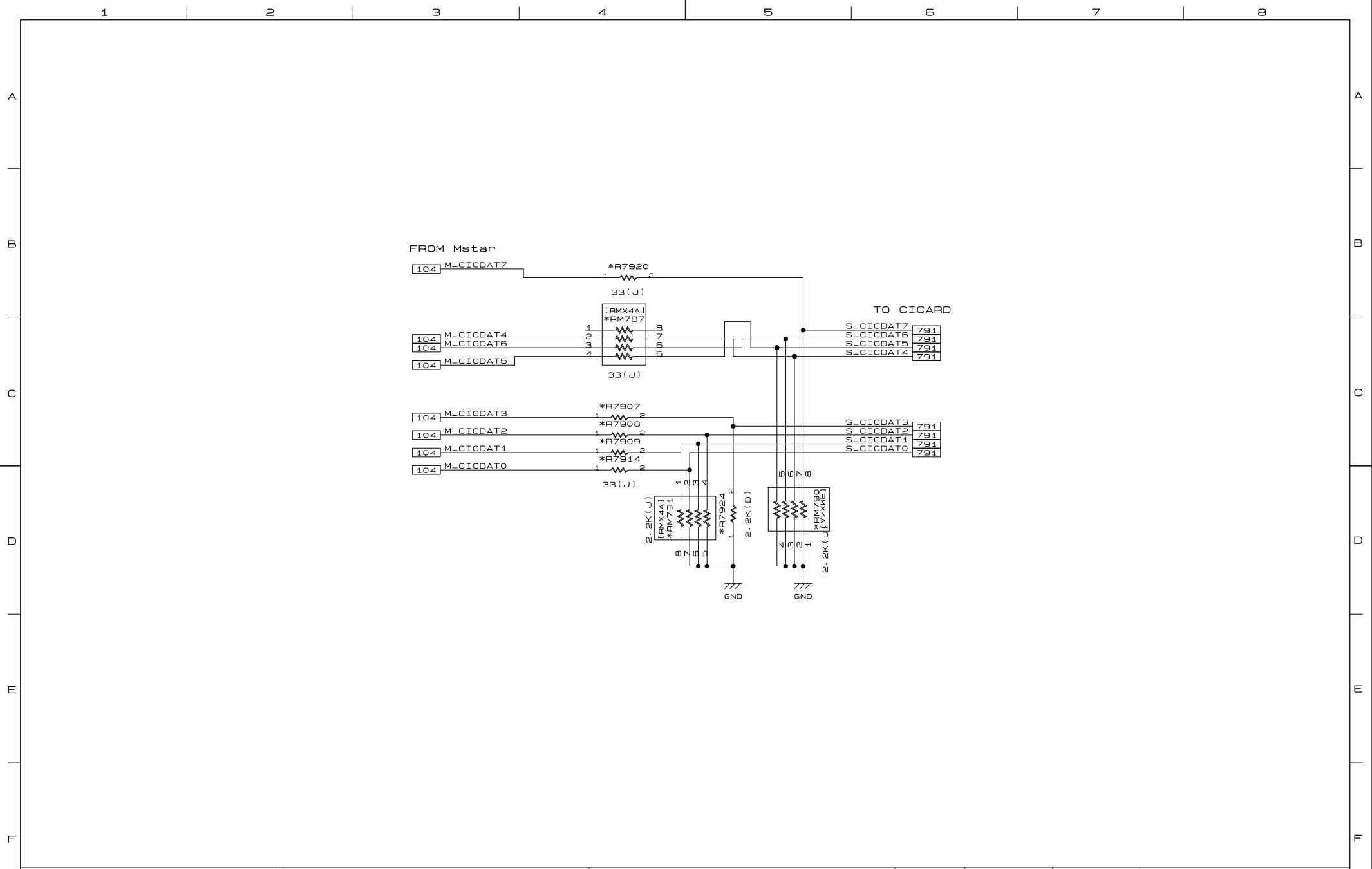
PAGE NO.
69

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION CI ADDRESS BUFFER	SH. NO. 793	PAGE NO. 70	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY
OSANAI

TITLE
MAIN UNIT

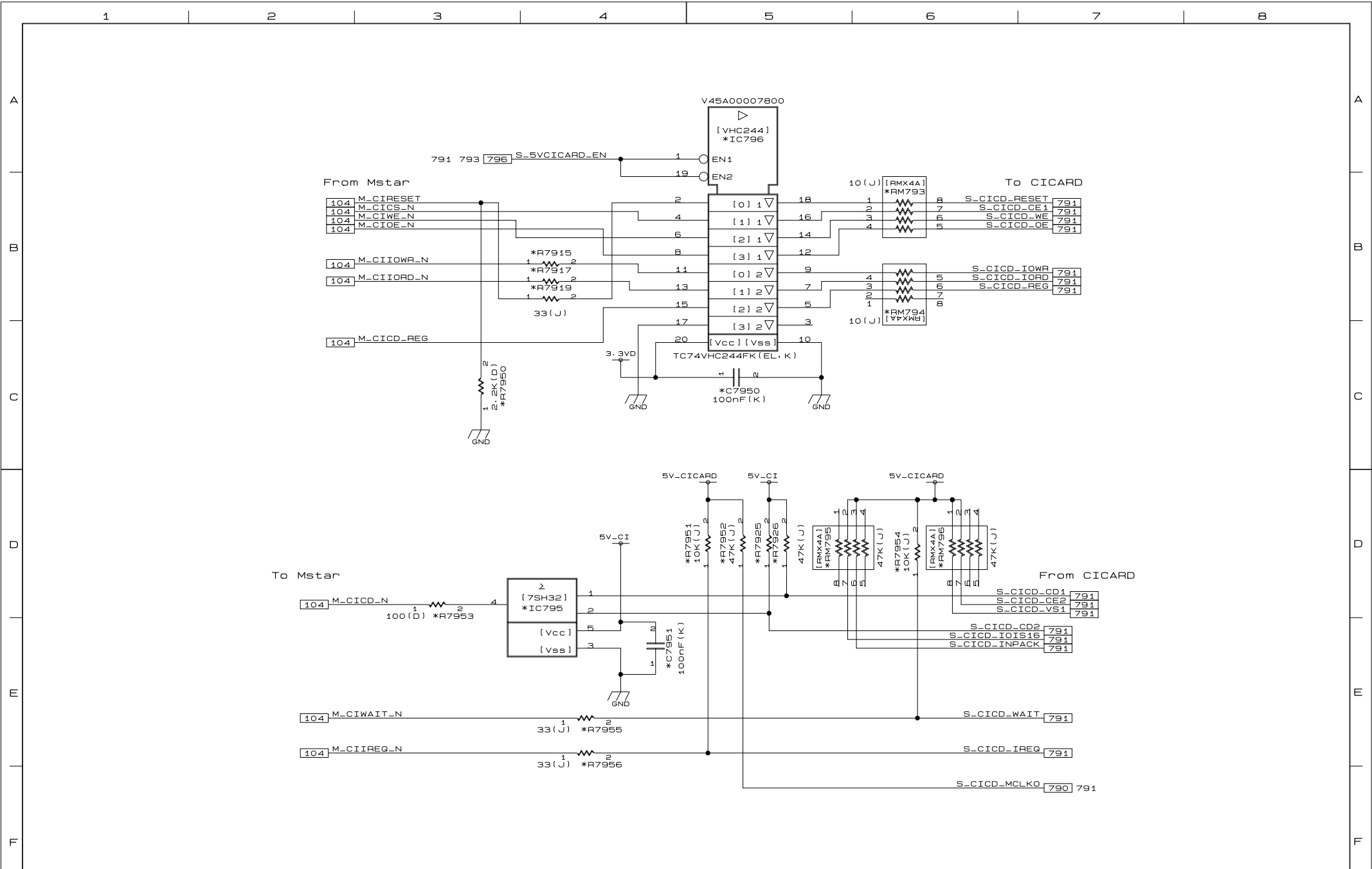
FUNCTION
CI DATA RESISTOR

SH. NO.
794

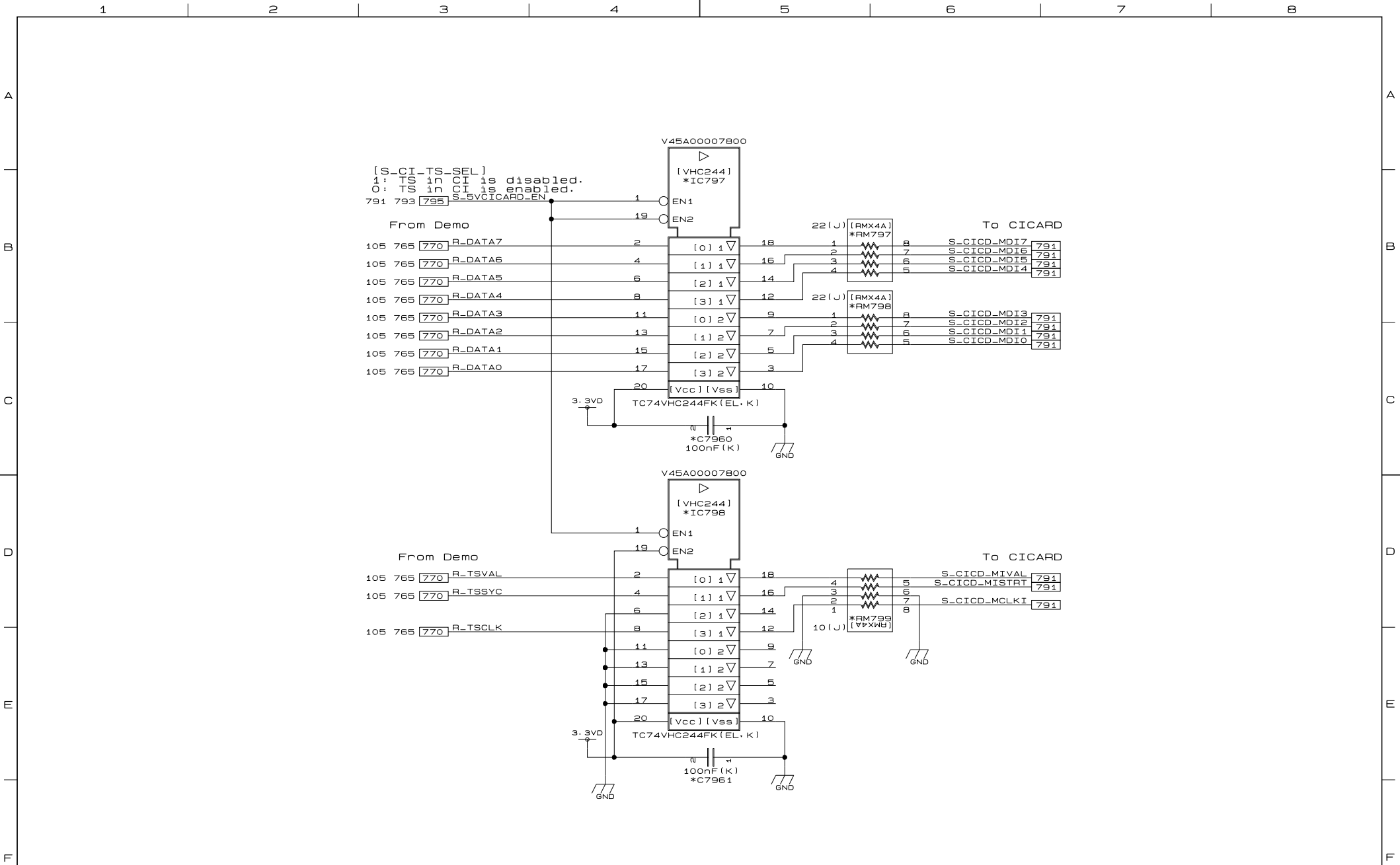
PAGE NO.
71

REV. MARK
MP

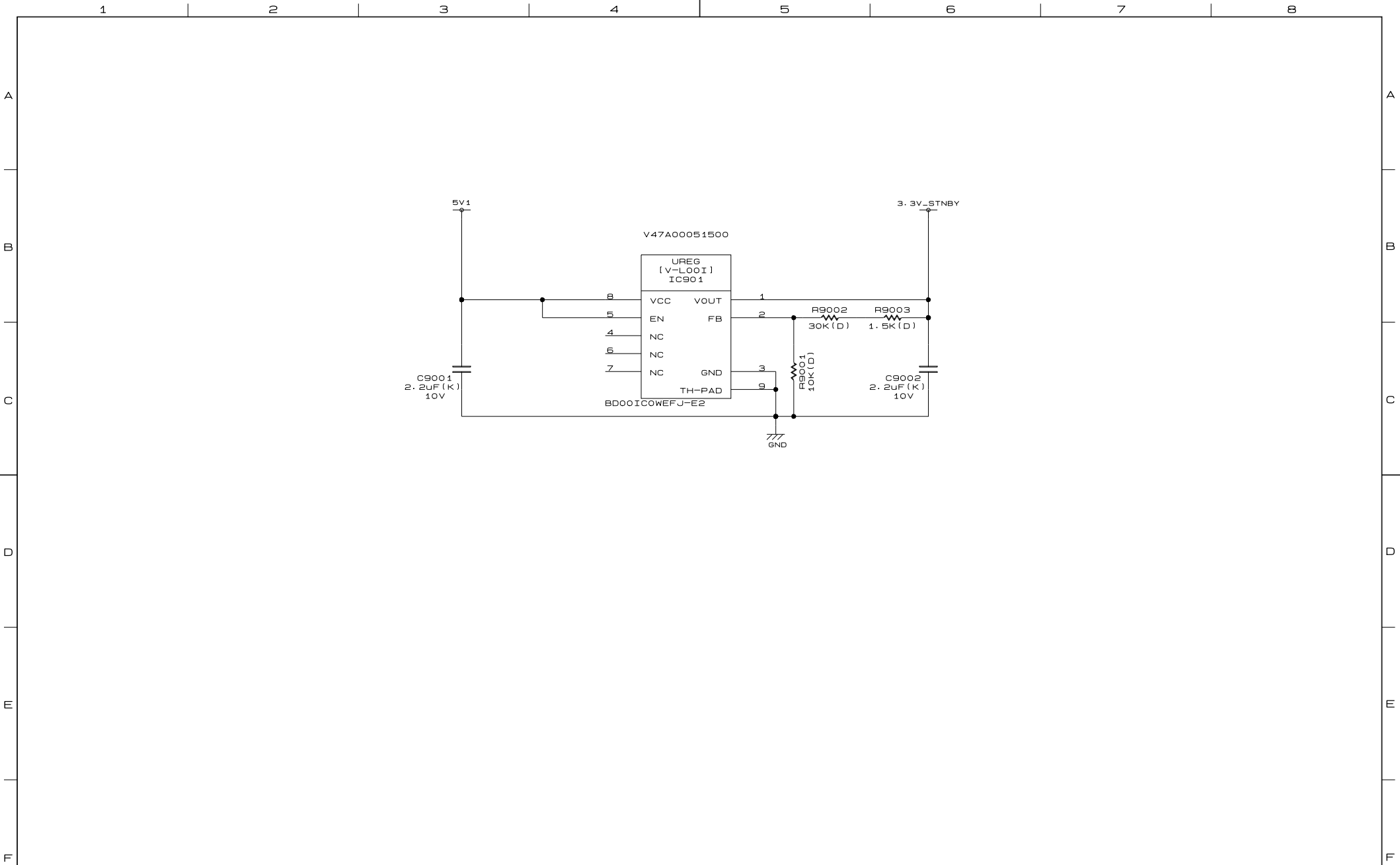
DRAWING. NO.
PE1000ZA



DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION CI CONTROL BUFFER	SH. NO. 795	PAGE NO. 72	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION CI TS OUTPUT BUFFER	SH. NO. 796	PAGE NO. 73	REV. MARK MP	DRAWING. NO. PE1000ZA
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DESIGNED BY
HASUMI

TITLE
MAIN UNIT

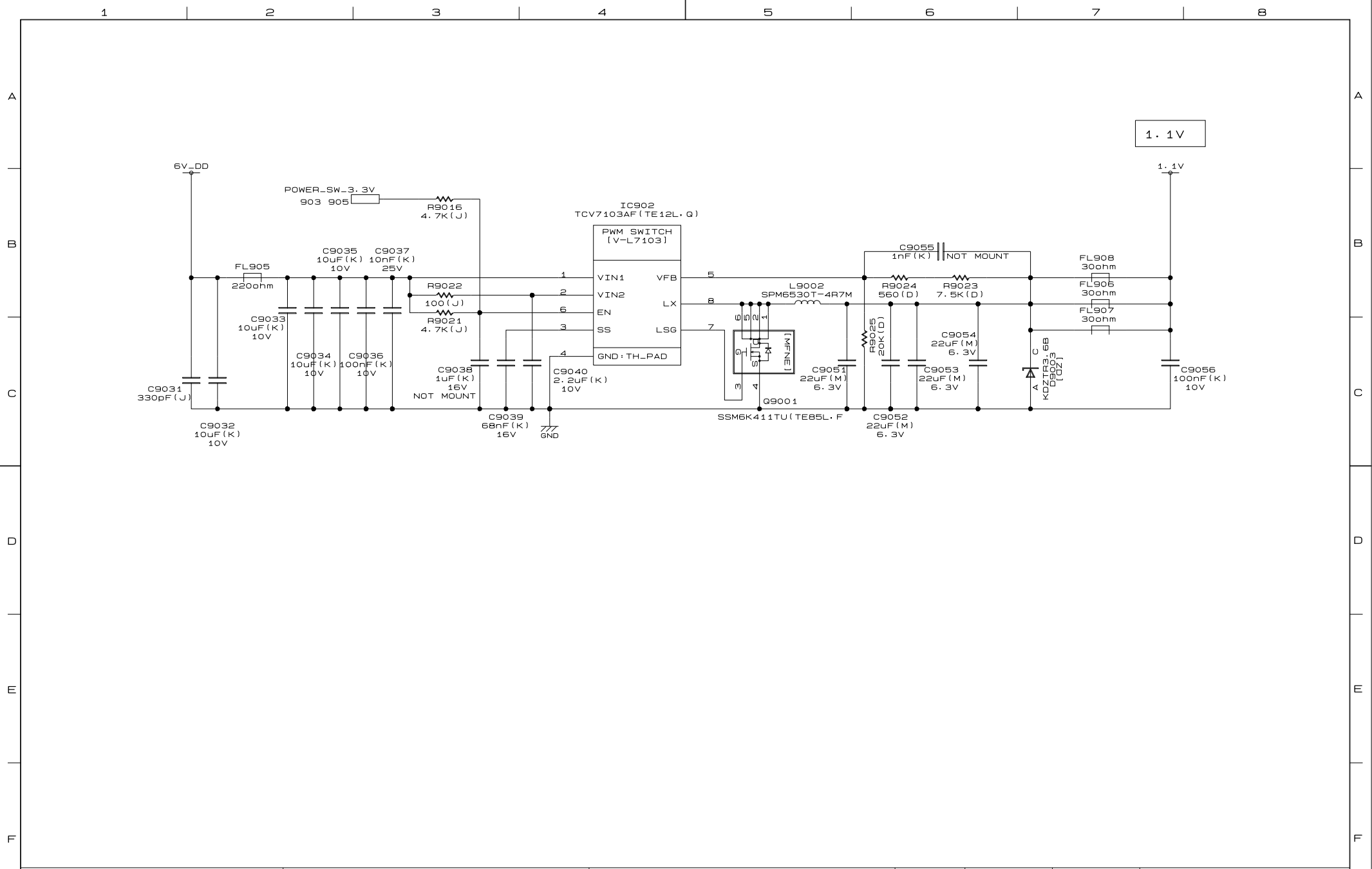
FUNCTION
3.3V STANDBY

SH. NO.
901

PAGE NO.
74

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY 2011/06/14
HASUMI

TITLE
MAIN UNIT

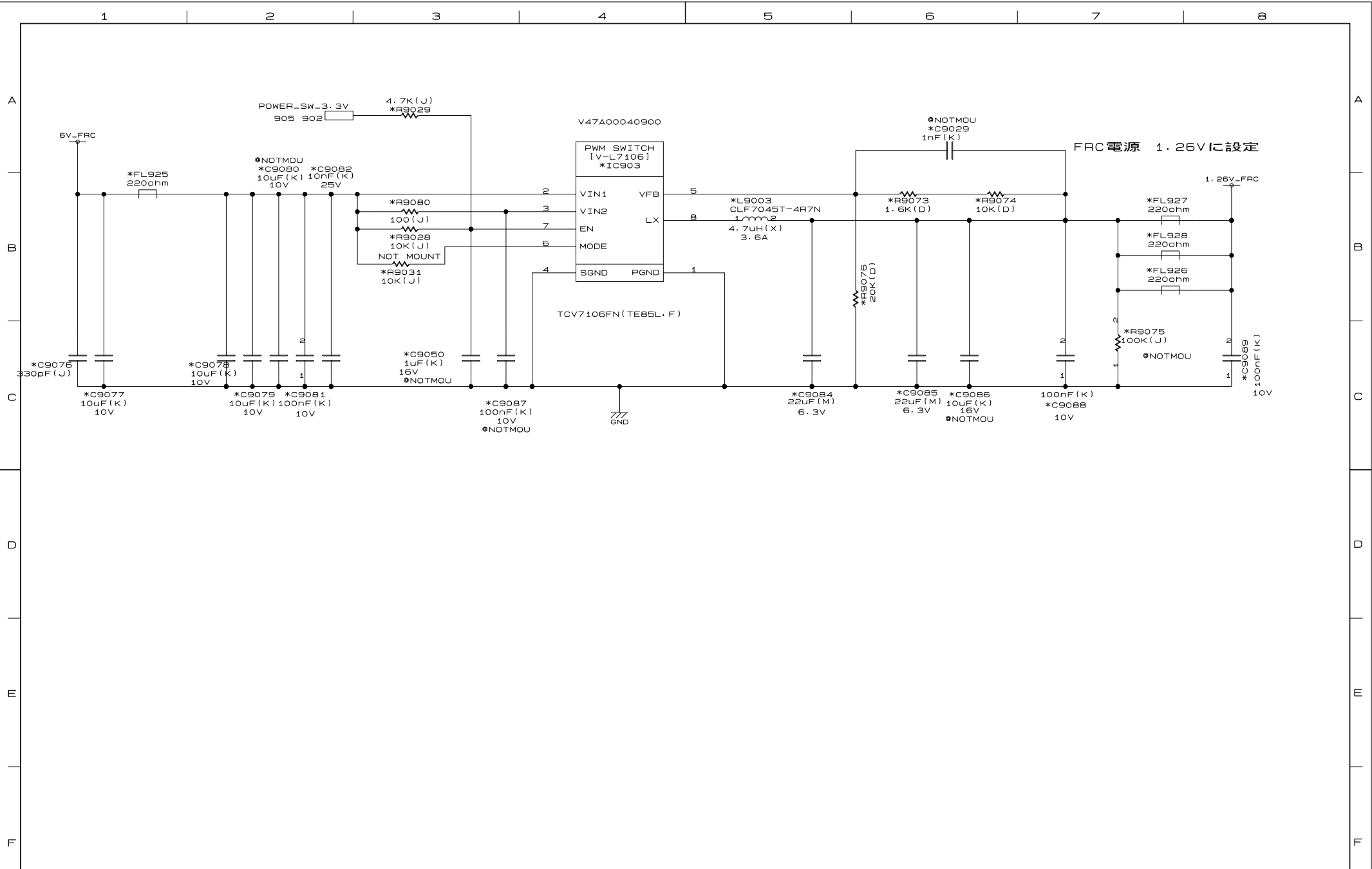
FUNCTION
POWER 1.1V

SH. NO.
902

PAGE NO.
75

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY 2011/06/14
HASUMI

TITLE
MAIN UNIT

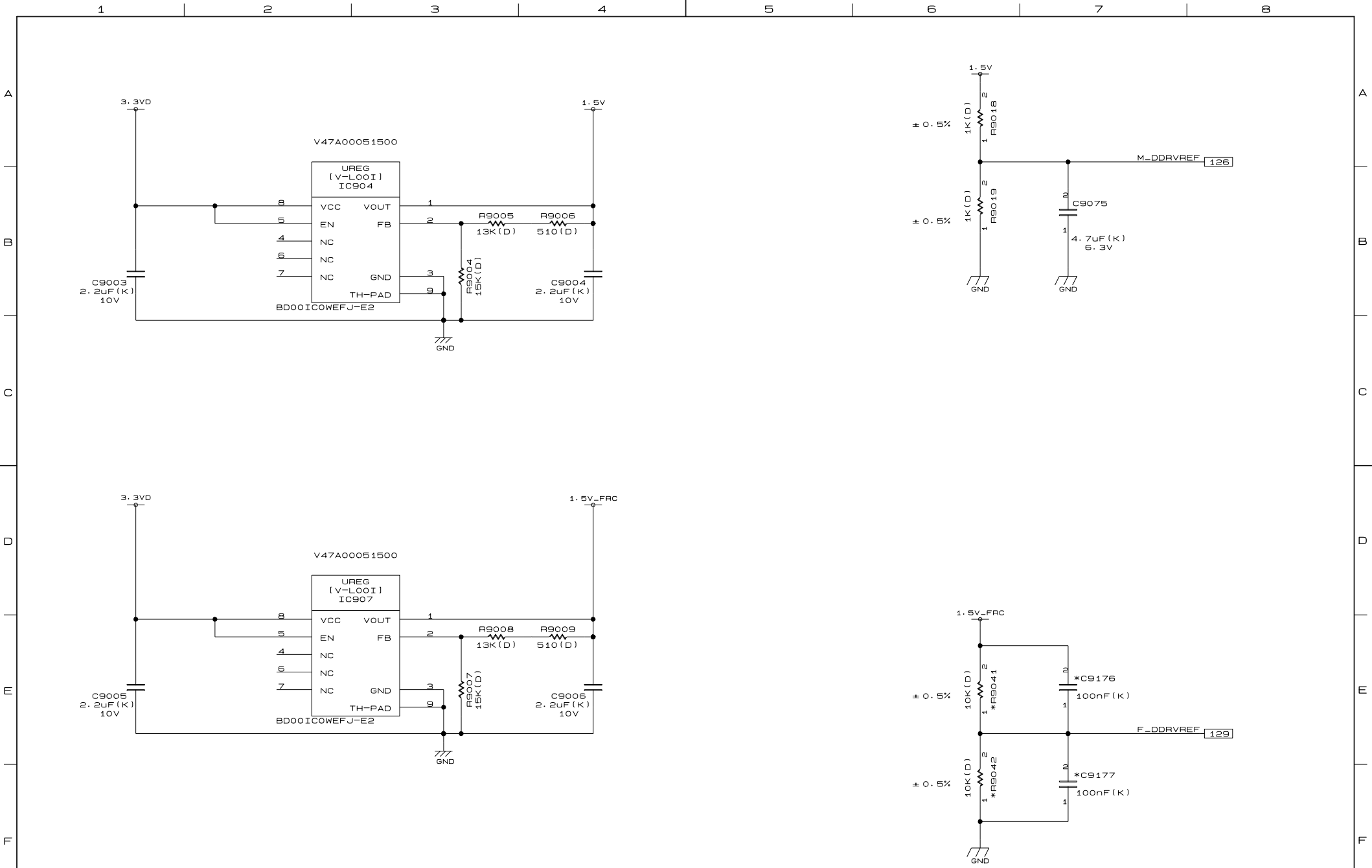
FUNCTION
FRC POWER 1.26V

SH. NO.
903

PAGE NO.
76

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY
HASUMI

TITLE
MAIN UNIT

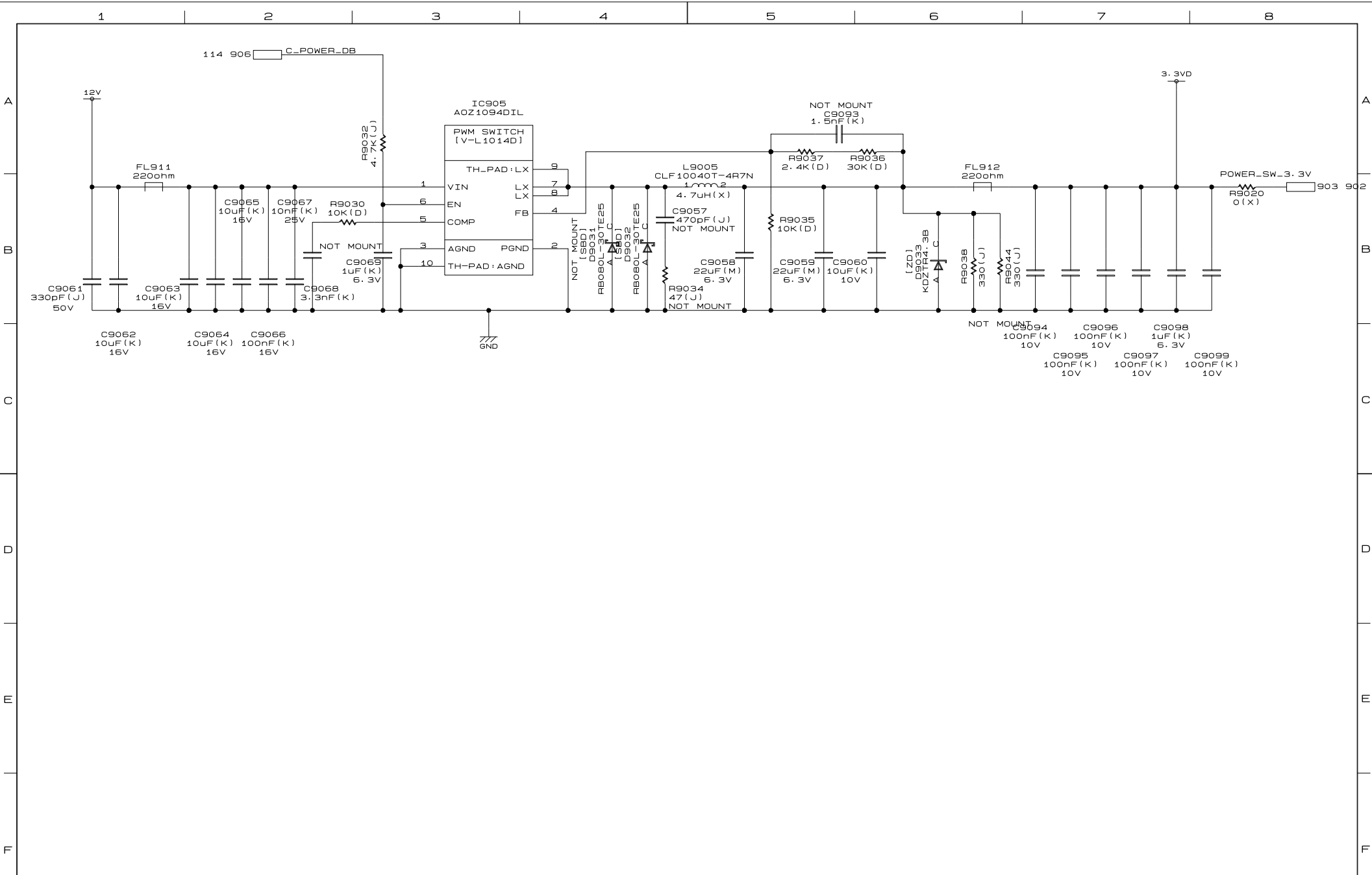
FUNCTION
FRC POWER 1.5V

SH. NO.
904

PAGE NO.
77

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY 2011/06/14
HASUMI

TITLE
MAIN UNIT

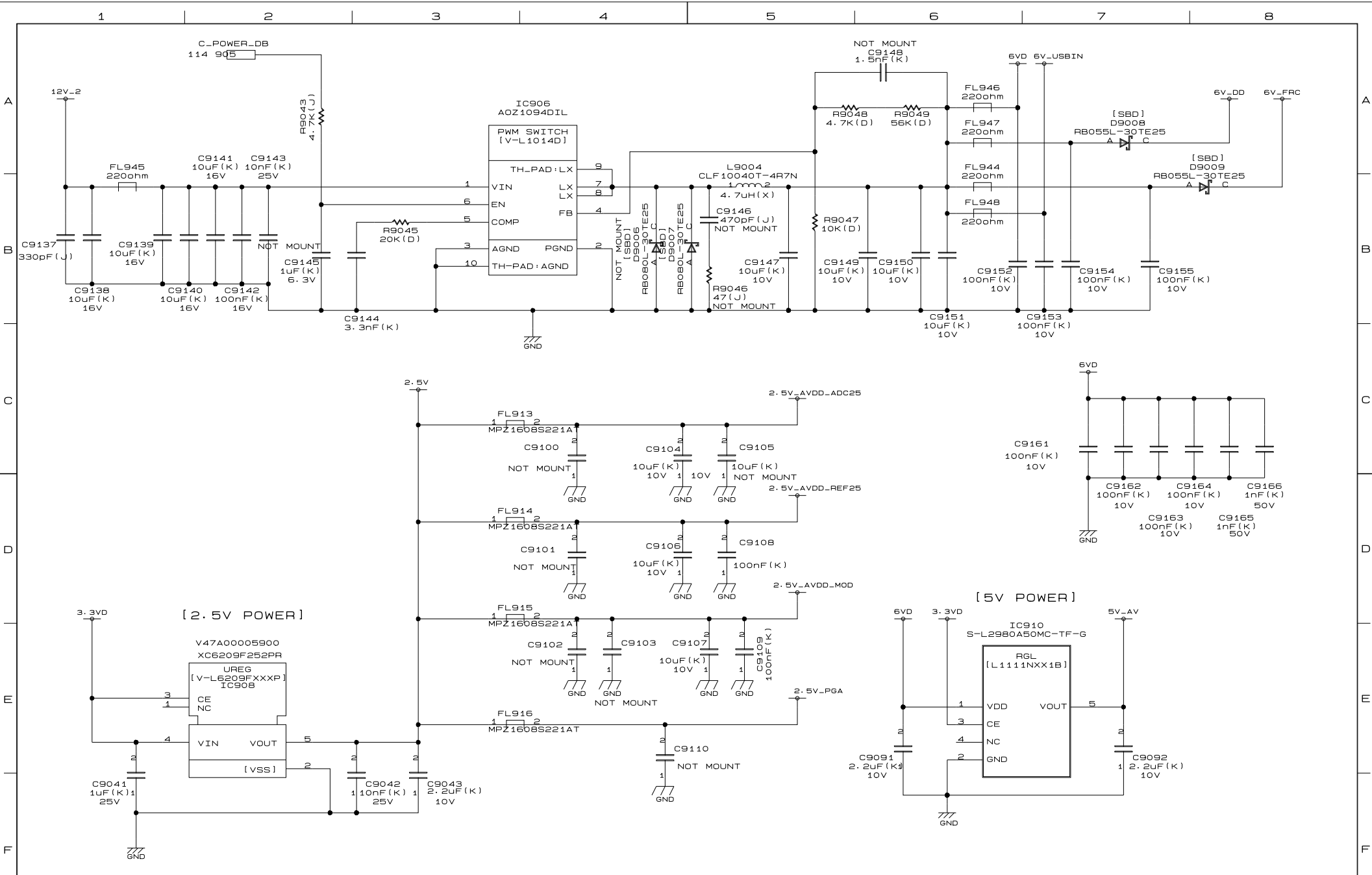
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POWER 3.3VD

SH. NO.
905

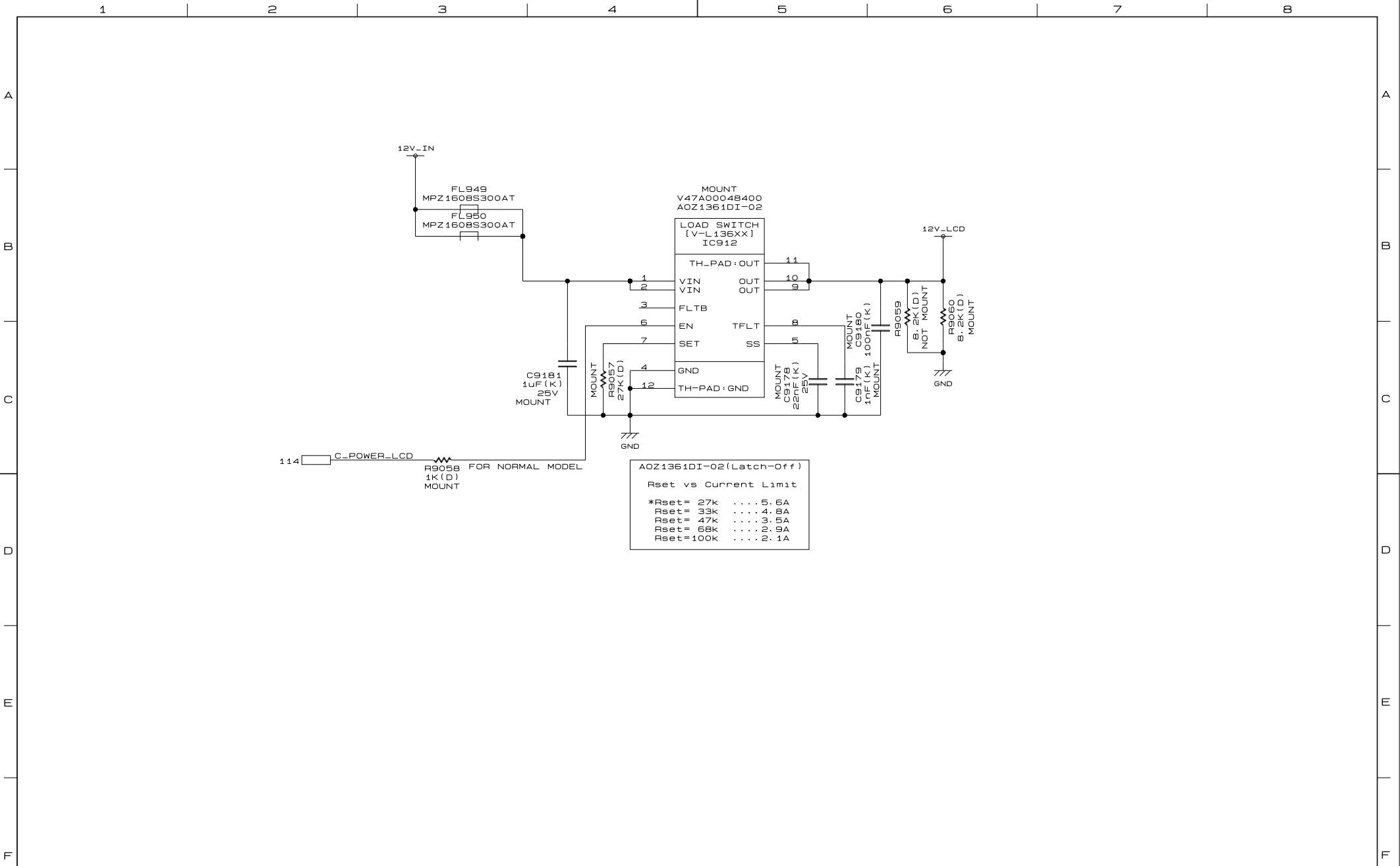
PAGE NO.
78

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY	2011/07/11	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
HASUMI		MAIN UNIT	POWER 6V	906	79	MP	PE1000ZA



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HASUMI

TITLE
MAIN UNIT

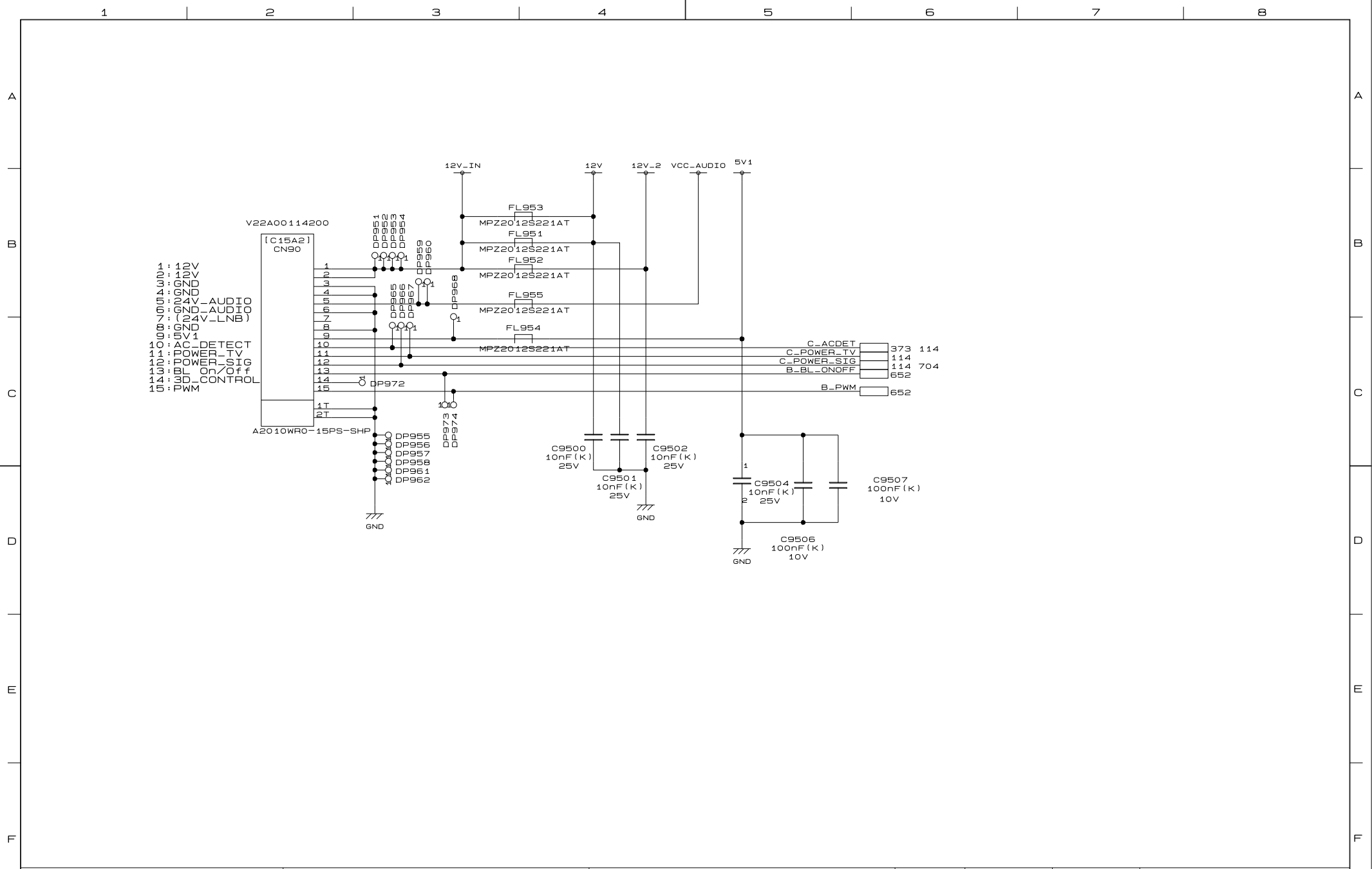
FUNCTION
PANEL I/F (POWER)

SH. NO.
914

PAGE NO.
80

REV. MARK
MP

DRAWING. NO.
PE1000ZA



DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION POWER CONNECTOR	SH. NO. 950	PAGE NO. 81	REV. MARK MP	DRAWING. NO. PE1000ZA
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1 2 3 4 5 6 7 8

A

A

B

B

C

C

D

D

E

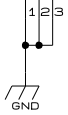
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F

F

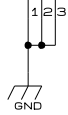
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VE1A00004500
OG-503040

{V-FC3}
E960



NOT MOUNT
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E961



DESIGNED BY

OSANAI

TITLE

MAIN UNIT

FUNCTION

EMC CONTROL

SH. NO.

960

PAGE NO.

82

REV. MARK

MP

DRAWING. NO.

PE1000ZA